# **DTOS Mach Kernel Interfaces**

**Secure Computing Corporation** 

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## Mach 3 Kernel Principles

#### Mach 3 Kernel Interfaces

#### Mach 3 Server Writer's Guide

#### Mach 3 Server Library Interfaces

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## CHAPTER 1 Introduction

This book documents the various interfaces to the DTOS variant of the Mach 3 kernel. The text generally describes each interface to the kernel in isolation. Entries that have a special security relevant variant are described together to avoid redundancy. The relationship of interfaces to one another, and the way that interfaces are combined to write user servers is the subject of companion volumes.

The organization of this book is such that it follows the organization of the kernel into its major functional areas. Although the kernel interface is itself not object oriented, the division of interfaces into areas is largely done according to the significant object utilized or manipulated by the interfaces. Each such object receives its own chapter. Of course, the assignment of interfaces into these chapters is a difficult and highly subjective process. An interface that requires rights for two ports of two different types could be grouped with the set of interfaces associated with either object type. Each interface, though, appears only once in this book.

Appendices give a description of the structures and fields used by these interfaces, a list of possible error return values from the kernel, an alphabetical index by object type and one by function and data structure name.

## **Interface Descriptions**

Each interface is listed separately, each starting on its own page. For each interface, some or all of the following features are presented:

- The name of the interface
- A brief description

- The pertinent library. All functions in this volume are contained in **libmach\_sa.a** (and, by implication, **libmach.a**) unless otherwise noted. Also listed is the header file that provides the function prototype or defines the data structure (if not **mach.h**).
- A synopsis of the interface, in C form
- Any macro or special forms of the call
- An extended description of the function performed by the call
- Identification of the request specific security permissions that must be held to make the request
- A description of each parameter to the call
- · Additional notes on the use of the interface
- · Cautions relating to the interface use
- · An explanation of the significant return values
- · References to related interfaces or data structures

## **Interface Types**

Most of the interfaces in this book are MIG generated interfaces. That is, they are stub routines generated from MIG interface description files. Calling these interfaces will actually result in a Mach IPC message being sent to the port that is the first argument in the call. This has three important effects.

I

- These calls may fail for various MIG or IPC related reasons. The list of error returns for these calls should always be considered to also include the IPC related errors (MACH\_MSG\_..., MACH\_SEND\_... and MACH\_RCV\_...) and the MIG related errors (MIG\_...).
- These calls may fail because required security permissions are not held by the requesting task. The list of error returns for these calls should always be considered to also include the security related error, KERN\_INSUFFICIENT\_PERMISSION.
- These calls only invoke their expected effect when the acting port is indeed a port of the specified type. That is, if a call expects a port that names a task (a kernel task port) and the port is instead a port managed by a task, the MIG stub routine will still happily generate the appropriate Mach message and send it to that task. What the target task will do with the message is up to it. Note that it is this effect that allows the Net Message server to transparently redirect messages.

A few of these interfaces are actually system calls (traps). In general, the system calls (with the obvious exception of the **mach\_msg** call) work only on the current task or thread. (Some functions are a hybrid; they first try the system call, and, failing that, they try sending a Mach message. This is an optimization for some interfaces for which the target is usually the invoking task or thread.) Any routine not documented as a system call is a MIG stub routine.

Most of these interfaces are of the type **Function**. This means that there is actually a C callable function (most likely in **libmach\_sa.a**) that has the calling sequence listed and that when called invokes some kernel or kernel related service. If the interface is a system trap instead of a message, it will be listed as a **System Trap**.

#### **Special Forms**

Some interfaces have the type **Server Interface**. Such a description applies to interfaces that are called in server tasks on behalf of messages sent from the kernel. That is, it is assumed that some task is listening (probably with mach\_msg\_server) on a port to which the kernel is to send messages. A received message will be passed to a MIG generated server routine (service\_server) which will call an appropriate server target function. It is these server target functions, one for each different message that the kernel generates, that are listed as **Server Interfaces**. For any given kernel message, there are any number of possible server interface calling sequences that can be generated, by permuting the order of the data provided in the message, omitting some data elements or including or omitting various header field elements (such as sequence numbers). In most cases, a single server interface calling sequence has been chosen with a given MIG generated server message de-multiplexing routine that calls these interfaces. In some cases, there are more than one MIG generated server routines which call upon different server interfaces associated with that MIG service routine. In any event, all Server Interfaces contain within their documentation the name of the MIG generated server routine that invokes the interface.

## **Special Forms**

There are various special interface forms defined in this volume.

- The **Macro** form specifies macros (typically defined in **mach.h**) that provide shorthand equivalents for some variations of the longer function call.
- The Sequence Number form of a Server Interface defines an additional MIG generated interface that supplies the sequence number from the message causing the server interface to be invoked. The existence of such a form implies the existence of an alternate MIG generated message de-multiplexing routine that invokes this special interface form.
- The **Asynchronous** form defines a MIG generated version of a **Function** that allows the function to be invoked asynchronously. Such a version requires an additional parameter to specify the reply port to which the reply is sent. The return value from the asynchronous function is the return status from the **mach\_msg** call sending the request, not the resulting status of the kernel operation. The asynchronous interface also requires a matching **Server Interface** that defines the reply message containing data that would have been output values from the normal function, as well as the resulting status from the kernel operation.

## **Parameter Types**

Each interface description supplies the C type of the various parameters. The parameter descriptions then indicate whether these parameters are input ("in"), output ("out") or both ("in/out"). This information appears in square brackets before the parameter description. Additional information also appears within these brackets for special or non-obvious parameter conventions.

The most common notation is "scalar", which means that the parameter somehow derives from an *int* type. Port types are scalar types but are marked specifically as to the type of port named by the parameter.

If the notation says "structure", the parameter is a direct structure type whose layout is described in APPENDIX E.

The notation "pointer to in array/structure/scalar" means that the caller supplies a pointer to the data. Arrays always have this property following from C language rules. If not so noted, input parameters are passed by value.

Output parameters are always passed by reference following C language rules. Hence the notation "out array/structure/scalar" actually means that the caller must supply a pointer to the storage to receive the output value. If a parameter is in/out, the notation "pointer to in/out array/structure/scalar" will appear. Since the parameter is also an output parameter, it must be passed by reference, hence it appears as a "pointer to in array/structure/scalar" when used as an input parameter.

In contrast, the notation "out pointer to dynamic array" means that the kernel will allocate space for returned data (as if by **vm\_allocate**) and will modify the pointer named by the output parameter (that is, the parameter to the function is a pointer to a pointer) to point to this allocated memory. The task should **vm\_deallocate** this space when done referencing it.

For a Server Interface, the corresponding version of the above is "in pointer to dynamic array". This indicates that the kernel has allocated space for the data (as if by **vm\_allocate**) and is supplying a pointer to the data as the input parameter to the server interface routine. It is the job of the server interface routine to arrange for this data to be **vm\_deallocate** when the data is no longer needed.

An "unbounded out in-line array" specifies the variable in-line/out-of-line (referred to as unbounded in-line) array feature of MIG described in the *Server Writer's Guide*. The caller supplies a pointer to a pointer whose value contains the address of an array whose size is specified in some other parameter (or known implicitly). Upon return, if this target pointer no longer points to the caller's array (most likely because the caller's array was not sufficiently large to hold the return data), then the kernel allocated space (as if by **vm\_allocate**) into which the data was placed; otherwise, the data was placed into the supplied array.

## **Error Return Values**

APPENDIX F documents the various error return values defined by the kernel. However, since the kernel interfaces are actually MIG generated stubs that send IPC messages, the set of errors that is possible for any given interface is quite extensive although few possibilities are seen in practice.

#### Security Controls

The various functions described in this volume (with the exception of the system traps) are MIG generated stub subroutines. As such, if the number of parameters or their sizes is incorrect, the stub may fail in a machine dependent way as would any other subroutine.

Assuming the correct number and size of the parameters, the MIG stub will simply marshall these values, making no consistency checks. The stub then attempts to send this message using **mach\_msg**. As such, the various IPC errors (MACH\_SEND\_...) are possible. In particular, if the destination port is completely bogus, the caller will receive MACH\_SEND\_INVALID\_DEST. Note that most errors involving invalid rights or outof-line memory addresses will be detected as IPC errors.

If the destination port is valid but names a port whose receive right is held by a task, the stub generated message will be sent to that task; what the task will do is up to it. Assuming that the destination port does name a kernel object, the message will go to the kernel. If the message is not one that object accepts, the caller will get KERN\_INVALID\_ARGUMENT. For operations that bind two objects (such as **task\_assign**), this error is returned if either object is of the wrong type. However, when an additional right is sent for the purposes of asserting privilege, or when the additional right itself is being manipulated, specific error return values are generated if the "privilege" port is of the wrong type.

Invalid non-port parameter values return the error KERN\_INVALID\_VALUE if their value is inherently ill-formed or out of range, but return specific error values if the value is not permitted at this point in time (such as a port name that is a valid name, but does not currently name a valid right).

Each kernel subsystem defines its own interesting set of errors which are listed for the relevant interfaces. Generic messaging and security errors are not listed for each interface, only those specific to that interface's functioning.

A return value of KERN\_SUCCESS (or any other equivalent value) indicates that the requested operation was performed and any return values returned.

## **Security Controls**

All of the MIG generated and the hybrid MIG/system call interfaces are subject the following general control rules.

- The requesting task must have *av\_send* permission to the first port in the parameter list.
- The requesting task must also have *av\_transfer\_send* and *av\_set\_reply* permission to the reply port provided in the MIG generated request message.
- All IPC permission checks are applied to MIG generated interfaces. ie if a port is given as an output parameter, the client must have *av\_hold\_send*, and *av\_can\_send* permissions.

Thus the respective security sections of each interface description, only describes the control issues specific to that interface.

#### Introduction

In the case of the "pure" system call interfaces only the interface specific control check is made. In this case the check is made against the implicit task or thread port as is appropriate for the interface.

## CHAPTER 2 IPC Interface

This chapter discusses the specifics of the kernel's inter-"process" communication (IPC) interfaces. The interfaces discussed are only the interfaces directly involved in sending and receiving IPC messages.

## mach\_msg/mach\_msg\_secure

**System Trap / Function** — Sends and receives a message using the same message buffer

## **SYNOPSIS**

mach_msg_return_t mach_msg	
(mach_msg_header_t*	msg,
mach_msg_option_t	option,
mach_msg_size_t	send_size,
mach_msg_size_t	rcv_size,
mach_port_t	rcv_name,
mach_msg_timeout_t	timeout,
mach_port_t	notify);
mach_msg_return_t mach_msg_secure	
(mach_msg_header_t*	msg,
mach_msg_option_t	option,
mach_msg_size_t	send_size,
mach_msg_size_t	rcv_size,
mach_port_t	rcv_name,
mach_msg_timeout_t	timeout,
mach_port_t	notify,
security_id_t*	rec_subj_sid,
security_id_t*	sender_subj_sid,
int	av_buf_size,
mach_access_vector_t*	av_buf);

## DESCRIPTION

The **mach\_msg** and **mach\_msg\_secure** system calls send and receive Mach messages. Mach messages contain typed data, which can include port rights and addresses of large regions of memory.

If the *option* argument contains MACH\_SEND\_MSG, it sends a message. The *send\_size* argument specifies the size of the message to send. The *msgh\_remote\_port* field of the message header specifies the destination of the message.

If the *option* argument contains MACH\_RCV\_MSG, it receives a message. The *rcv\_size* argument specifies the size of the message buffer that will receive the message; messages larger than *rcv\_size* are not received. The *rcv\_name* argument specifies the port or port set from which to receive.

If the *option* argument contains both MACH\_SEND\_MSG and MACH\_RCV\_MSG, then **mach\_msg** and **mach\_msg\_secure** do both send and receive operations. If the send operation encounters an error (any return code other than MACH\_MSG\_SUCCESS), then the call returns immediately without

attempting the receive operation. Semantically the combined call is equivalent to separate send and receive calls, but it saves a system call and enables other internal optimizations.

If the *option* argument specifies neither MACH\_SEND\_MSG nor MACH\_RCV\_MSG, then **mach\_msg** and **mach\_msg\_secure** do nothing.

Some options, like MACH\_SEND\_TIMEOUT and MACH\_RCV\_TIMEOUT, share a supporting argument. If these options are used together, they make independent use of the supporting argument's value.

## **SECURITY**

The DTOS kernel provides controls beyond those of the Mach capability mechanism described in the NOTES section below. The kernel security mechanisms enforce the permissions described in the **mach\_access\_vector\_t** structure defined in APPENDIX E. In addition to the appropriate rights, the following access permissions control message operations.

#### Sending Message

The sending task must have *av\_can\_send* permission to the destination port. If a reply port is used, the sending task must have *av\_set\_reply* permission to the reply port.

#### **Receiving Message**

The receiving task must have *av\_can\_receive* permission to the port indicated by *rcv\_name*. Messages will be received from a port in a port set only if the requesting task has *av\_can\_receive* permission to the port. When a task uses a port as a reply port for an RPC type of operation, the requesting task must also have *av\_can\_send* permission to that port.

#### Passing SEND, SEND\_ONCE or RECEIVE Right

Passing of rights is done by sending a message to a port P1 where the body of the message contains a port right to port P2. The task sending the message must have respectively, *av\_transfer\_send*, *av\_transfer\_send\_once* or *av\_transfer\_receive* permission to port P2, depending on whether the right is a send, send\_once or receive. In addition the sending task must have *av\_transfer\_right* to the destination port P1, in order to transfer any right in the body of the message.

Upon receipt of a right the receiving task must have respectively, *av\_hold\_send*, *av\_hold\_send\_once* or *av\_hold\_receive* permission to the port associated with the right in the message body.

#### Passing Out Of Line data

To pass out of line data in a message the sending task must have *av\_transfer\_ool* permission to the destination port. In addition, if the out-of-line data contains a port right, the permission requirements

described in the above section on "Passing SEND, SEND\_ONCE or RE-CEIVE Right" also apply.

The security aspects of **mach\_msg** and **mach\_msg\_secure** include the following additional control issues.

• On a receive, the receiving task must have *av\_interpose* permission to receive messages designated to subject security identifiers other than that of the receiving task.

The security aspects of **mach\_msg\_secure** include the following additional control issues.

- On a send the sending task must have *av\_specify* permission to the destination port in order to specify the message sender's subject security identifier to be associated with the message.
- On a send the sending task must have av\_specify permission to the destination port in order to specify any of the values in the *av\_buf*.

If the sending task does not specify or does not have *av\_specify* permission to the destination port the DTOS kernel provides the security identifier of the sending task. In all cases the DTOS Kernel associates the access vector describing the sending tasks permission to the destination port with the message.

In-line and out-of-line data are currently handled differently with respect to the security identifier assigned to the data. In-line data is assigned a security identifier or corresponding to the security identifier of the memory region where it is placed. Out-of-line data may retains the security identifier assigned to the memory region from which the data came if so requested.

## PARAMETERS

msg

[pointer to in/out structure containing random and reply ports] A message buffer. This should be aligned on a long-word boundary.

option

[in scalar] Message options are bit values, combined with bitwise-or. One or both of MACH\_SEND\_MSG and MACH\_RCV\_MSG should be used. Other options act as modifiers.

#### send\_size

[in scalar] When sending a message, specifies the size of the message buffer. Otherwise zero should be supplied.

rcv\_size

[in scalar] When receiving a message, specifies the size of the message buffer. Otherwise zero should be supplied.

#### rcv\_name

[in random port] When receiving a message, specifies the port or port set. Otherwise MACH\_PORT\_NULL should be supplied.

#### timeout

[in scalar] When using the MACH\_SEND\_TIMEOUT and MACH\_RCV\_TIMEOUT options, specifies the time in milliseconds to wait before giving up. Otherwise MACH\_MSG\_TIMEOUT\_NONE should be supplied.

#### notify

[in notify port] When using the MACH\_SEND\_NOTIFY, MACH\_SEND\_CANCEL, and MACH\_RCV\_NOTIFY options, specifies the port used for the notification. Otherwise MACH\_PORT\_NULL should be supplied.

#### rec\_subj\_sid

[pointer to in/out security id] When sending a message this parameter specifies the subject security identifier of the tasks that will be allowed to receive the message. Set to the address of a location that contains SEC\_NULL\_SID to indicate that there is no receiver restriction on the message.

When receiving a message this parameter contains the subject security identifier which the sender specified as the intended message receiver. Returns the address of a location containing SEC\_NULL\_SID if no intended recipient was supplied.

#### sender\_subj\_sid

[pointer to in/out security id] When sending a message this parameter specifies the subject security identifier to be provided as the message's *effective* sender. The sender must have *av\_specify* access to the port for the value to be used. Set to the address of a location containing SEC\_NULL\_SID to indicate that the sending task's subject security identifier is to be used. When receiving a message this parameter contains the *effective* subject security identifier of the message sender.

#### av\_buf\_size

The size of the subsequent structure av\_buf in bytes. If this size is set to zero, it is assumed that av\_buf is not specified.

#### av\_buf

[pointer to in/out access vector array structure] When receiving a message, this parameter points to a buffer that will contain the access vector describing the effective sender's permission to the port providing the message, the notify vector, the override vector, and the cache control vector. When sending a message, this parameter points to a buffer to the access vector, the notify vector, the override vector, and the cache control vector that the receiver will receive. The sender must have *av\_specify* access to the port for the value to be used. Set to MACH\_NO\_LABEL to indicate that the effective sender's permission is to be provided to the receiver.

### NOTES

The Mach kernel provides message-oriented, capability-based inter-process communication. The inter-process communication (IPC) primitives efficiently support many different styles of interaction, including remote procedure calls, object-oriented distributed programming, streaming of data, and sending very large amounts of data.

#### **Major Concepts**

The IPC primitives operate on three abstractions: messages, ports, and port sets. User tasks access all other kernel services and abstractions via the IPC primitives.

The message primitives let tasks send and receive messages. Tasks send messages to ports. Messages sent to a port are delivered reliably (messages may not be lost) and are received in the order in which they were sent. Messages contain a fixed-size header and a variable amount of typed data following the header. The header describes the destination and size of the message.

The IPC implementation makes use of the VM system to efficiently transfer large amounts of data. The message body can contain an address of a region of the sender's address space which should be transferred as part of the message. When a task receives a message containing an out-of-line region of data, the data appears in an unused portion of the receiver's address space. This transmission of out-of-line data is optimized so that sender and receiver share the physical pages of data copy-on-write, and no actual data copy occurs unless the pages are written. Regions of memory up to the size of a full address space may be sent in this manner.

Ports hold a queue of messages. Tasks operate on a port to send and receive messages by exercising capabilities (rights) for the port. Multiple tasks can hold send rights for a port. Tasks can also hold send-once rights, which grant the ability to send a single message. Only one task can hold the receive capability (receive right) for a port. Port rights can be transferred between tasks via messages. The sender of a message can specify in the message body that the message contains a port right. If a message contains a receive right for a port, then the receive right is removed from the sender of the message and the right is transferred to the receiver of the message. While the receive right is in transit, tasks holding send rights can still send messages to the port, and they are queued until a task acquires the receive right and uses it to receive the messages.

Tasks can receive messages from ports and port sets. The port set abstraction allows a single thread to wait for a message from any of several ports. Tasks manipulate port sets with a port set name, which is taken from the same name space as are the port rights. The port-set name may not be transferred in a message. A port set holds receive rights, and a receive operation on a port set blocks waiting for a message sent to any of the constituent ports. A port may not belong to more than one port set, and if a port is a member of a port set, the holder of the receive right can't receive directly from the port.

Port rights are a secure, location-independent way of naming ports. The port queue is a protected data structure, only accessible via the kernel's exported message primitives. Rights are also protected by the kernel; there is no way for a malicious user task to guess a port's internal name and send a message to a port to which it shouldn't have access. Port rights do not carry any location information. When a receive right for a port moves from task to task, and even between tasks on different machines, the send rights for the port remain unchanged and continue to function.

#### **Port Rights**

Each task has its own space of port rights. Port rights are named with positive integers. Except for the reserved values MACH\_PORT\_NULL (0) and MACH\_PORT\_DEAD (-1), this is a full 32-bit name space. When the kernel chooses a name for a new right, it is free to pick any unused name (one which denotes no right) in the space.

There are three basic kinds of rights: receive rights, send rights and send-once rights. A port name can name any of these types of rights, a port-set, be a dead name, or name nothing. Dead names are not capabilities. They act as place-holders to prevent a name from being otherwise used.

A port is destroyed, or dies, when its receive right is de-allocated. When a port dies, send and send-once rights for the port turn into dead names. Any messages queued at the port are destroyed, which de-allocates the port rights and out-of-line memory in the messages.

Tasks may hold multiple user-references for send rights and dead names. When a task receives a send right which it already holds, the kernel increments the right's user-reference count. When a task de-allocates a send right, the kernel decrements its user-reference count, and the task only loses the send right when the count goes to zero.

Send-once rights always have a user-reference count of one, although a port can have multiple send-once rights, because each send-once right held by a task has a different name. In contrast, when a task holds send rights or a receive right for a port, the rights share a single name.

Each send-once right generated guarantees the receipt of a single message, either a message sent to that send-once right or, if the send-once right is in any way destroyed, a send-once notification. A message body can carry port rights; the *msgt\_name* (*msgtl\_name*) field in a type descriptor specifies the type of port right and how the port right is to be extracted from the caller. The values MACH\_PORT\_NULL and MACH\_PORT\_DEAD are always valid in place of a port right in a message body.

In a sent message, the following *msgt\_name* values denote port rights:

#### MACH\_MSG\_TYPE\_MAKE\_SEND

The message will carry a send right, but the caller must supply a receive right. The send right is created from the receive right, and the receive right's make-send count is incremented.

#### MACH\_MSG\_TYPE\_COPY\_SEND

The message will carry a send right, and the caller should supply a send right. The user reference count for the supplied send right is not changed. The caller may also supply a dead name and the receiving task will get MACH\_PORT\_DEAD.

#### MACH\_MSG\_TYPE\_MOVE\_SEND

The message will carry a send right, and the caller should supply a send right. The user reference count for the supplied send right is decremented, and the right is destroyed if the count becomes zero. Unless a receive right remains, the name becomes available for recycling. The caller may also supply a dead name, which loses a user reference, and the receiving task will get MACH\_PORT\_DEAD.

#### MACH\_MSG\_TYPE\_MAKE\_SEND\_ONCE

The message will carry a send-once right, but the caller must supply a receive right. The send-once right is created from the receive right. Note that send once rights can only be created from the receive right.

#### MACH\_MSG\_TYPE\_MOVE\_SEND\_ONCE

The message will carry a send-once right, and the caller should supply a send-once right. The caller loses the supplied send-once right. The caller may also supply a dead name, which loses a user reference, and the receiving task will get MACH\_PORT\_DEAD.

#### MACH\_MSG\_TYPE\_MOVE\_RECEIVE

The message will carry a receive right, and the caller should supply a receive right. The caller loses the supplied receive right, but retains any send rights with the same name.

If a message carries a send or send-once right, and the port dies while the message is in transit, then the receiving task will get MACH\_PORT\_DEAD instead of a right.

The following *msgt\_name* values in a received message indicate that it carries port rights:

#### MACH\_MSG\_TYPE\_PORT\_SEND

This value is an alias for MACH\_MSG\_TYPE\_MOVE\_SEND. The message carried a send right. If the receiving task already has send and/ or receive rights for the port, then that name for the port will be reused. Otherwise, the new right will have a new, previously unused, name. If the task already has send rights, it gains a user reference for the right (unless this would cause the user-reference count to overflow). Otherwise, it acquires send rights, with a user-reference count of one.

#### MACH\_MSG\_TYPE\_PORT\_SEND\_ONCE

This value is an alias for MACH\_MSG\_TYPE\_MOVE\_SEND\_ONCE. The message carried a send-once right. The right will have a new, previously unused, name.

#### MACH\_MSG\_TYPE\_PORT\_RECEIVE

This value is an alias for MACH\_MSG\_TYPE\_MOVE\_RECEIVE. The message carried a receive right. If the receiving task already has send rights for the port, then that name for the port will be reused. Otherwise, the right will have a new, previously unused, name. The make-send count and sequence number of the receive right are reset to zero, but the port retains other attributes like queued messages, extant send and send-once rights, and requests for port-destroyed and no-senders notifications. (Note: It is currently planned to remove port-destroyed notifications from the kernel interface and to define no-senders notifications as being canceled when a receive right is moved.)

#### Memory

A message body can contain an address of a region of the sender's address space which should be transferred as part of the message. The message carries a logical copy of the memory, but the kernel uses VM techniques to defer any actual page copies. Unless the sender or the receiver modifies the data, the physical pages remain shared.

An out-of-line transfer occurs when the data's type descriptor specifies *msgt\_inline* as FALSE. The address of the memory region should follow the type descriptor in the message body. The type descriptor and the address contribute to the message's size (*send\_size*, *msgh\_size*). The out-of-line data does not contribute to the message's size.

The name, size, and number fields in the type descriptor describe the type and length of the out-of-line data, not the address. Out-of-line memory frequently requires long type descriptors (**mach\_msg\_type\_long\_t**), because the *msgt\_number* field is too small to describe a page of 4K bytes.

Out-of-line memory arrives somewhere in the receiver's address space as new memory. It has the same inheritance and protection attributes as newly **vm\_allocate**'ed memory. The receiver has the responsibility of de-allocating (with **vm\_deallocate**) the memory when it is no longer needed. Security-conscious receivers should exercise caution when dealing with out-of-line memory

from un-trustworthy sources, because the memory may be backed by an unreliable memory manager.

Null out-of-line memory is legal. If the out-of-line region size is zero (for example, because *msgtl\_number* is zero), then the region's specified address is ignored. A received null out-of-line memory region always has a zero address.

Unaligned addresses and region sizes that are not page multiples are legal. A received message can also contain regions with unaligned addresses and funny sizes. In the general case, the first and last pages in the new memory region in the receiver do not contain data from the sender, but are partly zero. The received address points into the middle of the first page. This possibility doesn't complicate de-allocation, because **vm\_deallocate** does the right thing, rounding the start address down and the end address up to de-allocate all arrived pages.

Out-of-line memory has a de-allocate option, controlled by the *msgt\_deallocate* bit. If it is TRUE and the out-of-line memory region is not null, then the region is implicitly de-allocated from the sender, as if by **vm\_deallocate**. In particular, the start and end addresses are rounded so that every page overlapped by the memory region is de-allocated. The use of *msgt\_deallocate* effectively changes the memory copy into a memory movement. In a received message, *msgt\_deallocate* is TRUE in type descriptors for out-of-line memory.

Out-of-line memory can carry port rights.

#### Message Send

The send operation queues a message to a port. The message carries a copy of the caller's data. After the send, the caller can freely modify the message buffer or the out-of-line memory regions and the message contents will remain unchanged.

Message delivery is reliable and sequenced. Messages are not lost, and messages sent to a port from a single thread are received in the order in which they were sent.

If the destination port's queue is full, then several things can happen. If the message is sent to a send-once right (*msgh\_remote\_port* carries a send-once right), then the kernel ignores the queue limit and delivers the message. Otherwise the caller blocks until there is room in the queue, unless the MACH\_SEND\_TIMEOUT or MACH\_SEND\_NOTIFY options are used. If a port has several blocked senders, then any of them may queue the next message when space in the queue becomes available, with the proviso that a blocked sender will not be indefinitely starved.

These options modify MACH\_SEND\_MSG. If MACH\_SEND\_MSG is not also specified, they are ignored.

#### MACH\_SEND\_TIMEOUT

The *timeout* argument should specify a maximum time (in milliseconds) for the call to block before giving up. If the message can't be queued before the timeout interval elapses, then the call returns MACH\_SEND\_TIMED\_OUT. A zero timeout is legitimate.

#### MACH\_SEND\_NOTIFY

The *notify* argument should specify a receive right for a notify port. If the send were to block, then instead the message is queued, MACH\_SEND\_WILL\_NOTIFY is returned, and a msg-accepted notification is requested. If MACH\_SEND\_TIMEOUT is also specified, then MACH\_SEND\_NOTIFY doesn't take effect until the timeout interval elapses.

Only one message at a time can be forcibly queued to a send right with MACH\_SEND\_NOTIFY. A msg-accepted notification is sent to the notify port when another message can be forcibly queued. If an attempt is made to use MACH\_SEND\_NOTIFY before then, the call returns a MACH\_SEND\_NOTIFY\_IN\_PROGRESS error.

The msg-accepted notification carries the name of the send right. If the send right is de-allocated before the msg-accepted notification is generated, then the msg-accepted notification carries the value MACH\_PORT\_NULL. If the destination port is destroyed before the notification is generated, then a send-once notification is generated instead.

(Note: It is currently planned that this option will be deleted, as well as the provision of the corresponding notification.)

#### MACH\_SEND\_INTERRUPT

If specified, the **mach\_msg** call will return MACH\_SEND\_INTERRUPTED if a software interrupt aborts the call. Otherwise, the send operation will be retried.

#### MACH\_SEND\_CANCEL

The *notify* argument should specify a receive right for a notify port. If the send operation removes the destination port right from the caller, and the removed right had a dead-name request registered for it, and *notify* is the notify port for the dead-name request, then the dead-name request may be silently canceled (instead of resulting in what would have been a port-deleted notification).

This option is typically used to cancel a dead-name request made with the MACH\_RCV\_NOTIFY option. It should only be used as an optimization.

Some return codes, like MACH\_SEND\_TIMED\_OUT, imply that the message was almost sent, but could not be queued. In these situations, the kernel tries to

return the message contents to the caller with a pseudo-receive operation. This prevents the loss of port rights or memory which only exist in the message. For example, a receive right which was moved into the message, or out-of-line memory sent with the de-allocate bit.

The pseudo-receive operation is very similar to a normal receive operation. The pseudo-receive handles the port rights in the message header as if they were in the message body. They are not reversed (as is the appearance in a normal received message). After the pseudo-receive, the message is ready to be resent. If the message is not resent, note that out-of-line memory regions may have moved and some port rights may have changed names.

The pseudo-receive operation may encounter resource shortages. This is similar to a MACH\_RCV\_BODY\_ERROR return code from a receive operation. When this happens, the normal send return codes are augmented with the MACH\_MSG\_IPC\_SPACE, MACH\_MSG\_IPC\_SPACE, MACH\_MSG\_IPC\_KERNEL, and MACH\_MSG\_VM\_KERNEL bits to indicate the nature of the resource shortage.

The queueing of a message carrying receive rights may create a circular loop of receive rights and messages, which can never be received. For example, a message carrying a receive right can be sent to that receive right. This situation is not an error, but the kernel will garbage-collect such loops, destroying the messages.

#### **Message Receive**

The receive operation de-queues a message from a port. The receiving task acquires the port rights and out-of-line memory regions carried in the message.

The *rcv\_name* argument specifies a port or port set from which to receive. If a port is specified, the caller must possess the receive right for the port and the port must not be a member of a port set. If no message is present, then the call blocks, subject to the MACH\_RCV\_TIMEOUT option.

If a port set is specified, the call will receive a message sent to any of the member ports. It is permissible for the port set to have no member ports, and ports may be added and removed while a receive from the port set is in progress. The received message can come from any of the member ports which have messages, with the proviso that a member port with messages will not be indefinitely starved. The *msgh\_local\_port* field in the received message header specifies from which port in the port set the message came.

The *rcv\_size* argument specifies the size of the caller's message buffer. The **mach\_msg** call will not receive a message larger than *rcv\_size*. Messages that are too large are destroyed, unless the MACH\_RCV\_LARGE option is used.

The destination and reply ports are reversed in a received message header. The *msgh\_local\_port* field carries the name of the destination port, from which the message was received, and the *msgh\_remote\_port* field carries the reply port

right. The bits in msgh\_bits are also reversed. The MACH MSGH BITS LOCAL bits have the value MACH\_MSG\_TYPE\_PORT\_SEND if the message was sent to a send right, and the value MACH\_MSG\_TYPE\_PORT\_SEND\_ONCE if was sent to a sendonce right. The MACH\_MSGH\_BITS\_REMOTE bits describe the reply port right.

Received messages are stamped with a sequence number, taken from the port from which the message was received. (Messages received from a port set are stamped with a sequence number from the appropriate member port.) Newly created ports start with a zero sequence number, and the sequence number is reset to zero whenever the port's receive right moves between tasks. When a message is de-queued from the port, it is stamped with the port's sequence number and the port's sequence number is then incremented. The de-queue and increment operations are atomic, so that multiple threads receiving messages from a port can use the *msgh\_seqno* field to reconstruct the original order of the messages.

A received message can contain port rights and out-of-line memory. The *msgh\_local\_port* field does not carry a port right; the act of receiving the message destroys the send or send-once right for the destination port. The *msgh\_remote\_port* field does carry a port right, and the message body can carry port rights and memory if MACH\_MSGH\_BITS\_COMPLEX is present in *msgh\_bits*. Received port rights and memory should be consumed or de-allocated in some fashion.

In almost all cases, *msgh\_local\_port* will specify the name of a receive right, either *rcv\_name*, or, if *rcv\_name* is a port set, a member of *rcv\_name*. If other threads are concurrently manipulating the receive right, the situation is more complicated. If the receive right is renamed during the call, then *msgh\_local\_port* specifies the right's new name. If the caller loses the receive right after the message was de-queued from it, then **mach\_msg** will proceed instead of returning MACH\_RCV\_PORT\_DIED. If the receive right was destroyed, then *msgh\_local\_port* specifies MACH\_PORT\_DEAD. If the receive right still exists, but isn't held by the caller, then *msgh\_local\_port* specifies MACH\_PORT\_NULL.

These options modify MACH\_RCV\_MSG. If MACH\_RCV\_MSG is not also specified, they are ignored.

#### MACH\_RCV\_TIMEOUT

The *timeout* argument should specify a maximum time (in milliseconds) for the call to block before giving up. If no message arrives before the timeout interval elapses, then the call returns MACH\_RCV\_TIMED\_OUT. A zero timeout is legitimate.

#### MACH\_RCV\_NOTIFY

The *notify* argument should specify a receive right for a notify port. If receiving the reply port creates a new port right in the caller, then the

notify port is used to request a dead-name notification for the new port right.

#### MACH\_RCV\_INTERRUPT

If specified, the **mach\_msg** call will return MACH\_RCV\_INTERRUPTED if a software interrupt aborts the call. Otherwise, the receive operation will be retried.

#### MACH\_RCV\_LARGE

If the message is larger than *rcv\_size*, then the message remains queued instead of being destroyed. The call returns MACH\_RCV\_TOO\_LARGE and the actual size of the message is returned in the *msgh\_size* field of the message header. If this option is not specified, messages too large will be de-queued and then destroyed; the caller receives the message's header, with all fields correct, including the destination port but excepting the reply port, which is MACH\_PORT\_NULL.

If a resource shortage prevents the reception of a port right, the port right is destroyed and the caller sees the name MACH\_PORT\_NULL. If a resource shortage prevents the reception of an out-of-line memory region, the region is destroyed and the caller sees a zero address. In addition, the *msgt\_size* (*msgtl\_size*) field in the region's type descriptor is changed to zero. If a resource shortage prevents the reception of out-of-line memory carrying port rights, then the port rights are always destroyed if the memory region can not be received. A task never receives port rights or memory for which it is not told.

The MACH\_RCV\_HEADER\_ERROR return code indicates a resource shortage in the reception of the message's header. The reply port and all port rights and memory in the message body are destroyed. The caller receives the message's header, with all fields correct except for the reply port.

The MACH\_RCV\_BODY\_ERROR return code indicates a resource shortage in the reception of the message's body. The message header, including the reply port, is correct. The kernel attempts to transfer all port rights and memory regions in the body, and only destroys those that can't be transferred.

#### Atomicity

The **mach\_msg** call handles port rights in a message header atomically. Port rights and out-of-line memory in a message body do not enjoy this atomicity guarantee. The message body may be processed front-to-back, back-to-front, first out-of-line memory then port rights, in some random order, or even atomically.

For example, consider sending a message with the destination port specified as MACH\_MSG\_TYPE\_MOVE\_SEND and the reply port specified as MACH\_MSG\_TYPE\_COPY\_SEND. The same send right, with one user-reference, is supplied for both the *msgh\_remote\_port* and *msgh\_local\_port* fields. Because **mach\_msg** processes the message header atomically, this succeeds. If

*msgh\_remote\_port* were processed before *msgh\_local\_port*, then **mach\_msg** would return MACH\_SEND\_INVALID\_REPLY in this situation.

On the other hand, suppose the destination and reply port are both specified as MACH\_MSG\_TYPE\_MOVE\_SEND, and again the same send right with one user-reference is supplied for both. Now the send operation fails, but because it processes the header atomically, **mach\_msg** can return either MACH\_SEND\_INVALID\_DEST or MACH\_SEND\_INVALID\_REPLY.

For example, consider receiving a message at the same time another thread is de-allocating the destination receive right. Suppose the reply port field carries a send right for the destination port. If the de-allocation happens before the de-queuing, then the receiver gets MACH\_RCV\_PORT\_DIED. If the de-allocation happens after the receive, then the *msgh\_local\_port* and the *msgh\_remote\_port* fields both specify the same right, which becomes a dead name when the receive right is de-allocated. If the de-allocation happens between the de-queue and the receive, then the *msgh\_local\_port* and *msgh\_remote\_port* fields both specify MACH\_PORT\_DEAD. Because the header is processed atomically, it is not possible for just one of the two fields to hold MACH\_PORT\_DEAD.

The MACH\_RCV\_NOTIFY option provides a more likely example. Suppose a message carrying a send-once right reply port is received with MACH\_RCV\_NOTIFY at the same time the reply port is destroyed. If the reply port is destroyed first, then *msgh\_remote\_port* specifies MACH\_PORT\_DEAD and the kernel does not generate a dead-name notification. If the reply port is destroyed after it is received, then *msgh\_remote\_port* specifies a dead name for which the kernel generates a dead-name notification. It is not possible to receive the reply port right and have it turn into a dead name before the dead-name notification is requested; as part of the message header the reply port is received atomically.

#### Implementation

**mach\_msg** and **mach\_msg\_secure** are wrappers for system calls. These routines have the responsibility for repeating the interrupted system call.

#### CAUTIONS

Sending out-of-line memory with a non-page-aligned address, or a size which is not a page multiple, works but with a caveat. The extra bytes in the first and last page of the received memory are not zeroed, so the receiver can peek at more data than the sender intended to transfer. This might be a security problem for the sender.

If MACH\_RCV\_TIMEOUT is used without MACH\_RCV\_INTERRUPT, then the timeout duration might not be accurate. When the call is interrupted and automatically retried, the original timeout is used. If interrupts occur frequently enough, the timeout interval might never expire. MACH\_SEND\_TIMEOUT without MACH\_SEND\_INTERRUPT suffers from the same problem.

## **RETURN VALUE**

The send operation can generate the following return codes. These return codes imply that the call did nothing:

MACH\_SEND\_MSG\_TOO\_SMALL

The specified *send\_size* was smaller than the minimum size for a message.

MACH\_SEND\_NO\_BUFFER A resource shortage prevented the kernel from allocating a message buffer.

MACH\_SEND\_INVALID\_DATA The supplied message buffer was not readable.

MACH\_SEND\_INVALID\_HEADER The *msgh\_bits* value was invalid.

MACH\_SEND\_INVALID\_DEST The *msgh\_remote\_port* value was invalid.

- MACH\_SEND\_INVALID\_REPLY The *msgh\_local\_port* value was invalid.
- MACH\_SEND\_INVALID\_NOTIFY When using MACH\_SEND\_CANCEL, the *notify* argument did not denote a valid receive right.

These return codes imply that some or all of the message was destroyed:

- MACH\_SEND\_INVALID\_MEMORY The message body specified out-of-line data that was not readable.
- MACH\_SEND\_INVALID\_RIGHT The message body specified a port right which the caller didn't possess.

MACH\_SEND\_INVALID\_TYPE A type descriptor was invalid.

MACH\_SEND\_MSG\_TOO\_SMALL The last data item in the message ran over the end of the message.

These return codes imply that the message was returned to the caller with a pseudo-receive operation:

MACH\_SEND\_TIMED\_OUT The *timeout* interval expired. MACH\_SEND\_INTERRUPTED A software interrupt occurred.

#### MACH\_SEND\_INVALID\_NOTIFY

When using MACH\_SEND\_NOTIFY, the *notify* argument did not denote a valid receive right.

MACH\_SEND\_NO\_NOTIFY A resource shortage prevented the kernel from setting up a msg-accepted notification.

#### MACH\_SEND\_NOTIFY\_IN\_PROGRESS

A msg-accepted notification was already requested, and hasn't yet been generated.

These return codes imply that the message was queued:

- MACH\_SEND\_WILL\_NOTIFY The message was forcibly queued, and a msg-accepted notification was requested.
- MACH\_MSG\_SUCCESS The message was queued.

The receive operation can generate the following return codes. These return codes imply that the call did not de-queue a message:

- MACH\_RCV\_INVALID\_NAME The specified *rcv\_name* was invalid.
- MACH\_RCV\_IN\_SET The specified port was a member of a port set.
- MACH\_RCV\_TIMED\_OUT The *timeout* interval expired.
- MACH\_RCV\_INTERRUPTED A software interrupt occurred.
- MACH\_RCV\_PORT\_DIED The caller lost the rights specified by *rcv\_name*.

#### MACH\_RCV\_PORT\_CHANGED

*rcv\_name* specified a receive right which was moved into a port set during the call.

#### MACH\_RCV\_TOO\_LARGE

When using MACH\_RCV\_LARGE, and the message was larger than *rcv\_size*. The message is left queued, and its actual size is returned in the *msgh\_size* field of the message buffer.

These return codes imply that a message was de-queued and destroyed:

#### MACH\_RCV\_HEADER\_ERROR

A resource shortage prevented the reception of the port rights in the message header.

#### MACH\_RCV\_INVALID\_NOTIFY

When using MACH\_RCV\_NOTIFY, the *notify* argument did not denote a valid receive right.

#### MACH\_RCV\_TOO\_LARGE

When not using MACH\_RCV\_LARGE, a message larger than *rcv\_size* was de-queued and destroyed.

These return codes imply that a message was received:

#### MACH\_RCV\_BODY\_ERROR

A resource shortage prevented the reception of a port right or out-ofline memory region in the message body.

#### MACH\_RCV\_INVALID\_DATA

The specified message buffer was not writable. The calling task did successfully receive the port rights and out-of-line memory regions in the message.

#### MACH\_MSG\_SUCCESS

A message was received.

Resource shortages can occur after a message is de-queued, while transferring port rights and out-of-line memory regions to the receiving task. In this situation, **mach\_msg** and **mach\_msg\_secure** return MACH\_RCV\_HEADER\_ERROR or MACH\_RCV\_BODY\_ERROR. These return codes always carry extra bits (bitwise-or'ed) that indicate the nature of the resource shortage:

#### MACH\_MSG\_IPC\_SPACE

There was no room in the task's IPC name space for another port name.

#### MACH\_MSG\_VM\_SPACE

There was no room in the task's VM address space for an out-of-line memory region.

#### MACH\_MSG\_IPC\_KERNEL

A kernel resource shortage prevented the reception of a port right.

#### MACH\_MSG\_VM\_KERNEL

A kernel resource shortage prevented the reception of an out-of-line memory region.

## MACH\_MSG\_INSUFFICIENT\_PERMISSION

A permission check failure prevented the reception of a port right.

## **RELATED INFORMATION**

Functions: mach\_msg\_receive, mach\_msg\_send.

Data Structures: mach\_msg\_header, mach\_msg\_type, mach\_msg\_type\_long, mach\_msg\_accepted\_notification, mach\_send\_once\_notification.

## mach\_msg\_receive

Function — Receives a message from a port or port set

#### LIBRARY

Not declared anywhere.

## **SYNOPSIS**

mach\_msg\_return\_t mach\_msg\_receive (mach\_msg\_header\_t\*

header);

## DESCRIPTION

The mach\_msg\_receive function is a shorthand for the following call:

mach\_msg (header, MACH\_RCV\_MSG, 0, header→msgh\_size, header→msgh\_local\_port, MACH\_MSG\_TIMEOUT\_NONE, MACH PORT NULL);

## SECURITY

The receiving task must have *av\_can\_receive* permission to the port indicated by *rcv\_name*. Messages will be received from a port in a port set only if the requesting task has *av\_can\_receive* permission to the port. When a task uses a port as a reply port for an RPC type of operation, the requesting task must also have *av\_can\_send* permission to that port.

## PARAMETERS

header

[pointer to in/out structure containing random port] The address of the buffer that is to receive the message. The *msgh\_local\_port* and *msgh\_size* fields in *header* must be set.

#### **RETURN VALUE**

Refer to mach\_msg for a description of the various receive errors.

## **RELATED INFORMATION**

Functions: mach\_msg, mach\_msg\_send.

Data Structures: mach\_msg\_header, mach\_msg\_type, mach\_msg\_type\_long.

# mach\_msg\_send

Function — Sends a message to a port

#### LIBRARY

Not declared anywhere.

#### **SYNOPSIS**

mach\_msg\_return\_t **mach\_msg\_send** (mach\_msg\_header\_t\*

header);

#### DESCRIPTION

The mach\_msg\_send function is a shorthand for the following call:

mach\_msg (header, MACH\_SEND\_MSG, header→msgh\_size, 0, MACH\_PORT\_NULL, MACH\_MSG\_TIMEOUT\_NONE, MACH\_PORT\_NULL);

# SECURITY

The sending task must have *av\_can\_send* permission to the destination port. If a reply port is used, the sending task must have *av\_set\_reply* permission to the reply port.

## PARAMETERS

header

[pointer to in structure containing random and reply ports] The address of the buffer that contains the message to be sent.

#### **RETURN VALUE**

Refer to mach\_msg for a description of the send errors.

#### **RELATED INFORMATION**

Functions: mach\_msg, mach\_msg\_receive.

Data Structures: mach\_msg\_header, mach\_msg\_type, mach\_msg\_type\_long.

IPC Interface

# CHAPTER 3 Port Manipulation Interface

This chapter discusses the specifics of the kernel's port manipulation interfaces. This includes port, port set and port right related functions. Also included are interfaces that return port related status information that applies to a single task.

# do\_mach\_notify\_dead\_name

Server Interface — Handles the occurrence of a dead-name notification

#### LIBRARY

Not declared anywhere.

## **SYNOPSIS**

kern_return_t <b>do_mach_notify_dead_name</b>	
(notify_port_t	notify,
mach_port_name_t	name);
do_seqnos_mach_notify_dead_name Sequence Number form	
kern_return_t do_seqnos_mach_notify_dead_name	
(notify_port_t	notify,
mach port segno t	segno,

#### DESCRIPTION

A **do\_mach\_notify\_dead\_name** function is called by **notify\_server** as the result of a kernel message indicating that the port name is now dead as the result of the associated receive right having died. In contrast, a port-deleted notification indicates that the port name is no longer usable (that is, it no longer names a valid right), typically as a result of the right so named being consumed or moved. *notify* is the port named via **mach\_port\_request\_notification** or **mach\_msg**.

name);

#### SECURITY

There are no security limitations on this kernel outcall.

mach\_port\_name\_t

## PARAMETERS

```
notify
```

[in notify port] The port to which the notification was sent.

```
seqno
```

[in scalar] The sequence number of this message relative to the notification port.

name

[in scalar] The dead name.

# NOTES

This interface is machine word length specific because of the port name parameter.

## **RETURN VALUE**

Irrelevant.

## **RELATED INFORMATION**

Functions: notify\_server, mach\_msg, do\_mach\_notify\_msg\_accepted, do\_mach\_notify\_port\_deleted, do\_mach\_notify\_send\_once. mach\_port\_request\_notification, do\_mach\_notify\_no\_senders, do\_mach\_notify\_port\_destroyed,

# do\_mach\_notify\_msg\_accepted

Server Interface — Handles the occurrence of a message accepted notification

#### LIBRARY

Not declared anywhere.

#### **SYNOPSIS**

do\_

kern_return_t do_mach_notify_msg_accepted	
(notify_port_t	notify,
mach_port_name_t	name);
_seqnos_mach_notify_msg_accepted Sequence Number form	
kern_return_t do_seqnos_mach_notify_msg_accepted	

	 0-	
(notify_port_t		notify,
mach_port_seqno_t		seqno,
mach_port_name_t		name);

#### DESCRIPTION

A **do\_mach\_notify\_msg\_accepted** function is called by **notify\_server** as the result of a kernel message indicating that a message forcibly queued to a port via MACH\_NOTIFY\_SEND was accepted. *notify* is the port named via **mach\_msg**.

(Note: This feature is current planned for deletion.)

## **SECURITY**

There are no security limitations on this kernel outcall.

#### PARAMETERS

notify

[in notify port] The port to which the notification was sent.

seqno

[in scalar] The sequence number of this message relative to the notification port.

#### name

[in scalar] The port whose message was accepted.

## NOTES

This interface is machine word length specific because of the port name parameter.

## **RETURN VALUE**

Irrelevant.

## **RELATED INFORMATION**

Functions: notify\_server, mach\_msg, do\_mach\_notify\_dead\_name, do\_mach\_notify\_port\_deleted, do\_mach\_notify\_send\_once. mach\_port\_request\_notification, do\_mach\_notify\_no\_senders, do\_mach\_notify\_port\_destroyed,

# do\_mach\_notify\_no\_senders

Server Interface — Handles the occurrence of a no-more-senders notification

#### LIBRARY

Not declared anywhere.

## **SYNOPSIS**

kern_return_t <b>do_mach_notify_no_senders</b>	
(notify_port_t	notify,
mach_port_mscount_t	mscount);
do_seqnos_mach_notify_no_senders Sequence Number form	

kern_return_t do_seqnos_mach_notify_no_senders	
(notify_port_t	
mach_port_seqno_t	
mach_port_mscount_t	

notify, seqno,

mscount);

## DESCRIPTION

A **do\_mach\_notify\_no\_senders** function is called by **notify\_server** as the result of a kernel message indicating that a receive right has no more senders. *noti-fy* is the port named via **mach\_port\_request\_notification**.

#### **SECURITY**

There are no security limitations on this kernel outcall.

## PARAMETERS

notify

[in notify port] The port to which the notification was sent.

seqno

[in scalar] The sequence number of this message relative to the notification port.

mscount

[in scalar] The value the port's make-send count had when the notification was generated.

## **RETURN VALUE**

Irrelevant.

# **RELATED INFORMATION**

Functions: notify\_server, mach\_msg, do\_mach\_notify\_msg\_accepted, do\_mach\_notify\_port\_deleted, do\_mach\_notify\_send\_once. mach\_port\_request\_notification, do\_mach\_notify\_dead\_name, do\_mach\_notify\_port\_destroyed,

# do\_mach\_notify\_port\_deleted

Server Interface — Handles the occurrence of a port-deleted notification

#### LIBRARY

Not declared anywhere.

## **SYNOPSIS**

kern_return_t do_mach_notify_port_deleted	
(notify_port_t	notify,
mach_port_name_t	name);
do_seqnos_mach_notify_port_deleted Sequence Number form	
kern_return_t do_seqnos_mach_notify_port_deleted	
(notify_port_t	notify,
mach_port_seqno_t	seqno,

#### DESCRIPTION

A **do\_mach\_notify\_port\_deleted** function is called by **notify\_server** as the result of a kernel message indicating that a port name is no longer usable (that is, it no longer names a valid right), typically as a result of the right so named being consumed or moved. In contrast, a dead-name notification indicates that the port name is now dead as the result of the associated receive right having died. *notify* is the port named via **mach\_port\_request\_notification** or **mach\_msg**.

name);

## SECURITY

There are no security limitations on this kernel outcall.

mach\_port\_name\_t

#### PARAMETERS

```
notify
```

[in notify port] The port to which the notification was sent.

seqno

[in scalar] The sequence number of this message relative to the notification port.

#### пате

[in scalar] The invalid name.

## NOTES

This interface is machine word length specific because of the port name parameter.

## **RETURN VALUE**

Irrelevant.

## **RELATED INFORMATION**

Functions: notify\_server, mach\_msg, do\_mach\_notify\_dead\_name, do\_mach\_notify\_no\_senders, do\_mach\_notify\_send\_once.

mach\_port\_request\_notification, do\_mach\_notify\_msg\_accepted, do\_mach\_notify\_port\_destroyed,

# do\_mach\_notify\_port\_destroyed

Server Interface — Handles the occurrence of a port destroyed notification

#### LIBRARY

Not declared anywhere.

#### **SYNOPSIS**

kern_return_t do_mach_notify_port_destroyed			
(notify_port_t	notify,		
mach_port_receive_t	rights);		
do_seqnos_mach_notify_port_destroyed Sequence Number form			

notify, seqno, rights);

kern_return_t do_seqnos_mach_notify_port_destroyed	
(notify_port_t	
mach_port_seqno_t	
mach_port_receive_t	

#### DESCRIPTION

A **do\_mach\_notify\_port\_destroyed** function is called by **notify\_server** as the result of a kernel message indicating that a receive right would have been destroyed. *notify* is the port named via **mach\_port\_request\_notification**.

(Note: This feature is currently planned for deletion.)

#### **SECURITY**

There are no security limitations on this kernel outcall.

#### PARAMETERS

```
notify
```

[in notify port] The port to which the notification was sent.

seqno

[in scalar] The sequence number of this message relative to the notification port.

```
rights
```

[in random port] The receive right that would have been destroyed.

## **RETURN VALUE**

Irrelevant.

# **RELATED INFORMATION**

Functions: notify\_server, mach\_msg, do\_mach\_notify\_msg\_accepted, do\_mach\_notify\_dead\_name, do\_mach\_notify\_send\_once. mach\_port\_request\_notification, do\_mach\_notify\_no\_senders, do\_mach\_notify\_port\_deleted,

# do\_mach\_notify\_send\_once

Server Interface — Handles the occurrence of a send-once notification

#### LIBRARY

Not declared anywhere.

#### **SYNOPSIS**

kern_return_t <b>do_mach_notify_send_once</b> (notify_port_t	notify);
do_seqnos_mach_notify_send_once Sequence Number form	
kern_return_t do_seqnos_mach_notify_send_once	
(notify_port_t	notify,
mach_port_seqno_t	seqno);

#### DESCRIPTION

A **do\_mach\_notify\_send\_once** function is called by **notify\_server** as the result of a kernel message indicating that a send-once right was in any way destroyed. *notify* is the port for which a send-once right was destroyed.

#### **SECURITY**

There are no security limitations on this kernel outcall.

## PARAMETERS

notify

[in notify port] The port to which the notification was sent.

seqno

[in scalar] The sequence number of this message relative to the notification port.

#### **RETURN VALUE**

Irrelevant.

## **RELATED INFORMATION**

Functions:notify\_server,mach\_msg,machdo\_mach\_notify\_msg\_accepted,dodo\_mach\_notify\_port\_deleted,do\_machdo\_mach\_notify\_dead\_name.

mach\_port\_request\_notification, do\_mach\_notify\_no\_senders, do\_mach\_notify\_port\_destroyed,

# mach\_port\_allocate/mach\_port\_allocate\_secure

**Function** — Creates a port right and optionally associates an object security identifier with the port.

## SYNOPSIS

kern\_return\_t mach\_port\_allocate

task,
right,
name);

kern\_return\_t mach\_port\_allocate\_secure

(mach_port_t	task,
mach_port_right_t	right,
mach_port_t*	name,
security_id_t	obj_sid);

#### DESCRIPTION

The **mach\_port\_allocate** function creates a new right in the specified task. The new right's name is returned in *name*. The **mach\_port\_allocate\_secure** function creates a new right in the specified task with the specified object security identifier.

## SECURITY

The requesting task must hold *tsv\_add\_name* permission to the task port *task*. If the request results in a new receive right being created for *task*'s task, task must have *av\_hold\_receive* permission to the newly allocated port.

When using **mach\_port\_allocate**, the port is allocated with an object security identifier derived from *task*'s subject security identifier. Refer to the Software Design Document for further information on how SIDs are derived.

If **mach\_port\_allocate\_secure** is given a SEC\_NULL\_SID as the *obj\_sid*, then its behavior is essentially the same as **mach\_port\_allocate**.

## PARAMETERS

task

[in task port] The task acquiring the port right.

right

[in scalar] The kind of entity to be created. This is one of the following:

#### MACH\_PORT\_RIGHT\_RECEIVE

mach\_port\_allocate creates a port. The new port is not a member of any port set. It doesn't have any extant send or send-once rights. Its make-send count is zero, its sequence number is zero, its queue limit is MACH\_PORT\_QLIMIT\_DEFAULT, and it has no queued messages. *name* denotes the receive right for the new port.

*task* does not hold send rights for the new port, only the receive right. **mach\_port\_insert\_right** and **mach\_port\_extract\_right** can be used to convert the receive right into a combined send/receive right.

#### MACH\_PORT\_RIGHT\_PORT\_SET

**mach\_port\_allocate** creates a port set. The new port set has no members. An object security identifier cannot be associated with a port set, hence, if one is specified with **mach\_port\_allocate\_secure**, it will be ignored.

#### MACH\_PORT\_RIGHT\_DEAD\_NAME

**mach\_port\_allocate** creates a dead name. The new dead name has one user reference. An object security identifier cannot be associated with a dead name, hence, if one is specified with **mach\_port\_allocate\_secure**, it will be ignored.

#### name

[out scalar] The task's name for the port right. This can be any name that wasn't in use.

#### obj\_sid

[in security id] The object security identifier to be associated with the created port. The interface **SSI\_context\_to\_mid** can be used to obtain a mandatory identifier from the Security Server. The mandatory identifier and the authentication identifier can be combined into a security identifier via **make\_sid**.

#### NOTES

This interface is machine word length specific because of the port name parameter.

## **RETURN VALUE**

#### KERN\_NO\_SPACE

There was no room in *task*'s IPC name space for another right.

# **RELATED INFORMATION**

Functions: mach\_port\_allocate\_name, mach\_port\_allocate\_name\_secure, mach\_port\_deallocate, mach\_port\_insert\_right, mach\_port\_extract\_right, SSI\_context\_to\_mid, make\_sid.

# mach\_port\_allocate\_name/ mach\_port\_allocate\_name\_secure

**Function** — Creates a port right with a given name and optionally associates an object security identifier with the port.

#### **SYNOPSIS**

kern_return_t mach_port_allocate_name	
(mach_port_t	task,
mach_port_right_t	right,
mach_port_t	name);
kern_return_t mach_port_allocate_name_secure	
(mach_port_t	task,
mach_port_right_t	right,
mach_port_t	name,
security_id_t	obj_sid);

#### **DESCRIPTION**

The **mach\_port\_allocate\_name** function creates a new right in the specified task, with a specified name for the new right. The **mach\_port\_allocate\_name\_secure** function creates a new right in the specified task, with a specified name and a specified object security identifier.

#### SECURITY

The requesting task must hold *tsv\_add\_name* permission to the task port *task*. If the request results in a new receive right being created for *task*'s task, task must have *av\_hold\_receive* permission to the newly allocated port.

When using **mach\_port\_allocate\_name**, the port is allocated with an object security identifier derived from *task*'s subject security identifier.

If **mach\_port\_allocate\_name\_secure** is given a SEC\_NULL\_SID as the *obj\_sid*, then its behavior is essentially the same as **mach\_port\_allocate\_name**.

#### PARAMETERS

```
task
```

[in task port] The task acquiring the port right.

right

[in scalar] The kind of entity to be created. This is one of the following values:

#### MACH\_PORT\_RIGHT\_RECEIVE

mach\_port\_allocate\_name creates a port. The new port is not a member of any port set. It doesn't have any extant send or send-once rights. Its make-send count is zero, its sequence number is zero, its queue limit is MACH\_PORT\_QLIMIT\_DEFAULT, and it has no queued messages. *name* denotes the receive right for the new port.

*task* does not hold send rights for the new port, only the receive right. **mach\_port\_insert\_right** and **mach\_port\_extract\_right** can be used to convert the receive right into a combined send/receive right.

#### MACH\_PORT\_RIGHT\_PORT\_SET

**mach\_port\_allocate\_name** creates a port set. The new port set has no members. An object security identifier cannot be associated with a port set, hence, if one is specified with **mach\_port\_allocate\_name\_secure**, it will be ignored.

#### MACH\_PORT\_RIGHT\_DEAD\_NAME

**mach\_port\_allocate\_name** creates a new dead name. The new dead name has one user reference. An object security identifier cannot be associated with dead name, hence, if one is specified with **mach\_port\_allocate\_name\_secure**, it will be ignored.

пате

[in scalar] The task's name for the port right. *name* must not already be in use for some right, and it can't be the reserved values MACH\_PORT\_NULL and MACH\_PORT\_DEAD.

[in security id] The object security identifier to be associated with the allocated port. The interface **SSI\_context\_to\_mid** can be used to obtain a mandatory identifier from the Security Server. The mandatory identifier and the authentication identifier can be combined into a security identifier via **make\_sid**.

#### NOTES

This interface is machine word length specific because of the port name parameter.

## **RETURN VALUE**

#### KERN\_NAME\_EXISTS

name was already in use for a port right.

obj\_sid

## **RELATED INFORMATION**

Functions:mach\_port\_allocate,mach\_port\_allocate\_secure,mach\_port\_deallocate, mach\_port\_rename,SSI\_context\_to\_mid, make\_sid.

# mach\_port\_deallocate

Function — Releases a user reference for a right

#### **SYNOPSIS**

kern\_return\_t mach\_port\_deallocate (mach\_port\_t task, mach\_port\_t name);

#### DESCRIPTION

The **mach\_port\_deallocate** function releases a user reference for a right. It is an alternate form of **mach\_port\_mod\_refs** that allows a task to release a user reference for a send or send-once right without failing if the port has died and the right is now actually a dead name.

If *name* denotes a dead name, send right, or send-once right, then the right loses one user reference. If it only had one user reference, then the right is destroyed.

## SECURITY

The requesting task must hold *tsv\_remove\_name* permission to the task port *task*.

#### PARAMETERS

task

[in task port] The task holding the right.

name

[in scalar] The task's name for the right.

#### NOTES

This interface is machine word length specific because of the port name parameter.

#### **RETURN VALUE**

KERN\_INVALID\_RIGHT name denoted an invalid right.

#### **RELATED INFORMATION**

Functions: mach\_port\_allocate, mach\_port\_mod\_refs.

mach\_port\_allocate\_name,

## mach\_port\_destroy

Function — Removes a task's rights for a name

#### **SYNOPSIS**

kern\_return\_t **mach\_port\_destroy** (mach\_port\_t mach port t

task; name);

#### DESCRIPTION

The **mach\_port\_destroy** function de-allocates all rights denoted by a name. The name becomes immediately available for reuse.

For most purposes, **mach\_port\_mod\_refs** and **mach\_port\_deallocate** are preferable.

If *name* denotes a port set, then all members of the port set are implicitly removed from the port set.

If *name* denotes a receive right that is a member of a port set, the receive right is implicitly removed from the port set. If there is a port-destroyed request registered for the port, then the receive right is not actually destroyed, but instead is sent in a port-destroyed notification. (Note: Port destroyed notifications are currently planned for deletion.) If there is no registered port-destroyed request, remaining messages queued to the port are destroyed and extant send and send-once rights turn into dead names. If those send and send-once rights have dead-name requests registered, then dead-name notifications are generated for them.

If *name* denotes a send-once right, then the send-once right is used to produce a send-once notification for the port.

If *name* denotes a send-once, send, and/or receive right, and it has a dead-name request registered, then the registered send-once right is used to produce a port-deleted notification for the name.

#### SECURITY

The requesting task must hold *tsv\_remove\_name* permission to the task port *task*.

#### PARAMETERS

task

[in task port] The task holding the right.

name

[in scalar] The task's name for the right.

## NOTES

This interface is machine word length specific because of the port name parameter.

# **RETURN VALUE**

KERN\_INVALID\_NAME name did not denote a right.

# **RELATED INFORMATION**

Functions:mach\_port\_allocate,mach\_port\_mod\_refs,mach\_port\_request\_notification.

mach\_port\_allocate\_name, mach\_port\_deallocate,

# mach\_port\_extract\_right

Function — Extracts a port right from a task

#### **SYNOPSIS**

kern\_return\_tmach\_port\_extract\_right(mach\_port\_ttask,mach\_port\_tname,mach\_msg\_type\_name\_tdesired\_type,mach\_port\_t\*right,mach\_msg\_type\_name\_t\*acquired\_type);

#### DESCRIPTION

The **mach\_port\_extract\_right** function extracts a port right from the target task and returns it to the caller as if the task sent the right voluntarily, using *desired\_type* as the value of *msgt\_name*. See **mach\_msg**.

The returned value of *acquired\_type* will be MACH\_MSG\_TYPE\_PORT\_SEND if a send right is extracted, MACH\_MSG\_TYPE\_PORT\_RECEIVE if a receive right is extracted, and MACH\_MSG\_TYPE\_PORT\_SEND\_ONCE if a send-once right is extracted.

# SECURITY

The requesting task must hold *tsv\_extract\_right* permission to the task port *task*. The requesting task must also have permission to hold the port right extracted..

## PARAMETERS

task

[in task port] The task holding the port right.

name

[in scalar] The task's name for the port right.

#### desired\_type

[in scalar] IPC type, specifying how the right should be extracted.

#### right

[out random port] The extracted right.

#### acquired\_type

[out scalar] The type of the extracted right.

# NOTES

This interface is machine word length specific because of the port name parameter.

# **RETURN VALUE**

KERN\_INVALID\_NAME *name* did not denote a right.

KERN\_INVALID\_RIGHT name denoted an invalid right.

# **RELATED INFORMATION**

Functions: mach\_port\_insert\_right, mach\_msg.

# mach\_port\_get\_receive\_status

Function — Returns the status of a receive right

#### **SYNOPSIS**

kern_return_t mach_port_get_receive_status	
(mach_port_t	task,
mach_port_t	name,
mach_port_status_t*	status);

#### DESCRIPTION

The **mach\_port\_get\_receive\_status** function returns the current status of the specified receive right.

#### **SECURITY**

The requesting task must hold *tsv\_observe\_pns\_info* permission to the task port *task*.

## PARAMETERS

task
<i>couble</i>

[in task port] The task holding the receive right.

name

[in scalar] The task's name for the receive right.

status

[out structure] The status information for the receive right.

#### NOTES

This interface is machine word length specific because of the port name parameter.

#### **RETURN VALUE**

KERN\_INVALID\_NAME name did not denote a right.

KERN\_INVALID\_RIGHT *name* denoted a right, but not a receive right.

# **RELATED INFORMATION**

Functions: mach\_port\_set\_qlimit, mach\_port\_set\_seqno.

mach\_port\_set\_mscount,

Data Structures: mach\_port\_status.

# mach\_port\_get\_refs

Function — Retrieves the number of user references for a right

#### **SYNOPSIS**

kern_return_t mach_port_get_refs	
(mach_port_t	task,
mach_port_t	name,
mach_port_right_t	right,
mach_port_urefs_t*	refs);

#### DESCRIPTION

The **mach\_port\_get\_refs** function returns the number of user references a task has for a right.

If *name* denotes a right, but not the type of right specified, then zero is returned. Otherwise a positive number of user references is returned. Note a name may simultaneously denote send and receive rights.

#### SECURITY

The requesting task must hold *tsv\_observe\_pns\_info* permission to the task port *task*.

#### PARAMETERS

task

[in task port] The task holding the right.

name

[in scalar] The task's name for the right.

right

[in scalar] The type of right / entity being examined: MACH\_PORT\_RIGHT\_SEND, MACH\_PORT\_RIGHT\_RECEIVE, MACH\_PORT\_RIGHT\_SEND\_ONCE, MACH\_PORT\_RIGHT\_PORT\_SET or MACH\_PORT\_RIGHT\_DEAD\_NAME.

refs

[out scalar] Number of user references.

# NOTES

This interface is machine word length specific because of the port name parameter.

# **RETURN VALUE**

KERN\_INVALID\_NAME *name* did not denote a right.

# **RELATED INFORMATION**

Functions: mach\_port\_mod\_refs.

# mach\_port\_get\_set\_status

Function — Returns the members of a port set

#### **SYNOPSIS**

kern_return_t mach_port_get_set_status	
(mach_port_t	task,
mach_port_t	name,
mach_port_array_t*	members,
mach_msg_type_number_t*	<i>count)</i> ;

#### **DESCRIPTION**

The **mach\_port\_get\_set\_status** function returns the members of a port set. *members* is an array that is automatically allocated when the reply message is received.

## **SECURITY**

The requesting task must hold *tsv\_observe\_pns\_info* permission to the task port *task*.

## PARAMETERS

task

[in task port] The task holding the port set.

пате

[in scalar] The task's name for the port set.

#### members

[out pointer to dynamic array of *mach\_port\_t*] The task's names for the port set's members.

count

[out scalar] The number of member names returned.

#### NOTES

This interface is machine word length specific because of the port name parameter.

## **RETURN VALUE**

KERN\_INVALID\_NAME name did not denote a right. KERN\_INVALID\_RIGHT *name* denoted a right, but not a port set.

# **RELATED INFORMATION**

Functions: mach\_port\_move\_member, vm\_deallocate.

## mach\_port\_insert\_right

**Function** — Inserts a port right into a task

#### **SYNOPSIS**

kern_return_t mach_port_insert_right	
(mach_port_t	task,
mach_port_t	name,
mach_port_t	right,
mach_msg_type_name_t	right_type);

#### DESCRIPTION

The **mach\_port\_insert\_right** function inserts into *task* the caller's right for a port, using a specified name for the right in the target task.

The specified *name* can't be one of the reserved values MACH\_PORT\_NULL or MACH\_PORT\_DEAD. The *right* can't be MACH\_PORT\_NULL or MACH\_PORT\_DEAD.

The argument *right\_type* specifies a right to be inserted and how that right should be extracted from the caller. It should be a value appropriate for *msgt\_name*; see **mach\_msg**.

If *right\_type* is MACH\_MSG\_TYPE\_MAKE\_SEND, MACH\_MSG\_TYPE\_MOVE\_SEND, or MACH\_MSG\_TYPE\_COPY\_SEND, then a send right is inserted. If the target already holds send or receive rights for the port, then *name* should denote those rights in the target. Otherwise, *name* should be unused in the target. If the target already has send rights, then those send rights gain an additional user reference. Otherwise, the target gains a send right, with a user reference count of one.

If *right\_type* is MACH\_MSG\_TYPE\_MAKE\_SEND\_ONCE or MACH\_MSG\_TYPE\_MOVE\_SEND\_ONCE, then a send-once right is inserted. The *name* should be unused in the target. The target gains a send-once right.

If *right\_type* is MACH\_MSG\_TYPE\_MOVE\_RECEIVE, then a receive right is inserted. If the target already holds send rights for the port, then *name* should denote those rights in the target. Otherwise, *name* should be unused in the target. The receive right is moved into the target task.

#### SECURITY

The requesting task must hold *tsv\_add\_name* permission to the task port *task*. The task having *task* as it's task port must also hold the appropriate *av\_hold\_receive*, *av\_hold\_send* or *av\_hold\_send\_once* permission to the port associated *with name*.

# PARAMETERS

#### task

[in task port] The task which gets the caller's right.

пате

[in scalar] The name by which *task* will know the right.

right

[in random port] The port right.

right\_type

[in scalar] IPC type of the sent right; e.g., MACH\_MSG\_TYPE\_COPY\_SEND or MACH\_MSG\_TYPE\_MOVE\_RECEIVE.

## NOTES

This interface is machine word length specific because of the port name parameter.

## **RETURN VALUE**

KERN\_NAME\_EXISTS *name* already denoted a right.

KERN\_INVALID\_CAPABILITY *right* was null or dead.

KERN\_UREFS\_OVERFLOW Inserting the right would overflow *name*'s user-reference count.

KERN\_RIGHT\_EXISTS task already had rights for the port, with a different name.

#### **RELATED INFORMATION**

Functions: mach\_port\_extract\_right, mach\_msg.

# mach\_port\_mod\_refs

Function — Changes the number of user refs for a right

#### **SYNOPSIS**

kern_return_t mach_port_mod_refs	
(mach_port_t	task,
mach_port_t	name,
mach_port_right_t	right,
mach_port_delta_t	delta);

#### DESCRIPTION

The **mach\_port\_mod\_refs** function requests that the number of user references a task has for a right be changed. This results in the right being destroyed, if the number of user references is changed to zero.

The *name* should denote the specified right. The number of user references for the right is changed by the amount *delta*, subject to the following restrictions: port sets, receive rights, and send-once rights may only have one user reference. The resulting number of user references can't be negative. If the resulting number of user references is zero, the effect is to de-allocate the right. For dead names and send rights, there is an implementation-defined maximum number of user references.

If the call destroys the right, then the effect is as described for **mach\_port\_destroy**, with the exception that **mach\_port\_destroy** simultaneously destroys all the rights denoted by a name, while **mach\_port\_mod\_refs** can only destroy one right. The name will be available for reuse if it only denoted the one right.

#### SECURITY

If the port is destroyed as a result of this request, the requesting task must hold *tsv\_remove\_name* permission to the task port *task*.

#### PARAMETERS

task

[in task port] The task holding the right.

пате

[in scalar] The task's name for the right.

right

[in scalar] The type of right / entity being modified: MACH\_PORT\_RIGHT\_SEND, MACH\_PORT\_RIGHT\_RECEIVE,

MACH\_PORT\_RIGHT\_SEND\_ONCE, MACH\_PORT\_RIGHT\_PORT\_SET MACH\_PORT\_RIGHT\_DEAD\_NAME.

delta

[in scalar] Signed change to the number of user references.

#### NOTES

This interface is machine word length specific because of the port name parameter.

## **RETURN VALUE**

KERN\_INVALID\_NAME name did not denote a right.

KERN\_INVALID\_RIGHT name denoted a right, but not the specified right.

KERN\_INVALID\_VALUE The user-reference count would become negative.

KERN\_UREFS\_OVERFLOW The user-reference count would overflow.

# **RELATED INFORMATION**

Functions: mach\_port\_destroy, mach\_port\_get\_refs.

or

# mach\_port\_move\_member

Function — Moves a receive right into/out of a port set

#### **SYNOPSIS**

kern\_return\_t **mach\_port\_move\_member** (mach\_port\_t mach\_port\_t mach\_port\_t

task, member, after);

#### **DESCRIPTION**

The **mach\_port\_move\_member** function moves a receive right into a port set. If the receive right is already a member of another port set, it is removed from that set first. If the port set is MACH\_PORT\_NULL, then the receive right is not put into a port set, but removed from its current port set.

#### **SECURITY**

The requesting task must hold *tsv\_manipulate\_port\_set* permission to the task port *task*.

#### PARAMETERS

task

[in task port] The task holding the port set and receive right.

member

[in scalar] The task's name for the receive right.

after

[in scalar] The task's name for the port set.

#### NOTES

This interface is machine word length specific because of the port name parameter.

## **RETURN VALUE**

KERN\_INVALID\_NAME

member or after did not denote a right.

KERN\_INVALID\_RIGHT

*member* denoted a right, but not a receive right, or *after* denoted a right, but not a port set.

KERN\_NOT\_IN\_SET

after was MACH\_PORT\_NULL, but member wasn't currently in a port set.

# **RELATED INFORMATION**

Functions: mach\_port\_get\_set\_status, mach\_port\_get\_receive\_status.

# mach\_port\_names

Function — Return information about a task's port name space

### **SYNOPSIS**

kern_return_t mach_port_names	
(mach_port_t	task,
mach_port_array_t*	names,
mach_msg_type_number_t*	ncount,
mach_port_type_array_t*	types,
mach_msg_type_number_t*	tcount);

# DESCRIPTION

The **mach\_port\_names** returns information about *task*'s port name space. It returns *task*'s currently active names, which represent some port, port set, or dead name right. For each name, it also returns what type of rights *task* holds (the same information returned by **mach\_port\_type**).

# **SECURITY**

The requesting task must hold *tsv\_observe\_pns\_info* permission to the task port *task*.

# PARAMETERS

task

[in task port] The task whose port name space is queried.

#### names

[out pointer to dynamic array of *mach\_port\_t*] The names of the ports, port sets, and dead names in the task's port name space, in no particular order.

#### ncount

[out scalar] The number of names returned.

#### types

[out pointer to dynamic array of *mach\_port\_type\_t*] The type of each corresponding name. Indicates what kind of rights the task holds with that name.

#### tcount

[out scalar] Should be the same as *ncount*.

# NOTES

This interface is machine word length specific because of the port name parameter.

# **RETURN VALUE**

Only generic errors apply.

# **RELATED INFORMATION**

Functions: mach\_port\_type, vm\_deallocate.

# mach\_port\_rename

Function — Change a task's name for a right

### **SYNOPSIS**

kern\_return\_t **mach\_port\_rename** (mach\_port\_t mach\_port\_t mach\_port\_t

task, old\_name, new\_name);

# **DESCRIPTION**

The **mach\_port\_rename** function changes the name by which a port, port set, or dead name is known to *task. new\_name* must not already be in use, and it can't be the distinguished values MACH\_PORT\_NULL and MACH\_PORT\_DEAD.

# **SECURITY**

The requesting task must hold *tsv\_port\_rename* permission to the task port *task*.

# PARAMETERS

task

[in task port] The task holding the port right.

old\_name

[in scalar] The original name of the port right.

new\_name

[in scalar] The new name for the port right.

# NOTES

This interface is machine word length specific because of the port name parameter.

# **RETURN VALUE**

KERN\_INVALID\_NAME *old\_name* did not denote a right.

KERN\_NAME\_EXISTS new\_name already denoted a right.

# **RELATED INFORMATION**

Functions: mach\_port\_names.

# mach\_port\_request\_notification

Function — Request a notification of a port event

# **SYNOPSIS**

kern_return_t mach_port_request_notification	
(mach_port_t	task,
mach_port_t	name,
mach_msg_id_t	variant,
mach_port_mscount_t	sync,
mach_port_t	notify,
mach_msg_type_name_t	notify_type,
mach_port_t*	previous);

# DESCRIPTION

The **mach\_port\_request\_notification** function registers a request for a notification and supplies a send-once right that the notification will use. It is an atomic swap, returning the previously registered send-once right (or MACH\_PORT\_NULL for none). A notification request may be cancelled by providing MACH\_PORT\_NULL.

The variant argument takes the following values:

#### MACH\_NOTIFY\_PORT\_DESTROYED

*sync* must be zero. The *name* must specify a receive right, and the call requests a port-destroyed notification for the receive right. If the receive right were to have been destroyed, say by **mach\_port\_destroy**, then instead the receive right will be sent in a port-destroyed notification to the registered send-once right.

(Note: This feature is currently planned for deletion.)

#### MACH\_NOTIFY\_DEAD\_NAME

The call requests a dead-name notification. *name* specifies send, receive, or send-once rights for a port. If the port is destroyed (and the right remains, becoming a dead name), then a dead-name notification which carries the name of the right will be sent to the registered send-once right. If *sync* is non-zero, the *name* may specify a dead name, and a dead-name notification is immediately generated.

Whenever a dead-name notification is generated, the user reference count of the dead name is incremented. For example, a send right with two user refs has a registered dead-name request. If the port is destroyed, the send right turns into a dead name with three user refs (instead of two), and a dead-name notification is generated. If the name is made available for reuse, perhaps because of **mach\_port\_destroy** or **mach\_port\_mod\_refs**, or the name denotes a send-once right which has a message sent to it, then the registered send-once right is used to generate a port-deleted notification instead.

#### MACH\_NOTIFY\_NO\_SENDERS

The call requests a no-senders notification. *name* must specify a receive right. If the receive right's make-send count is greater than or equal to the sync value, and it has no extant send rights, than an immediate no-senders notification is generated. Otherwise the notification is generated when the receive right next loses its last extant send right. In either case, any previously registered send-once right is returned.

The no-senders notification carries the value the port's make-send count had when it was generated. The make-send count is incremented whenever MACH\_MSG\_TYPE\_MAKE\_SEND is used to create a new send right from the receive right. The make-send count is reset to zero when the receive right is carried in a message.

(Note: Currently, moving a receive right does not affect any extant nosenders notifications. It is currently planned to change this so that nosenders notifications are canceled, with a send-once notification sent to indicate the cancelation.)

# SECURITY

The requesting task must hold *tsv\_register\_notification* permission to the task port *task*.

# PARAMETERS

task

[in task port] The task holding the specified right.

name

[in scalar] The task's name for the right.

#### variant

[in scalar] The type of notification.

#### sync

[in scalar] Some variants use this value to overcome race conditions.

#### notify

[in notify port] A send-once right, to which the notification will be sent.

notify\_type

[in scalar] IPC type of the sent right; either MACH\_MSG\_TYPE\_MAKE\_SEND\_ONCE or MACH\_MSG\_TYPE\_MOVE\_SEND\_ONCE.

previous

[out notify port] The previously registered send-once right.

### NOTES

This interface is machine word length specific because of the port name parameter.

# **RETURN VALUE**

KERN\_INVALID\_NAME name did not denote a right.

KERN\_INVALID\_RIGHT name denoted an invalid right.

KERN\_INVALID\_CAPABILITY *notify* was invalid.

When using MACH\_NOTIFY\_DEAD\_NAME:

KERN\_UREFS\_OVERFLOW

*name* denotes a dead name, but generating an immediate dead-name notification would overflow the name's user-reference count.

# **RELATED INFORMATION**

Functions: mach\_port\_get\_receive\_status.

# mach\_port\_set\_mscount

Function — Changes the make-send count of a port

### **SYNOPSIS**

kern_return_t mach_port_set_mscount	
(mach_port_t	task,
mach_port_t	name,
mach_port_mscount_t	mscount);

# DESCRIPTION

The **mach\_port\_set\_mscount** function changes the make-send count of *task*'s receive right named *name*. All values for *mscount* are valid.

# SECURITY

The requesting task must hold *tsv\_alter\_pns\_info* permission to the task port *task*.

# PARAMETERS

#### task

[in task port] The task owning the receive right.

#### name

[in scalar] *task*'s name for the receive right.

#### mscount

[in scalar] New value for the make-send count for the receive right.

# NOTES

This interface is machine word length specific because of the port name parameter.

# **RETURN VALUE**

KERN\_INVALID\_NAME name did not denote a right.

KERN\_INVALID\_RIGHT *name* denoted a right, but not a receive right.

# **RELATED INFORMATION**

Functions: mach\_port\_get\_receive\_status, mach\_port\_set\_qlimit.

# mach\_port\_set\_qlimit

Function — Changes the queue limit of a port

### **SYNOPSIS**

kern_return_t mach_port_set_qlimit	
(mach_port_t	task,
mach_port_t	name,
mach_port_msgcount_t	qlimit);

# DESCRIPTION

The **mach\_port\_set\_qlimit** function changes the queue limit of *task*'s receive right named *name*. Valid values for *qlimit* are between zero and MACH\_PORT\_QLIMIT\_MAX (defined in **mach.h**), inclusive.

# SECURITY

The requesting task must hold *tsv\_alter\_pns\_info* permission to the task port *task*.

# PARAMETERS

task

[in task port] The task owning the receive right.

name

[in scalar] *task*'s name for the receive right.

#### qlimit

[in scalar] The number of messages which may be queued to this port without causing the sender to block.

# NOTES

This interface is machine word length specific because of the port name parameter.

# **RETURN VALUE**

KERN\_INVALID\_NAME name did not denote a right.

KERN\_INVALID\_RIGHT

name denoted a right, but not a receive right.

# **RELATED INFORMATION**

Functions: mach\_port\_get\_receive\_status, mach\_port\_set\_mscount.

# mach\_port\_set\_seqno

Function — Changes the sequence number of a port

### **SYNOPSIS**

kern_return_t mach_port_set_seqno	
(mach_port_t	task,
mach_port_t	name,
mach_port_seqno_t	seqno);

# DESCRIPTION

The **mach\_port\_set\_seqno** function changes the sequence number of *task*'s receive right named *name*.

# **SECURITY**

The requesting task must hold *tsv\_alter\_pns\_info* permission to the task port *task*.

# PARAMETERS

#### task

[in task port] The task owning the receive right.

#### name

[in scalar] *task*'s name for the receive right.

#### seqno

[in scalar] The sequence number that the next message received from the port will have.

# NOTES

This interface is machine word length specific because of the port name parameter.

### **RETURN VALUE**

KERN\_INVALID\_NAME *name* did not denote a right.

KERN\_INVALID\_RIGHT *name* denoted a right, but not a receive right.

# **RELATED INFORMATION**

Functions: mach\_port\_get\_receive\_status

# mach\_port\_type/mach\_port\_type\_secure

Function — Return information about a task's port name

### **SYNOPSIS**

kern_return_t mach_port_type	
(mach_port_t	task,
mach_port_t	name,
mach_port_type_t*	ptype);
kern return t mach next type gegine	

kern\_return\_t mach\_port\_type\_secure

task,
name,
ptype,
obj_sid,
<i>av)</i> ;

# **DESCRIPTION**

The **mach\_port\_type** function returns information about *task*'s rights for a specific name in its port name space. The **mach\_port\_type\_secure** function returns information about *task*'s rights, *task*'s access, and the security id for the port associated with a specific name in its port name space. The returned *ptype* is a bit-mask indicating what rights *task* holds with this name. The bit-mask is composed of the following bits:

MACH\_PORT\_TYPE\_SEND The name denotes a send right.

MACH\_PORT\_TYPE\_RECEIVE The name denotes a receive right.

MACH\_PORT\_TYPE\_SEND\_ONCE The name denotes a send-once right.

MACH\_PORT\_TYPE\_PORT\_SET The name denotes a port set.

MACH\_PORT\_TYPE\_DEAD\_NAME The name is a dead name.

MACH\_PORT\_TYPE\_DNREQUEST A dead-name request has been registered for the right.

MACH\_PORT\_TYPE\_MAREQUEST

A msg-accepted request for the right is pending. (Note: This feature is planned for deletion.)

# MACH\_PORT\_TYPE\_COMPAT

The port right was created in the compatibility mode.

### **SECURITY**

The requesting task must hold *tsv\_observe\_pns\_info* permissions to the task port *task*.

### PARAMETERS

task

[in task port] The task whose port name space is queried.

пате

[in scalar] The name being queried.

ptype

[out scalar] The type of the name. Indicates what kind of right the task holds for the port, port set, or dead name.

### obj\_sid

[out security id] The security identifier of the port associated with the port right. SEC\_NULL\_SID if *name* is a port set or a dead name.

av

[out access vector] The access vector indicating *task*'s allowed accesses to *name*.

### NOTES

This interface is machine word length specific because of the port name parameter.

# **RETURN VALUE**

KERN\_INVALID\_NAME name did not denote a right.

# **RELATED INFORMATION**

Functions:mach\_port\_names,mach\_port\_get\_receive\_status,mach\_port\_get\_set\_status.

# mach\_reply\_port

System Trap— Creates a port for the task

# LIBRARY

#include <mach/mach\_traps.h>

# **SYNOPSIS**

 $mach\_port\_t \ mach\_reply\_port$ 

();

# DESCRIPTION

The **mach\_reply\_port** function creates a new port for the current task and returns the name assigned by the kernel. The kernel records the name in the task's port name space and grants the task receive rights for the port. The new port is not a member of any port set.

This function is an optimized version of **mach\_port\_allocate** that uses no port references. Its main purpose is to allocate a reply port for the task when the task is starting— namely, before it has any ports to use as reply ports for any IPC based system functions.

# SECURITY

The requesting task must hold tsv\_add\_name permission to its own task port.

# PARAMETERS

None

# CAUTIONS

Although the created port can be used for any purpose, the implementation may optimize its use as a reply port.

# **RETURN VALUE**

MACH\_PORT\_NULL No port was allocated.

[reply port]

Any other value indicates success.

# **RELATED INFORMATION**

Functions: mach\_port\_allocate.

# CHAPTER 4 Virtual Memory Interface

This chapter discusses the specifics of the kernel's virtual memory interfaces. This includes memory status related functions associated with a single task. Functions that are related to, or used by, external memory managers (pagers) are described in the next chapter.

# vm\_allocate/vm\_allocate\_secure

Function — Allocates a region of virtual memory

# SYNOPSIS

kern_return_t <b>vm_allocate</b>	
(mach_port_t	target_task,
vm_address_t*	address,
vm_size_t	size,
boolean_t	anywhere);
kern_return_t <b>vm_allocate_secure</b> (mach port t	target task,
vm_address_t*	address,
vm_size_t	size,
boolean_t	anywhere,
security_id_t	obj_sid);

### DESCRIPTION

The **vm\_allocate** and **vm\_allocate\_secure** functions allocate a region of virtual memory in the specified task's address space. A new region is always zero filled. The physical memory is not allocated until an executing thread references the new virtual memory. In addition to allocating a region of virtual memory, **vm\_allocate\_secure** associates a specific object security identifier with the memory region.

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If *anywhere* is true, the returned *address* will be at a page boundary; otherwise, the region starts at the beginning of the virtual page containing *address. size* is always rounded up to an integral number of pages. Because of this rounding to virtual page boundaries, the amount of memory allocated may be greater than *size*. Use **vm\_statistics** to find the current virtual page size.

Use the **mach\_task\_self** function to return the caller's value for *target\_task*. This macro returns the task kernel port for the caller.

Initially, there are no access restrictions on any of the pages of the newly allocated region. Child tasks inherit the new region as a copy.

# SECURITY

The requesting task must hold *tsv\_allocate\_vm\_region* permission to *target\_task* and *mosv\_map\_vm\_region* to the object port of the memory object backing the region for the *address* specified. Permissions to the memory are determined by the permissions that *target\_task* has to the memory object associated with the allocated memory.

If no object security identifier is provided, the memory is allocated with an object security identifier derived from *target\_task*'s subject security identifier.

# PARAMETERS

#### target\_task

[in task port] The port for the task in whose address space the region is to be allocated.

#### address

[pointer to in/out scalar] The starting address for the region. If there is not enough room following the address, the kernel does not allocate the region. The kernel returns the starting address actually used for the allocated region.

#### size

[in scalar] The number of bytes to allocate.

#### anywhere

[in scalar] Placement indicator. If false, the kernel allocates the region starting at *address*. If true, the kernel allocates the region wherever enough space is available within the address space. The kernel returns the starting address actually used in *address*.

#### obj\_sid

[in security id] The security identifier to be associated with the region to be allocated.

# NOTES

For languages other than C, use the **vm\_statistics** and **mach\_task\_self** functions to return the task's kernel port (for *target\_task*).

To establish different protections for the new region, use the **vm\_protect** and **vm\_inherit** functions.

A task's address space can contain both explicitly allocated memory and automatically allocated memory. The **vm\_allocate** function explicitly allocates memory. The kernel automatically allocates memory to hold out-of-line data passed in a message (and received with **mach\_msg**). The kernel allocates memory for the passed data as an integral number of pages.

This interface is machine word length specific because of the virtual address parameter.

# **RETURN VALUE**

KERN\_INVALID\_ADDRESS The specified address is illegal.

KERN\_NO\_SPACE

There is not enough space in the task's address space to allocate the new region.

# **RELATED INFORMATION**

Functions: task\_get\_special\_port, vm\_deallocate, vm\_inherit, vm\_protect, vm\_region, vm\_statistics.

### vm\_copy

**Function** — Copies a region in a task's virtual memory

### **SYNOPSIS**

kern\_return\_t vm\_copy (mach\_port\_t target\_task, vm\_address\_t source\_address, vm\_size\_t vm\_address\_t dest\_address);

# DESCRIPTION

The **vm\_copy** function copies a source region to a destination region within the same task's virtual memory. It is semantically equivalent to vm\_read followed by vm\_write. The destination region can overlap the source region.

The destination region must already be allocated. The source region must be readable, and the destination region must be writable.

# **SECURITY**

The requesting task must hold *tsv\_copy\_vm* permission to *target\_task*.

In the current implementation the data copied retains the security identifier assigned to the memory region from which the data came.

# PARAMETERS

target\_task

[in task port] The port for the task whose memory is to be copied.

#### source\_address

[in scalar] The starting address for the source region. The address must be on a page boundary.

#### count

[in scalar] The number of bytes in the source region. The number of bytes must convert to an integral number of virtual pages.

#### dest address

[in scalar] The starting address for the destination region. The address must be on a page boundary.

count,

# NOTES

This interface is machine word length specific because of the virtual address parameter.

# **RETURN VALUE**

### KERN\_PROTECTION\_FAILURE

The source region is protected against reading, or the destination region is protected against writing.

#### KERN\_INVALID\_ADDRESS

An address is illegal or specifies a non-allocated region, or there is not enough memory following one of the addresses.

# **RELATED INFORMATION**

Functions: vm\_protect, vm\_read, vm\_write, vm\_statistics.

# vm\_deallocate

Function — De-allocates a region of virtual memory

### **SYNOPSIS**

kern\_return\_t **vm\_deallocate** (mach\_port\_t vm\_address\_t vm\_size\_t

target\_task, address, size);

# DESCRIPTION

The **vm\_deallocate** function de-allocates a region of virtual memory in the specified task's address space.

The region starts at the beginning of the virtual page containing *address*; it ends at the end of the virtual page containing *address* + *size* - 1. Because of this rounding to virtual page boundaries, the amount of memory de-allocated may be greater than *size*. Use **vm\_statistics** to find the current virtual page size.

**vm\_deallocate** affects only *target\_task*. Other tasks that have access to the deallocated memory can continue to reference it.

# SECURITY

The requesting task must hold *tsv\_deallocate\_vm\_region* permission to *target\_task*.

# PARAMETERS

target\_task

[in task port] The port for the task in whose address space the region is to be de-allocated.

address

[in scalar] The starting address for the region.

size

[in scalar] The number of bytes to de-allocate.

### NOTES

**vm\_deallocate** can be used to de-allocate memory passed as out-of-line data in a message.

This interface is machine word length specific because of the virtual address parameter.

# **RETURN VALUE**

KERN\_INVALID\_ADDRESS The address is illegal or specifies a non-allocated region.

# **RELATED INFORMATION**

Functions: mach\_msg, vm\_allocate, vm\_statistics.

# vm\_inherit

Function — Sets the inheritance attribute for a region of virtual memory

### SYNOPSIS

kern_return_t <b>vm_inherit</b>	
(mach_port_t	target_task,
vm_address_t	address,
vm_size_t	size,
vm_inherit_t	new_inheritance);

### DESCRIPTION

The **vm\_inherit** function sets the inheritance attribute for a region within the specified task's address space. The inheritance attribute determines the type of access established for child tasks at task creation.

Because inheritance applies to virtual pages, the specified *address* and *size* are rounded to page boundaries, as follows: the region starts at the beginning of the virtual page containing *address*; it ends at the end of the virtual page containing *address* + *size* - 1. Because of this rounding to virtual page boundaries, the amount of memory affected may be greater than *size*. Use **vm\_statistics** to find the current virtual page size.

A parent and a child task can share the same physical memory only if the inheritance for the memory is set to VM\_INHERIT\_SHARE before the child task is created. This is the only way that two tasks can share memory (other than through the use of an external memory manager; see vm\_map).

Note that all the threads within a task share the task's memory.

# SECURITY

The requesting task must hold *tsv\_set\_vm\_region\_inherit* permission to *target\_task*.

# PARAMETERS

target\_task

[in task port] The port for the task whose address space contains the region.

address

[in scalar] The starting address for the region.

size

[in scalar] The number of bytes in the region.

### new\_inheritance

[in scalar] The new inheritance attribute for the region. Valid values are:

VM\_INHERIT\_SHARE Allows child tasks to share the region.

VM\_INHERIT\_COPY Gives child tasks a copy of the region.

### VM\_INHERIT\_NONE Provides no access to the region for child tasks.

# NOTES

This interface is machine word length specific because of the virtual address parameter.

# **RETURN VALUE**

#### KERN\_INVALID\_ADDRESS

The address is illegal or specifies a non-allocated region.

# **RELATED INFORMATION**

Functions: task\_create, vm\_map, vm\_region, norma\_task\_clone.

# vm\_machine\_attribute

Function — Sets and gets special attributes of a memory region

### **SYNOPSIS**

kern_return_t <b>vm_machine_attribute</b>	
(mach_port_t	target_task,
vm_address_t	address,
vm_size_t	size,
vm_machine_attribute_t	attribute,
vm_machine_attribute_val_t*	value);

# DESCRIPTION

The **vm\_machine\_attribute** function gets and sets special attributes of the memory region implemented by the implementation's underlying **pmap** module. These attributes are properties such as cachability, migrability and replicability. The behavior of this function is machine dependent.

# SECURITY

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The requesting task must hold *tsv\_access\_machine\_attribute* permission to *target\_task*.

# PARAMETERS

target\_task

[in task port] The port for the task in whose address space the memory object is to be manipulated.

address

[in scalar] The starting address for the memory region. The granularity of rounding of this value to page boundaries is implementation dependent.

size

[in scalar] The number of bytes in the region. The granularity of rounding of this value to page boundaries is implementation dependent.

#### attribute

[in scalar] The name of the attribute to be get/set. Possible values are:

MATTR\_CACHE Cachability

# MATTR\_MIGRATE Migratability

### MATTR\_REPLICATE Replicability

value

[pointer to in/out scalar] The new value for the attribute. The old value is also returned in this variable.

MATTR\_VAL\_OFF (generic) turn attribute off

MATTR\_VAL\_ON (generic) turn attribute on

MATTR\_VAL\_GET (generic) return current value

MATTR\_VAL\_CACHE\_FLUSH flush from all caches

MATTR\_VAL\_DCACHE\_FLUSH flush from data caches

MATTR\_VAL\_ICACHE\_FLUSH flush from instruction caches

# NOTES

This interface is machine word length specific because of the virtual address parameter.

# **RETURN VALUE**

KERN\_INVALID\_ADDRESS The address is illegal or specifies a non-allocated region.

# **RELATED INFORMATION**

Functions: vm\_wire.

### vm\_map

Function — Maps a memory object to a task's address space

### SYNOPSIS

kern_return_t <b>vm_map</b>	
(mach_port_t	target_task,
vm_address_t*	address,
vm_size_t	size,
vm_address_t	mask,
boolean_t	anywhere,
mach_port_t	memory_object,
vm_offset_t	offset,
boolean_t	сору,
vm_prot_t	cur_protection,
vm_prot_t	max_protection,
vm_inherit_t	inheritance);

### DESCRIPTION

The **vm\_map** function maps a portion of the specified memory object into the virtual address space belonging to *target\_task*. The target task can be the calling task or another task, identified by its task kernel port.

The portion of the memory object mapped is determined by *offset* and *size*. The kernel maps *address* to the offset, so that an access to the memory starts at the offset in the object.

The *mask* parameter specifies additional alignment restrictions on the kernel's selection of the starting address. Uses for this mask include:

- Forcing the memory address alignment for a mapping to be the same as the alignment within the memory object.
- Quickly finding the beginning of an allocated region by performing bit arithmetic on an address known to be in the region.
- Emulating a larger virtual page size.

The *cur\_protection*, *max\_protection*, and *inheritance* parameters set the protection and inheritance attributes for the mapped object. As a rule, at least the maximum protection should be specified so that a server can make a restricted (for example, read-only) mapping in a client atomically. The current protection and inheritance parameters are provided for convenience so that the caller does not have to call **vm\_inherit** and **vm\_protect** separately.

The same memory object can be mapped in more than once and by more than one task. If an object is mapped by multiple tasks, the kernel maintains consistency for all the mappings if they use the same page alignment for *offset* and are on the same host. In this case, the virtual memory to which the object is mapped is shared by all the tasks. Changes made by one task in its address space are visible to all the other tasks.

# SECURITY

The requesting task must hold *tsv\_allocate\_vm\_region* permission to *target\_task* and *mosv\_map\_vm\_region* to the memory objects object port. *target\_task*'s access to the mapped memory is determined by its permission to *memory\_object*.

### PARAMETERS

target\_task

[in task port] The port for the task to whose address space the memory object is to be mapped.

address

[pointer to in/out scalar] The starting address for the mapped object. The mapped object will start at the beginning of the page containing *address*. If there is not enough room following the address, the kernel does not map the object. The kernel returns the starting address actually used for the mapped object.

size

[in scalar] The number of bytes to allocate for the object. The kernel rounds this number up to an integral number of virtual pages.

mask

[in scalar] Alignment restrictions for starting address. Bits turned on in the mask will not be turned on in the starting address.

anywhere

[in scalar] Placement indicator. If false, the kernel allocates the object's region starting at *address*. If true, the kernel allocates the region anywhere at or following *address* that there is enough space available within the address space. The kernel returns the starting address actually used in *address*.

memory\_object

[in abstract-memory-object port] The port naming the abstract memory object. If MEMORY\_OBJECT\_NULL is specified, the kernel allocates zero-filled memory, as with **vm\_allocate**.

offset

[in scalar] An offset within the memory object, in bytes. The kernel maps *address* to the specified offset.

copy

[in scalar] Copy indicator. If true, the kernel copies the region for the memory object to the specified task's address space. If false, the region is mapped read-write.

#### cur\_protection

[in scalar] The initial current protection for the region. Valid values are obtained by or'ing together the following values:

VM\_PROT\_READ Allows read access.

VM\_PROT\_WRITE Allows write access.

VM\_PROT\_EXECUTE Allows execute access.

#### *max\_protection*

[in scalar] The maximum protection for the region. Values are the same as for *cur\_protection*.

#### inheritance

[in scalar] The initial inheritance attribute for the region. Valid values are:

VM\_INHERIT\_SHARE Allows child tasks to share the region.

#### VM\_INHERIT\_COPY Gives child tasks a copy of the region.

VM\_INHERIT\_NONE

Provides no access to the region for child tasks.

# NOTES

**vm\_map** allocates a region in a task's address space and maps the specified memory object to this region. **vm\_allocate** allocates a zero-filled temporary region in a task's address space.

Before a memory object can be mapped, a port naming it must be acquired from the memory manager serving it.

This interface is machine word length specific because of the virtual address parameter.

# CAUTIONS

Do not attempt to map a memory object unless it has been provided by a memory manager that implements the memory object interface. If another type of port is specified, a thread that accesses the mapped virtual memory may become permanently hung or may receive a memory exception.

# **RETURN VALUE**

### KERN\_NO\_SPACE

There is not enough space in the task's address space to allocate the new region for the memory object.

# **RELATED INFORMATION**

Functions: memory\_object\_init, vm\_allocate.

# vm\_protect

Function — Sets access privileges for a region of virtual memory

### SYNOPSIS

kern_return_t vm_protect	
(mach_port_t	target_task,
vm_address_t	address,
vm_size_t	size,
boolean_t	set_maximum,
vm_prot_t	new_protection);

# DESCRIPTION

The **vm\_protect** function sets access privileges for a region within the specified task's address space. *new\_protection* specifies a combination of read, write, and execute accesses that are allowed (rather than prohibited).

The region starts at the beginning of the virtual page containing *address*; it ends at the end of the virtual page containing *address* + *size* - 1. Because of this rounding to virtual page boundaries, the amount of memory protected may be greater than *size*. Use **vm\_statistics** to find the current virtual page size.

The enforcement of virtual memory protection is machine-dependent. Nominally, read access requires VM\_PROT\_READ permission, write access requires VM\_PROT\_WRITE permission, and execute access requires VM\_PROT\_EXECUTE permission. However, some combinations of access rights may not be supported. In particular, the kernel interface allows write access to require VM\_PROT\_READ and VM\_PROT\_WRITE permission and execute access to require VM\_PROT\_READ permission.

# SECURITY

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The requesting task must hold *tsv\_chg\_vm\_region\_prot* permission to *target\_task*.

# PARAMETERS

target\_task

[in task port] The port for the task whose address space contains the region.

address

[in scalar] The starting address for the region.

size

[in scalar] The number of bytes in the region.

set\_maximum

[in scalar] Maximum/current indicator. If true, the new protection sets the maximum protection for the region. If false, the new protection sets the current protection for the region. If the maximum protection is set below the current protection, the current protection is reset to the new maximum.

new\_protection

[in scalar] The new protection for the region. Valid values are obtained by or'ing together the following values:

VM\_PROT\_READ Allows read access.

VM\_PROT\_WRITE Allows write access.

VM\_PROT\_EXECUTE Allows execute access.

### NOTES

This interface is machine word length specific because of the virtual address parameter.

# **RETURN VALUE**

#### KERN\_PROTECTION\_FAILURE

The new protection increased the current or maximum protection beyond the existing maximum protection.

KERN\_INVALID\_ADDRESS

The address is illegal or specifies a non-allocated region.

# **RELATED INFORMATION**

Functions: vm\_inherit, vm\_region.

# vm\_read

Function — Reads a task's virtual memory

### **SYNOPSIS**

kern_return_t vm_read	
(mach_port_t	target_task,
vm_address_t	address,
vm_size_t	size,
vm_offset_t*	data,
mach_msg_type_number_t*	data_count);

### DESCRIPTION

The **vm\_read** function reads a portion of a task's virtual memory. It allows one task to read another task's memory.

# SECURITY

The requesting task must hold *tsv\_read\_vm\_region* permission to *target\_task*.

In the current implementation the data read retains the security identifier assigned to the memory region from which the data came.

### PARAMETERS

#### target\_task

[in task port] The port for the task whose memory is to be read.

#### address

[in scalar] The address at which to start the read. This address must name a page boundary.

#### size

[in scalar] The number of bytes to read.

#### data

[out pointer to dynamic array of bytes] The array of data returned by the read.

#### data\_count

[out scalar] The number of bytes in the returned array. The count converts to an integral number of pages.

### NOTES

This interface is machine word length specific because of the virtual address parameter.

### **RETURN VALUE**

#### KERN\_NO\_SPACE

There is not enough room in the calling task's address space to allocate the region for the returned data.

#### KERN\_PROTECTION\_FAILURE

The specified region in the target task is protected against reading.

### KERN\_INVALID\_ADDRESS

The address is illegal or specifies a non-allocated region, or there are less than *size* bytes of data following the address.

### **RELATED INFORMATION**

Functions: vm\_copy, vm\_deallocate, vm\_write.

# vm\_region/vm\_region\_secure

Function — Returns information on a region of virtual memory

### **SYNOPSIS**

kern_return_t <b>vm_region</b>	
(mach_port_t	target_task,
vm_address_t*	address,
vm_size_t*	size,
vm_prot_t*	mach_protection,
vm_prot_t*	max_protection,
vm_inherit_t*	inheritance,
boolean_t*	shared,
mach_port_t*	object_name,
vm_offset_t*	offset);
kern_return_t <b>vm_region_secure</b>	
(mach_port_t	target_task,

#### vm\_address\_t\* address, vm\_size\_t\* size, vm\_prot\_t\* mach\_protection, vm\_prot\_t\* protection, vm\_prot\_t\* *max\_protection*, vm\_inherit\_t\* inheritance, boolean\_t\* shared, mach\_port\_t\* object\_name, vm\_offset\_t\* offset, security\_id\_t\* obj\_sid, mach\_access\_vector\_t av);

#### **DESCRIPTION**

The **vm\_region** and **vm\_region\_secure** functions return information on a region within the specified task's address space. **vm\_region\_secure** also returns *protection*, which incorporates *mach\_protection* and the memory protections from the access vector; *obj\_sid*, the security identifier for the region containing the address; and *av*, *target\_task*'s access vector for the region.

The function begins looking at *address* and continues until it finds an allocated region. If the input address is within a region, the function uses the start of that region. The starting address for the located region is returned in *address*.

# SECURITY

The requesting task must hold *tsv\_get\_vm\_region\_info* permission to *target\_task*.

### PARAMETERS

#### target\_task

[in task port] The port for the task whose address space contains the region.

#### address

[pointer to in/out scalar] The address at which to start looking for a region. The function returns the starting address actually used.

#### size

[out scalar] The number of bytes in the located region. The number converts to an integral number of virtual pages.

#### mach\_protection

[out scalar] The current Mach protection for the region (i.e., the original *protection* value).

#### protection

[out scalar] The current protection for the region which incorporates *mach\_protection* and the memory protections from the access vector, *av*.

#### max\_protection

[out scalar] The maximum protection allowed for the region.

#### inheritance

[out scalar] The inheritance attribute for the region.

#### shared

[out scalar] Shared indicator. If true, the region is shared by another task. If false, the region is not shared.

#### object\_name

[out memory-cache-name port] The name of a send right to the name port for the memory object associated with the region. See **memory\_object\_init**.

#### offset

[out scalar] The region's offset into the memory object. The region begins at this offset.

#### obj\_sid

[out security id] The security identifier for the memory object associated with the memory region containing *address*.

#### av

[out access vector] The access vector indicating *target\_task*'s allowed access to the region containing *address*.

# NOTES

This interface is machine word length specific because of the virtual address parameter.

# **RETURN VALUE**

#### KERN\_NO\_SPACE

There is no region at or beyond the specified starting address.

# **RELATED INFORMATION**

Functions: **vm\_allocate**, **vm\_deallocate**, **vm\_inherit**, **vm\_protect**, **memory\_object\_init**, et al.

# vm\_statistics

Function — Returns statistics on the kernel's use of virtual memory

### **SYNOPSIS**

kern\_return\_t **vm\_statistics** (mach\_port\_t vm statistics data t\*

target\_task,
 vm\_stats);

#### DESCRIPTION

The **vm\_statistics** function returns statistics on the kernel's use of virtual memory from the time the kernel was booted.

See vm\_statistics for a description of the structure used.

For related information for a specific task, use task\_info.

### SECURITY

The requesting task must hold tsv\_get\_vm\_statistics permission to target\_task.

### PARAMETERS

target\_task

[in task port] The task that is requesting the statistics.

vm\_stats

[out structure] The structure in which the statistics will be returned.

### NOTES

This interface is machine word length specific because of the virtual address parameter.

#### **RETURN VALUE**

Only generic errors apply.

### **RELATED INFORMATION**

Functions: task\_info.

Data Structures: vm\_statistics.

# vm\_wire

Function — Specifies the pageability of a region of virtual memory

#### LIBRARY

#include <mach/mach\_host.h>

### **SYNOPSIS**

kern\_return\_t vm\_wire

(mach\_port\_t mach\_port\_t vm\_address\_t vm\_size\_t vm\_prot\_t host\_priv, target\_task, address, size, wired\_access);

### DESCRIPTION

The **vm\_wire** function sets the pageability privileges for a region within the specified task's address space. *wired\_access* specifies the types of accesses to the memory region which must not suffer from (internal) faults of any kind after this call returns. A page is wired into physical memory if any task accessing it has a non-null *wired\_access* value for the page.

The region starts at the beginning of the virtual page containing *address*; it ends at the end of the virtual page containing *address* + *size* - 1. Because of this rounding to virtual page boundaries, the amount of memory affected may be greater than *size*. Use **vm\_statistics** to find the current virtual page size.

### SECURITY

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The requesting task must hold *tsv\_wire\_vm\_for\_task* permission to *target\_task* and *hpsv\_wire\_vm* permission to *host\_priv*.

### PARAMETERS

host\_priv

[in host-control port] The host control port for the host on which *target\_task* executes.

#### target\_task

[in task port] The port for the task whose address space contains the region.

#### address

[in scalar] The starting address for the region.

size

[in scalar] The number of bytes in the region.

#### wired\_access

[in scalar] The pageability of the region. Valid values are:

#### VM\_PROT\_NONE Un-wire (allow to be paged) the region of memory.

Any other value specifies that the region is to be wired and that the target task must have at least the specified amount of access to the region.

### NOTES

This call requires the privileged host port on which *target\_task* executes because of the privileged nature of committing physical memory.

This interface is machine word length specific because of the virtual address parameter.

### **RETURN VALUE**

KERN\_INVALID\_HOST The privileged host port was not specified

KERN\_INVALID\_ADDRESS The address is illegal or specifies a non-allocated region.

# **RELATED INFORMATION**

Functions: thread\_wire.

# vm\_write

Function — Writes data to a task's virtual memory

### **SYNOPSIS**

kern_return_t <b>vm_write</b>	
(mach_port_t	target_task,
vm_address_t	address,
vm_offset_t	data,
mach_msg_type_number_t	data_count);

#### DESCRIPTION

The **vm\_write** function writes an array of data to a task's virtual memory. It allows one task to write to another task's memory.

The result of **vm\_write** is as if *target\_task* had directly written into the set of pages. Hence, *target\_task* must have write permission to the pages.

# SECURITY

The requesting task must hold *tsv\_write\_vm\_region* permission to *target\_task*.

The SID of memory region to which the data is written, is unaffected. If the write results in creation of a new memory region, the SID assigned to that region will be the default memory object sid for the *target\_task*.

### PARAMETERS

target\_task

[in task port] The port for the task whose memory is to be written.

address

[in scalar] The address at which to start the write. The starting address must be on a page boundary.

data

[in pointer to page aligned array of bytes] An array of data to be written.

#### data\_count

[in scalar] The number of bytes in the array. The size of the array must convert to an integral number of pages.

### NOTES

This interface is machine word length specific because of the virtual address parameter.

#### **RETURN VALUE**

#### KERN\_PROTECTION\_FAILURE The specified region in the target task is protected against writing.

#### KERN\_INVALID\_ADDRESS

The address is illegal or specifies a non-allocated region, or there are less than *data\_count* bytes available following the address.

### **RELATED INFORMATION**

Functions: vm\_copy, vm\_protect, vm\_read, vm\_statistics.

# CHAPTER 5 External Memory Management Interface

This chapter discusses the specifics of the kernel's external memory management interfaces. Interfaces that relate to the basic use of virtual memory for a task appear in the previous chapter.

# memory\_object\_change\_attributes

Function — Changes various performance related attributes

#### **SYNOPSIS**

kern\_return\_t memory\_object\_change\_attributes

(mach\_port\_tmemory\_control,boolean\_tmay\_cache\_object,memory\_object\_copy\_strategy\_tcopy\_strategy,mach\_port\_treply\_port);

### DESCRIPTION

The **memory\_object\_change\_attributes** function sets various performance-related attributes for the specified memory object, so as to:

- Retain data from a memory object even after all address space mappings have been de-allocated (*may\_cache\_object* parameter).
- Perform optimizations for virtual memory copy operations (*copy\_strategy* parameter).

### SECURITY

The requesting task must hold *mcsv\_set\_attributes* permission to *memory\_control*.

### PARAMETERS

#### memory\_control

[in memory-cache-control port] The memory cache control port to be used by the memory manager for cache management requests. This port is provided by the kernel in a **memory\_object\_init** call.

#### may\_cache\_object

[in scalar] Cache indicator. If true, the kernel can cache data associated with the memory object, even if virtual memory references to it are removed.

#### copy\_strategy

[in scalar] How the kernel should handle copying of regions associated with the memory object. Valid values are:

#### MEMORY\_OBJECT\_COPY\_NONE

Use normal procedure when copying the memory object's data. Normally, the kernel requests each page with read access, copies the data, and then (optionally) flushes the data.

#### MEMORY\_OBJECT\_COPY\_CALL

Notify the memory manager (via **memory\_object\_copy**) before copying any data.

### MEMORY\_OBJECT\_COPY\_DELAY

Use copy-on-write technique. This strategy allows the kernel to efficiently copy large amounts of data and guarantees that the memory manager will not externally modify the data. It is the most commonly used copy strategy.

#### MEMORY\_OBJECT\_COPY\_TEMPORARY

Mark the object as temporary. This has the same effect as the MEMORY\_OBJECT\_COPY\_DELAY strategy and has the additional attribute that when the last mapping of the memory object is removed, the object is destroyed without returning any in-memory pages.

#### reply\_port

[in reply port] A port to which a reply (**memory\_object\_change\_completed**) is to be sent indicating the completion of the attribute change. Such a reply would be useful if the cache attribute is turned off, since such a change, if the memory object is no longer mapped, may result in the object being terminated, or if the copy strategy is changed, which may result in additional page requests.

#### NOTES

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Sharing cached data among all the clients of a memory object can have a major impact on performance, especially if it can be extended across successive, as well as concurrent, uses. For example, the memory objects that represent program images can be used regularly by different programs. By retaining the data for these memory objects in cache, the number of secondary storage accesses can be reduced significantly.

#### **RETURN VALUE**

Since this function does not receive a reply message, it has no return value. Only message transmission errors apply.

#### **RELATED INFORMATION**

Functions:memory\_object\_change\_completed,<br/>memory\_object\_get\_attributes,memory\_object\_copy,<br/>memory\_object\_ready,memory\_object\_set\_attributes (old form).memory\_object\_ready,

### memory\_object\_change\_completed

Server Interface — Indicates completion of an attribute change call

#### LIBRARY

Not declared anywhere.

### **SYNOPSIS**

kern\_return\_t **memory\_object\_change\_completed** (mach\_port\_t boolean\_t memory\_object\_copy\_strategy\_t

reply\_port, may\_cache\_object, copy\_strategy);

#### seqnos\_memory\_object\_change\_completed Sequence Number form

kern\_return\_t **seqnos\_memory\_object\_change\_completed** (mach\_port\_t mach\_port\_seqno\_t boolean\_t memory\_object\_copy\_strategy\_t *cop* 

reply\_port, seqno, may\_cache\_object, copy\_strategy);

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#### DESCRIPTION

A **memory\_object\_change\_completed** function is called as the result of a kernel message confirming the kernel's action in response to a **memory\_object\_change\_attributes** call from the memory manager.

When the kernel completes the requested changes, it calls **memory\_object\_change\_completed** (asynchronously) using the port explicitly provided in the **memory\_object\_change\_attributes** call. A response is generated so that the manager can synchronize with changes to the copy strategy (which affects the manner in which pages will be requested) and a termination message possibly resulting from un-cacheing a not-mapped object.

### PARAMETERS

#### reply\_port

[in reply port] The port named in the corresponding **memory\_object\_change\_attributes** call.

seqno

[in scalar] The sequence number of this message relative to the port named in the **memory\_object\_change\_attributes** call.

may\_cache\_object

[in scalar] The new cache attribute.

copy\_strategy

[in scalar] The new copy strategy.

# NOTES

No memory cache control port is supplied in this call because the attribute change may cause termination of the object leading to what would be an invalid cache port.

### **RETURN VALUE**

Irrelevant.

# **RELATED INFORMATION**

Functions: memory\_object\_change\_attributes, memory\_object\_server, seqnos\_memory\_object\_server.

### memory\_object\_copy

Server Interface — Indicates that a memory object has been copied

#### LIBRARY

Not declared anywhere.

### **SYNOPSIS**

kern\_return\_t memory\_object\_copy

(mach\_port\_t memory\_object\_control\_t vm\_offset\_t vm\_size\_t mach\_port\_t old\_memory\_object, old\_memory\_control, offset, length, new\_memory\_object);

#### seqnos\_memory\_object\_copy Sequence Number form

kern\_return\_t seqnos\_memory\_object\_copy

(mach\_port\_t
mach\_port\_seqno\_t
memory\_object\_control\_t
vm\_offset\_t
vm\_size\_t
mach\_port\_t

old\_memory\_object, seqno, old\_memory\_control, offset, length, new\_memory\_object);

### DESCRIPTION

A **memory\_object\_copy** function is called as the result of a message from the kernel indicating that the kernel has copied the specified region within the old memory object.

This call includes only the new abstract memory object port itself. The kernel will subsequently issue a **memory\_object\_init** call on the new abstract memory object after it has prepared the currently cached pages of the old object. When the memory manager receives the **memory\_object\_init** call, it is expected to reply with the **memory\_object\_ready** call. The kernel uses the new abstract memory object, memory cache control, and memory cache name ports to refer to the new copy.

The kernel makes the memory\_object\_copy call only if:

- The memory manager had previously set the old object's copy strategy attribute to MEMORY\_OBJECT\_COPY\_CALL (using memory\_object\_change\_attributes or memory\_object\_ready).
- A user of the old object has asked the kernel to copy it.

Cached pages from the old memory object at the time of the copy are handled as follows:

- Readable pages may be copied to the new object without notification and with all access permissions.
- Pages not copied are locked to prevent write access.

The memory manager should treat the new memory object as temporary. In other words, the memory manager should not change the new object's contents or allow it to be mapped in another client. The memory manager can use the **memory\_object\_data\_unavailable** call to indicate that the appropriate pages of the old object can be used to fulfill a data request.

### PARAMETERS

old\_memory\_object

[in abstract-memory-object port] The port that represents the old (copied from) abstract memory object.

seqno

[in scalar] The sequence number of this message relative to the abstract memory object port.

old\_memory\_control

[in memory-cache-control port] The kernel memory cache control port for the old memory object.

#### offset

[in scalar] The offset within the old memory object.

#### length

[in scalar] The number of bytes copied, starting at *offset*. The number converts to an integral number of virtual pages.

#### new\_memory\_object

[in abstract-memory-object port] The new abstract memory object created by the kernel. The kernel provides all port rights (including the receive right) for the new memory object.

### NOTES

It is possible for a memory manager to receive a **memory\_object\_data\_return** message for a page of the new memory object before receiving any other requests for that data.

### **RETURN VALUE**

Any return value other than KERN\_SUCCESS or MIG\_NO\_REPLY will cause **mach\_msg\_server** to remove the old and new memory cache control port references.

# **RELATED INFORMATION**

Functions: memory\_object\_data\_unavailable, memory\_object\_ready, seqnos\_memory\_object\_server. memory\_object\_change\_attributes, memory\_object\_init, memory\_object\_server,

# memory\_object\_data\_error

Function — Indicates no data for a memory object

1. . . .

#### **SYNOPSIS**

kern_return_t memory_object_data_error	
(mach_port_t	memory_control,
vm_offset_t	offset,
vm_size_t	size,
kern_return_t	reason);

#### DESCRIPTION

The **memory\_object\_data\_error** function indicates that the memory manager cannot provide the kernel with the data requested for the given region, specifying a reason for the error.

When the kernel issues a **memory\_object\_data\_request** call, the memory manager can respond with a **memory\_object\_data\_error** call to indicate that the page cannot be retrieved, and that a memory failure exception should be raised in any client threads that are waiting for the page. Clients are permitted to catch these exceptions and retry their page faults. As a result, this call can be used to report transient errors as well as permanent ones. A memory manager can use this call for both hardware errors (for example, disk failures) and software errors (for example, accessing data that does not exist or is protected).

### SECURITY

The requesting task must hold *mcsv\_provide\_data* permission to *memory\_control*.

#### PARAMETERS

memory\_control

[in memory-cache-control port] The memory cache control port to be used by the memory manager for cache management requests. This port is provided by the kernel in a **memory\_object\_init** call.

offset

[in scalar] The offset within the memory object, in bytes.

size

[in scalar] The number of bytes of data (starting at *offset*). The number must convert to an integral number of memory object pages.

reason

[in scalar] Reason for the error. The value could be a POSIX error code for a hardware error.

#### NOTES

If *reason* has a system code of err\_kern, the kernel will substitute an error value of KERN\_MEMORY\_ERROR.

### **RETURN VALUE**

Since this function does not receive a reply message, it has no return value. Only message transmission errors apply.

### **RELATED INFORMATION**

Functions: **memory\_object\_data\_request**, **memory\_object\_data\_supply**, **memory\_object\_data\_unavailable**.

# memory\_object\_data\_provided

Function — Supplies data for a region of a memory object (old form)

### **SYNOPSIS**

kern_return_t memory_object_da	ata_provi	ided
--------------------------------	-----------	------

memory_control,
offset,
data,
data_count,
lock_value);

### DESCRIPTION

The **memory\_object\_data\_provided** function supplies the kernel with a range of data for the specified memory object. A memory manager can only provide data that was requested by a **memory\_object\_data\_request** call from the kernel.

# SECURITY

The requesting task must hold *mcsv\_provide\_data* permission to *memory\_control*.

### PARAMETERS

```
memory_control
```

[in memory-cache-control port] The memory cache control port to be used by the memory manager for cache management requests. This port is provided by the kernel in a **memory\_object\_init** call.

offset

[in scalar] The offset within the memory object, in bytes.

### data

[pointer to page aligned in array of bytes] The address of the data being provided to the kernel.

#### data\_count

[in scalar] The amount of data to be provided. The number must be an integral number of memory object pages.

#### lock\_value

[in scalar] One or more forms of access **not** permitted for the specified data. Valid values are:

VM\_PROT\_NONE Prohibits no access (that is, all forms of access are permitted).

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VM\_PROT\_READ Prohibits read access.

VM\_PROT\_WRITE Prohibits write access.

VM\_PROT\_EXECUTE Prohibits execute access.

VM\_PROT\_ALL Prohibits all forms of access.

#### NOTES

The kernel accepts only integral numbers of pages. It discards any partial pages without notification.

memory_object_data_provided	is	the	old	form	of
memory_object_data_supply.					

### CAUTIONS

A memory manager must be careful that it not attempt to provide data that has not been explicitly requested. In particular, a memory manager must ensure that it does not provide writable data again before it receives back modifications from the kernel. This may require that the memory manager remember which pages it has provided, or that it exercise other cache control functions (via **memory\_object\_lock\_request**) before proceeding. The kernel prohibits the overwriting of live data pages and will not accept pages it has not requested

### **RETURN VALUE**

Since this function does not receive a reply message, it has no return value. Only message transmission errors apply.

### **RELATED INFORMATION**

Functions: memory\_object\_data\_error, memory\_object\_data\_request, memory\_object\_data\_supply, memory\_object\_data\_unavailable, memory\_object\_lock\_request.

# memory\_object\_data\_request

Server Interface — Requests data from a memory object

#### LIBRARY

Not declared anywhere.

### **SYNOPSIS**

kern\_return\_t memory\_object\_data\_request

(mach\_port\_tmemory\_object,mach\_port\_tmemory\_control,vm\_offset\_toffset,vm\_size\_tlength,vm\_prot\_tdesired\_access);

# seqnos\_memory\_object\_data\_request

Sequence Number form

kern\_return\_t seqnos\_memory\_object\_data\_request

(mach\_port\_t
mach\_port\_seqno\_t
mach\_port\_t
vm\_offset\_t
vm\_size\_t
vm\_prot\_t

memory\_object, seqno, memory\_control, offset, length, desired\_access);

#### DESCRIPTION

A **memory\_object\_data\_request** function is called as the result of a kernel message requesting data from the specified memory object, for at least the access specified.

The kernel issues this call after a cache miss (that is, a page fault for which the kernel does not have the data). The kernel requests only amounts of data that are multiples of the page size included in the **memory\_object\_init** call.

The memory manager is expected to use **memory\_object\_data\_supply** to return at least the specified data, with as much access as it can allow. If the memory manager cannot provide the data (for example, because of a hardware error), it can use the **memory\_object\_data\_error** call. The memory manager can also use **memory\_object\_data\_unavailable** to tell the kernel to supply zero-filled memory for the region.

### PARAMETERS

#### memory\_object

[in abstract-memory-object port] The abstract memory object port that represents the memory object data, as supplied to the kernel in a **vm\_map** call.

seqno

[in scalar] The sequence number of this message relative to the abstract memory object port.

#### memory\_control

[in memory-cache-control port] The memory cache control port to be used for a response by the memory manager. If the memory object has been supplied to more than one kernel, this parameter identifies the kernel that is making the call.

offset

[in scalar] The offset within the memory object.

#### length

[in scalar] The number of bytes requested, starting at *offset*. The number converts to an integral number of virtual pages.

#### desired\_access

[in scalar] The memory access modes to be allowed for the cached data. Possible values are obtained by or'ing together the following values:

# VM\_PROT\_READ

Allows read access.

#### VM\_PROT\_WRITE Allows write access.

VM\_PROT\_EXECUTE

Allows execute access.

### **RETURN VALUE**

Any return value other than KERN\_SUCCESS or MIG\_NO\_REPLY will cause **mach\_msg\_server** to remove the memory cache control port reference.

### **RELATED INFORMATION**

Functions:memory\_object\_data\_error,memory\_object\_data\_supply,memory\_object\_data\_unavailable,memory\_object\_server,seqnos\_memory\_object\_server.memory\_object\_server,

# memory\_object\_data\_return

Server Interface — Writes data back to a memory object

### LIBRARY

Not declared anywhere.

### **SYNOPSIS**

kern_return_t memory_object_data_return	
(mach_port_t	memory_object,
mach_port_t	memory_control,
vm_offset_t	offset,
vm_offset_t	data,
vm_size_t	data_count,
boolean_t	dirty,
boolean_t	kernel_copy);

#### seqnos\_memory\_object\_data\_return

Sequence Number form

kern\_return\_t seqnos\_memory\_object\_data\_return

(mach_port_t	memory_object,
mach_port_seqno_t	seqno,
mach_port_t	memory_control,
vm_offset_t	offset,
vm_offset_t	data,
vm_size_t	data_count,
boolean_t	dirty,
boolean_t	kernel_copy);

#### DESCRIPTION

A **memory\_object\_data\_return** function is called as the result of a kernel message providing the memory manager with data that has been evicted from the physical memory cache.

The kernel writes back only data that has been modified or is precious. When the memory manager no longer needs the data (for example, after the data has been written to permanent storage), it should use **vm\_deallocate** to release the memory resources.

# PARAMETERS

memory\_object

[in abstract-memory-object port] The abstract memory object port that represents the memory object data, as supplied to the kernel in a **vm\_map** call.

#### seqno

[in scalar] The sequence number of this message relative to the abstract memory object port.

#### memory\_control

[in memory-cache-control port] The memory cache control port to be used for a response by the memory manager. If the memory object has been supplied to more than one kernel, this parameter identifies the kernel that is making the call.

#### offset

[in scalar] The offset within the memory object.

#### data

[in pointer to dynamic array of bytes] The data that has been evicted from the physical memory cache.

#### data\_count

[in scalar] The number of bytes to be written, starting at *offset*. The number converts to an integral number of memory object pages.

#### dirty

[in scalar] If TRUE, the pages returned have been modified.

#### kernel\_copy

[in scalar] If TRUE, the kernel has kept a copy of the page.

#### NOTES

The kernel can flush clean (that is, un-modified) non-precious pages at its own discretion. As a result, the memory manager cannot rely on the kernel to keep a copy of its data or even to provide notification that its data has been discarded.

### **RETURN VALUE**

Any return value other than KERN\_SUCCESS or MIG\_NO\_REPLY will cause **mach\_msg\_server** to remove the memory cache control port reference and to de-allocate the returned data.

#### **RELATED INFORMATION**

Functions:memory\_object\_data\_supply,memory\_object\_data\_write(oldform),vm\_deallocate,memory\_object\_server,seqnos\_memory\_object\_server.seqnos\_memory\_object\_server,

# memory\_object\_data\_supply

Function — Supplies data for a region of a memory object

#### **SYNOPSIS**

kern_return_t memory_object_data_supply	
(mach_port_t	memory_control,
vm_offset_t	offset,
vm_offset_t	data,
mach_msg_type_number_t	data_count,
boolean_t	deallocate,
vm_prot_t	lock_value,
boolean_t	precious,
mach_port_t	reply_port);

### DESCRIPTION

The **memory\_object\_data\_supply** function supplies the kernel with a range of data for the specified memory object. A memory manager can only provide data that was requested by a **memory\_object\_data\_request** call from the kernel.

# SECURITY

The requesting task must hold *mcsv\_change\_page\_locks*, *mcsv\_make\_page\_precious* and *mcsv\_provide\_data* permission to *memory\_control*.

### PARAMETERS

```
memory_control
```

[in memory-cache-control port] The memory cache control port to be used by the memory manager for cache management requests. This port is provided by the kernel in a **memory\_object\_init** call.

 $o\!f\!fset$ 

[in scalar] The offset within the memory object, in bytes.

data

[pointer to page aligned in array of bytes] The address of the data being provided to the kernel.

#### data\_count

[in scalar] The amount of data to be provided. The number must be an integral number of memory object pages.

#### deallocate

[in scalar] If TRUE, the pages to be copied (starting at *data*) will be deallocated from the memory manager's address space as a result of being copied into the message, allowing the pages to be moved into the kernel instead of being physically copied.

#### lock\_value

[in scalar] One or more forms of access **not** permitted for the specified data. Valid values are:

#### VM\_PROT\_NONE

Prohibits no access (that is, all forms of access are permitted).

I

#### VM\_PROT\_READ

Prohibits read access.

#### VM\_PROT\_WRITE Prohibits write access.

#### VM\_PROT\_EXECUTE Prohibits execute access.

#### VM\_PROT\_ALL Prohibits all forms of access.

precious

[in scalar] If TRUE, the pages being supplied are "precious", that is, the memory manager is not (necessarily) retaining its own copy. These pages must be returned to the manager when evicted from memory, even if not modified.

#### reply\_port

[in reply port] A port to which the kernel should send a **memory\_object\_supply\_completed** to indicate the status of the accepted data. MACH\_PORT\_NULL is allowed. The reply message indicates which pages have been accepted.

#### NOTES

The kernel accepts only integral numbers of pages. It discards any partial pages without notification.

#### CAUTIONS

A memory manager must be careful that it not attempt to provide data that has not been explicitly requested. In particular, a memory manager must ensure that it does not provide writable data again before it receives back modifications from the kernel. This may require that the memory manager remember which pages it has provided, or that it exercise other cache control functions (via **memory\_object\_lock\_request**) before proceeding. The kernel prohibits the overwriting of live data pages and will not accept pages it has not requested

### **RETURN VALUE**

Since this function does not receive a reply message, it has no return value. Only message transmission errors apply.

### **RELATED INFORMATION**

Functions: memory\_object\_data\_error, memory\_object\_data\_provided (old form), memory\_object\_data\_request, memory\_object\_data\_unavailable, memory\_object\_lock\_request, memory\_object\_supply\_completed.

# memory\_object\_data\_unavailable

Function — Indicates no data for a memory object

#### **SYNOPSIS**

kern\_return\_t **memory\_object\_data\_unavailable** (mach\_port\_t vm\_offset\_t vm\_size\_t

memory\_control, offset, size);

### **DESCRIPTION**

The **memory\_object\_data\_unavailable** function indicates that the memory manager cannot provide the kernel with the data requested for the given region. Instead, the kernel should provide the data for this region.

A memory manager can use this call in any of the following situations:

- When the object was created by the kernel (via memory\_object\_create) and the kernel has not yet provided data for the region (via either memory\_object\_data\_initialize or memory\_object\_data\_return). In this case, the object is a temporary memory object; the memory manager is the default memory manager; and the kernel should provide zero-filled pages for the object.
- When the object was created by a **memory\_object\_copy**. In this case, the kernel should copy the region from the original memory object.
- When the object is a normal user-created memory object. In this case, the kernel should provide unlocked zero-filled pages for the region.

### SECURITY

The requesting task must hold *mcsv\_provide\_data* permission to *memory\_control*.

#### PARAMETERS

#### memory\_control

[in memory-cache-control port] The memory cache control port to be used by the memory manager for cache management requests. This port is provided by the kernel in a **memory\_object\_init** or a **memory\_object\_create** call.

offset

[in scalar] The offset within the memory object, in bytes.

size

[in scalar] The number of bytes of data (starting at *offset*). The number must convert to an integral number of memory object pages.

# **RETURN VALUE**

Since this function does not receive a reply message, it has no return value. Only message transmission errors apply.

### **RELATED INFORMATION**

Functions:memory\_object\_copy,memory\_object\_create,memory\_object\_data\_error,memory\_object\_data\_request,memory\_object\_data\_supply.memory\_object\_data\_request,

# memory\_object\_data\_unlock

Server Interface — Requests access to a memory object

#### LIBRARY

Not declared anywhere.

### **SYNOPSIS**

kern_return_	_t memory_	_object_	_data_	_unloc	K
--------------	------------	----------	--------	--------	---

memory_object,	(mach_port_t
memory_control,	mach_port_t
offset,	vm_offset_t
length,	vm_size_t
desired_access);	vm_prot_t

#### seqnos\_memory\_object\_data\_unlock Sequence Number form

kern\_return\_t seqnos\_memory\_object\_data\_unlock

(mach\_port\_t mach\_port\_seqno\_t mach\_port\_t vm\_offset\_t vm\_size\_t vm\_prot\_t memory\_object, seqno, memory\_control, offset, length, desired\_access);

### DESCRIPTION

A **memory\_object\_data\_unlock** function is called as the result of a kernel message requesting the memory manager to permit at least the desired access to the specified data cached by the kernel. The memory manager is expected to use the **memory\_object\_lock\_request** call in response.

### PARAMETERS

memory\_object

[in abstract-memory-object port] The abstract memory object port that represents the memory object data, as supplied to the kernel in a **vm\_map** call.

seqno

[in scalar] The sequence number of this message relative to the abstract memory object port.

memory\_control

[in memory-cache-control port] The memory cache control port to be used for a response by the memory manager. If the memory object has been supplied to more than one kernel, this parameter identifies the kernel that is making the call.

#### offset

[in scalar] The offset within the memory object.

#### length

[in scalar] The number of bytes to which the access applies, starting at *offset*. The number converts to an integral number of memory object pages.

#### desired\_access

[in scalar] The memory access modes requested for the cached data. Possible values are obtained by or'ing together the following values:

VM\_PROT\_READ Allows read access.

VM\_PROT\_WRITE Allows write access.

VM\_PROT\_EXECUTE Allows execute access.

### **RETURN VALUE**

Any return value other than KERN\_SUCCESS or MIG\_NO\_REPLY will cause **mach\_msg\_server** to remove the memory cache control port reference.

### **RELATED INFORMATION**

Functions: memory\_object\_lock\_completed, memory\_object\_lock\_request, memory\_object\_server, seqnos\_memory\_object\_server.

**Mach 3 Kernel Interfaces** 

# memory\_object\_data\_write

Server Interface — Writes changed data back to a memory object (old form)

#### **LIBRARY**

Not declared anywhere.

### **SYNOPSIS**

kern_return_t memory_object_data_write	
(mach_port_t	memory_object,
mach_port_t	memory_control,
vm_offset_t	offset,
vm offset t	data.

#### seqnos\_memory\_object\_data\_write Sequence Number form

vm size t

kern\_return\_t seqnos\_memory\_object\_data\_write

 $(mach_port_t$ mach\_port\_seqno\_t mach\_port\_t vm\_offset\_t vm\_offset\_t vm\_size\_t

memory\_object, seqno, memory\_control, offset, data, data\_count);

data\_count);

### DESCRIPTION

A memory\_object\_data\_write function is called as the result of a kernel message providing the memory manager with data that has been modified while cached in physical memory. This old form is used if the memory manager makes the object ready via the old memory\_object\_set\_attributes instead of memory\_object\_ready.

The kernel writes back only data that has been modified. When the memory manager no longer needs the data (for example, after the data has been written to permanent storage), it should use vm\_deallocate to release the memory resources.

#### PARAMETERS

memory object

[in abstract-memory-object port] The abstract memory object port that represents the memory object data, as supplied to the kernel in a vm\_map call.

#### seqno

[in scalar] The sequence number of this message relative to the abstract memory object port.

#### memory\_control

[in memory-cache-control port] The memory cache control port to be used for a response by the memory manager. If the memory object has been supplied to more than one kernel, this parameter identifies the kernel that is making the call.

#### offset

[in scalar] The offset within the memory object.

#### data

[in pointer to dynamic array of bytes] The data that has been modified while cached in physical memory.

#### data\_count

[in scalar] The number of bytes to be written, starting at *offset*. The number converts to an integral number of memory object pages.

### NOTES

The kernel can flush clean (that is, un-modified) pages at its own discretion. As a result, the memory manager cannot rely on the kernel to keep a copy of its data or even to provide notification that its data has been discarded.

### **RETURN VALUE**

Any return value other than KERN\_SUCCESS or MIG\_NO\_REPLY will cause **mach\_msg\_server** to remove the memory cache control port reference and to de-allocate the returned data.

### **RELATED INFORMATION**

Functions: memory\_object\_data\_return, memory\_object\_set\_attributes, vm\_deallocate, memory\_object\_server, seqnos\_memory\_object\_server.

### memory\_object\_destroy

Function — Shuts down a memory object

#### **SYNOPSIS**

kern\_return\_t **memory\_object\_destroy** (mach\_port\_t kern return t

memory\_control, reason);

#### DESCRIPTION

The **memory\_object\_destroy** function tells the kernel to shut down the specified memory object. As a result of this call, the kernel no longer supports paging activity or any memory object calls on the memory object. The kernel issues a **memory\_object\_terminate** call to pass to the memory manager all rights to the memory object port, the memory control port, and the memory name port.

To ensure that any modified cached data is returned before the object is terminated, the memory manager should call **memory\_object\_lock\_request** with *should\_flush* set and a lock value of VM\_PROT\_WRITE before it makes the **memory\_object\_destroy** call.

### SECURITY

The requesting task must hold *mcsv\_destroy\_object* permission to *memory\_control*.

#### PARAMETERS

memory\_control

[in memory-cache-control port] The memory cache control port to be used by the memory manager for cache management requests. This port is provided by the kernel in a **memory\_object\_init** call.

reason

[in scalar] An error code indicating when the object must be destroyed.

#### NOTES

The reason code is currently ignored by the kernel.

### **RETURN VALUE**

Since this function does not receive a reply message, it has no return value. Only message transmission errors apply.

# **RELATED INFORMATION**

Functions: memory\_object\_lock\_request, memory\_object\_terminate.

# memory\_object\_get\_attributes

Function — Returns current attributes for a memory object

#### **SYNOPSIS**

kern\_return\_t memory\_object\_get\_attributes

(mach_port_t	memory_control,
boolean_t*	object_ready,
boolean_t*	may_cache_object,
memory_object_copy_strategy_t*	copy_strategy);

## DESCRIPTION

The **memory\_object\_get\_attributes** function retrieves the current attributes for the specified memory object.

## SECURITY

The requesting task must hold *mcsv\_get\_attributes* permission to *memory\_control.* 

## PARAMETERS

memory\_control

[in memory-cache-control port] The memory cache control port to be used by the memory manager for cache management requests. This port is provided by the kernel in a **memory\_object\_init** call.

#### object\_ready

[out scalar] Ready indicator. If true, the kernel can issue new data and unlock requests on the memory object.

#### may\_cache\_object

[out scalar] Cache indicator. If true, the kernel can cache data associated with the memory object, even if virtual memory references to it are removed.

#### copy\_strategy

[out scalar] How the kernel should handle copying of regions associated with the memory object. Possible values are:

#### MEMORY\_OBJECT\_COPY\_NONE

Use normal procedure when copying the memory object's data. Normally, the kernel requests each page with read access, copies the data, and then (optionally) flushes the data.

#### MEMORY\_OBJECT\_COPY\_CALL

Notify the memory manager (via **memory\_object\_copy**) before copying any data.

#### MEMORY\_OBJECT\_COPY\_DELAY

Use copy-on-write technique. This strategy allows the kernel to efficiently copy large amounts of data and guarantees that the memory manager will not externally modify the data. It is the most commonly used copy strategy.

#### MEMORY\_OBJECT\_COPY\_TEMPORARY

Mark the object as temporary. This had the same effect as the MEMORY\_OBJECT\_COPY\_DELAY strategy and has the additional attribute that when the last mapping of the memory object is removed, the object is destroyed without returning any in-memory pages.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: memory\_object\_change\_attributes, memory\_object\_copy, memory\_object\_ready.

## memory\_object\_init

Server Interface — Initializes a memory object

#### LIBRARY

Not declared anywhere.

## **SYNOPSIS**

kern\_return\_t memory\_object\_init

(mach\_port\_t mach\_port\_t mach\_port\_t vm\_size\_t memory\_object, memory\_control, memory\_object\_name, memory\_object\_page\_size);

#### seqnos\_memory\_object\_init Sequence Number form

kern\_return\_t seqnos\_memory\_object\_init

(mach\_port\_t
mach\_port\_seqno\_t
mach\_port\_t
mach\_port\_t
vm\_size\_t

memory\_object, seqno, memory\_control, memory\_object\_name, memory\_object\_page\_size);

## DESCRIPTION

A **memory\_object\_init** function is called as the result of a kernel message notifying a memory manager that the kernel has been asked to map the specified memory object into a task's virtual address space.

When asked to map a memory object for the first time, the kernel responds by making a **memory\_object\_init** call on the abstract memory object. This call is provided as a convenience to the memory manager, to allow it to initialize data structures and prepare to receive other requests.

In addition to the abstract memory object port itself, the call provides the following two ports:

- A memory cache control port that the memory manager can use to control use of its data by the kernel. The memory manager gets send rights for this port.
- A memory cache name port that the kernel will use to identify the memory object to other tasks.

The kernel holds send rights for the abstract memory object port, and both send and receive rights for the memory cache control and name ports. The call also supplies the virtual page size to be used for the memory mapping. The memory manager can use this size to detect mappings that use different data structures at initialization time, or to allocate buffers for use in reading data.

If a memory object is mapped into the address space of more than one task on different hosts (with independent kernels), the memory manager will receive a **memory\_object\_init** call from each kernel, containing a unique set of control and name ports. Note that each kernel may also use a different page size.

## PARAMETERS

memory\_object

[in abstract-memory-object port] The abstract memory object port that represents the memory object data, as supplied to the kernel in a **vm\_map** call.

seqno

[in scalar] The sequence number of this message relative to the abstract memory object port.

memory\_control

[in memory-cache-control port] The memory cache control port to be used by the memory manager. If the memory object has been supplied to more than one kernel, this parameter identifies the kernel that is making the call.

#### memory\_object\_name

[in memory-cache-name port] The memory cache name port used by the kernel to refer to the memory object data in response to **vm\_region** calls.

#### memory\_object\_page\_size

[in scalar] The page size used by the kernel. All calls involving this kernel must use data sizes that are integral multiples of this page size.

#### NOTES

When the memory manager is ready to accept data requests for this memory object, it must call **memory\_object\_ready**. Otherwise, the kernel will not process requests on this object.

## **RETURN VALUE**

Any return value other than KERN\_SUCCESS or MIG\_NO\_REPLY will cause **mach\_msg\_server** to remove the memory cache control and name port references.

## **RELATED INFORMATION**

Functions:memory\_object\_ready,memory\_object\_terminate,memory\_object\_server, seqnos\_memory\_object\_server.

# memory\_object\_lock\_completed

Server Interface — Indicates completion of a consistency control call

#### LIBRARY

Not declared anywhere.

#### **SYNOPSIS**

kern\_return\_t memory\_object\_lock\_completed

(mach_port_t	reply_port,
mach_port_t	memory_control,
vm_offset_t	offset,
vm_size_t	length);

# seqnos\_memory\_object\_lock\_completed

Sequence Number form

kern\_return\_t seqnos\_memory\_object\_lock\_completed

(mach_port_t	reply_port,
mach_port_seqno_t	seqno,
mach_port_t	memory_control,
vm_offset_t	offset,
vm_size_t	length);

## DESCRIPTION

A **memory\_object\_lock\_completed** function is called as the result of a kernel message confirming the kernel's action in response to a **memory\_object\_lock\_request** call from the memory manager. The memory manager can use the **memory\_object\_lock\_request** call to:

- Alter access restrictions specified in the **memory\_object\_data\_supply** call or a previous **memory\_object\_lock\_request** call.
- Write back modifications made in memory.
- Invalidate its cached data.

When the kernel completes the requested actions, it calls memory\_object\_lock\_completed (asynchronously) using the port explicitly provided in the memory\_object\_lock\_request call. Because the memory manager cannot know which pages have been modified, or even which pages remain in the cache, it cannot know how many pages will be written back in response memory\_object\_lock\_request call. Receiving the to а memory\_object\_lock\_completed call is the only sure means of detecting completion. The completion call includes the offset and length values from the consistency request to distinguish it from other consistency requests.

## PARAMETERS

reply\_port

[in reply port] The port named in the corresponding **memory\_object\_lock\_request** call.

seqno

[in scalar] The sequence number of this message relative to the port named in the **memory\_object\_lock\_request** message.

#### memory\_control

[in memory-cache-control port] The memory cache control port to be used for a response by the memory manager. If the memory object has been supplied to more than one kernel, this parameter identifies the kernel that is making the call.

offset

[in scalar] The offset within the memory object.

length

[in scalar] The number of bytes to which the call refers, starting at *offset*. The number converts to an integral number of memory object pages.

## **RETURN VALUE**

Any return value other than KERN\_SUCCESS or MIG\_NO\_REPLY will cause **mach\_msg\_server** to remove the memory cache control port reference.

## **RELATED INFORMATION**

Functions: memory\_object\_lock\_request, memory\_object\_server, seqnos\_memory\_object\_server.

# memory\_object\_lock\_request

Function — Restricts access to memory object data

#### **SYNOPSIS**

memory_control,
offset,
size,
should_return,
should_flush,
lock_value,
reply_port);

# DESCRIPTION

The **memory\_object\_lock\_request** function allows the memory manager to make the following requests of the kernel:

- Clean the pages within the specified range by writing back all changed (that is, dirty) and precious pages. The kernel uses the memory\_object\_data\_return call to write back the data. The *should\_return* parameter must be set to non-zero.
- Flush all cached data within the specified range. The kernel invalidates the range of data and revokes all uses of that data. The *should\_flush* parameter must be set to true.
- Alter access restrictions specified in the memory\_object\_data\_supply call or a previous memory\_object\_lock\_request call. The *lock\_value* parameter must specify the new access restrictions. Note that this parameter can be used to unlock previously locked data.

Once the kernel performs all of the actions requested by this call, it issues a **memory\_object\_lock\_completed** call using the *reply\_port* port.

# SECURITY

The requesting task must hold *mcsv\_remove\_page, mcsv\_change\_page\_locks,* and *mcsv\_invoke\_lock\_request* permission to *memory\_control.* 

## PARAMETERS

#### memory\_control

[in memory-cache-control port] The memory cache control port to be used by the memory manager for cache management requests. This port is provided by the kernel in a **memory\_object\_init** call.

#### offset

[in scalar] The offset within the memory object, in bytes.

#### size

[in scalar] The number of bytes of data (starting at *offset*) to be affected. The number must convert to an integral number of memory object pages.

#### should\_return

[in scalar] Clean indicator. Values are:

#### MEMORY\_OBJECT\_RETURN\_NONE

Don't return any pages. If *should\_flush* is TRUE, pages will be discarded.

#### MEMORY\_OBJECT\_RETURN\_DIRTY

Return only dirty (modified) pages. If *should\_flush* is TRUE, precious pages will be discarded; otherwise, the kernel maintains responsibility for precious pages.

#### MEMORY\_OBJECT\_RETURN\_ALL

Both dirty and precious pages are returned. If *should\_flush* is FALSE, the kernel maintains responsibility for the precious pages.

#### should\_flush

[in scalar] Flush indicator. If true, the kernel flushes all pages within the range.

#### lock\_value

[in scalar] One or more forms of access **not** permitted for the specified data. Valid values are:

#### VM\_PROT\_NO\_CHANGE

Do not change the protection of any pages.

#### VM\_PROT\_NONE

Prohibits no access (that is, all forms of access are permitted).

#### VM\_PROT\_READ

Prohibits read access.

# VM\_PROT\_WRITE

Prohibits write access.

#### VM\_PROT\_EXECUTE Prohibits execute access.

VM\_PROT\_ALL Allows all forms of access.

reply\_port

[in reply port] The response port to be used by the kernel on a call to **memory\_object\_lock\_completed**, or MACH\_PORT\_NULL if no response is required.

#### NOTES

I

The **memory\_object\_lock\_request** call affects only data that is cached at the time of the call. Access restrictions cannot be applied to pages for which data has not been provided.

When a running thread requires an access that is currently prohibited, the kernel issues a **memory\_object\_data\_unlock** call specifying the access required. The memory manager can then use **memory\_object\_lock\_request** to relax its access restrictions on the data.

To indicate that an unlock request is invalid (that is, requires permission that can never be granted), the memory manager must first flush the page. When the kernel requests the data again with the higher permission, the memory manager can indicate the error by responding with a call to **memory\_object\_data\_error**.

## **RETURN VALUE**

Since this function does not receive a reply message, it has no return value. Only message transmission errors apply.

## **RELATED INFORMATION**

Functions: **memory\_object\_data\_supply**, **memory\_object\_data\_unlock**, **memory\_object\_lock\_completed**.

# memory\_object\_ready

Function — Marks a memory object is ready to receive paging operations

#### **SYNOPSIS**

kern\_return\_t memory\_object\_ready

(mach\_port\_t boolean\_t memory\_object\_copy\_strategy\_t memory\_control, may\_cache\_object, copy\_strategy);

#### DESCRIPTION

The **memory\_object\_ready** function informs the kernel that the manager is ready to receive data or unlock requests on behalf of clients. Performance-related attributes for the specified memory object can also be set at this time. These attributes control whether the kernel is permitted to:

- Retain data from a memory object even after all address space mappings have been de-allocated (*may\_cache\_object* parameter).
- Perform optimizations for virtual memory copy operations (*copy\_strategy* parameter).

#### SECURITY

The requesting task must hold *mcsv\_set\_attributes* permission to *memory\_control*.

## PARAMETERS

#### memory\_control

[in memory-cache-control port] The memory cache control port to be used by the memory manager for cache management requests. This port is provided by the kernel in a **memory\_object\_init** call.

#### may\_cache\_object

[in scalar] Cache indicator. If true, the kernel can cache data associated with the memory object, even if virtual memory references to it are removed.

#### copy\_strategy

[in scalar] How the kernel should handle copying of regions associated with the memory object. Valid values are:

#### MEMORY\_OBJECT\_COPY\_NONE

Use normal procedure when copying the memory object's data. Normally, the kernel requests each page with read access, copies the data, and then (optionally) flushes the data.

#### MEMORY\_OBJECT\_COPY\_CALL

Notify the memory manager (via **memory\_object\_copy**) before copying any data.

### MEMORY\_OBJECT\_COPY\_DELAY

Use copy-on-write technique. This strategy allows the kernel to efficiently copy large amounts of data and guarantees that the memory manager will not externally modify the data. It is the most commonly used copy strategy.

#### MEMORY\_OBJECT\_COPY\_TEMPORARY

Mark the object as temporary. This had the same effect as the MEMORY\_OBJECT\_COPY\_DELAY strategy and has the additional attribute that when the last mapping of the memory object is removed, the object is destroyed without returning any in-memory pages.

#### NOTES

Sharing cached data among all the clients of a memory object can have a major impact on performance, especially if it can be extended across successive, as well as concurrent, uses. For example, the memory objects that represent program images can be used regularly by different programs. By retaining the data for these memory objects in cache, the number of secondary storage accesses can be reduced significantly.

# **RETURN VALUE**

Since this function does not receive a reply message, it has no return value. Only message transmission errors apply.

## **RELATED INFORMATION**

Functions:memory\_object\_change\_attributes,memory\_object\_copy,memory\_object\_get\_attributes,memory\_object\_copy,memory\_object\_set\_attributes (old form).memory\_object\_init,

# memory\_object\_set\_attributes

Function — Sets attributes for a memory object (old form)

#### **SYNOPSIS**

kern\_return\_t memory\_object\_set\_attributes

(mach\_port\_tmemory\_control,boolean\_tobject\_ready,boolean\_tmay\_cache\_object,memory\_object\_copy\_strategy\_tcopy\_strategy);

# DESCRIPTION

The **memory\_object\_set\_attributes** function allows the memory manager to set performance-related attributes for the specified memory object. These attributes control whether the kernel is permitted to:

- Make data or unlock requests on behalf of clients (*object\_ready* parameter).
- Retain data from a memory object even after all address space mappings have been de-allocated (*may\_cache\_object* parameter).
- Perform optimizations for virtual memory copy operations (*copy\_strategy* parameter).

## SECURITY

The requesting task must hold *mcsv\_set\_attributes* permission to *memory\_control*.

#### PARAMETERS

#### memory\_control

[in memory-cache-control port] The memory cache control port to be used by the memory manager for cache management requests. This port is provided by the kernel in a **memory\_object\_init** call.

#### object\_ready

[in scalar] Ready indicator. If true, the kernel can issue new data and unlock requests on the memory object.

#### may\_cache\_object

[in scalar] Cache indicator. If true, the kernel can cache data associated with the memory object, even if virtual memory references to it are removed.

#### copy\_strategy

[in scalar] How the kernel should handle copying of regions associated with the memory object. Valid values are:

#### MEMORY\_OBJECT\_COPY\_NONE

Use normal procedure when copying the memory object's data. Normally, the kernel requests each page with read access, copies the data, and then (optionally) flushes the data.

#### MEMORY\_OBJECT\_COPY\_CALL

Notify the memory manager (via **memory\_object\_copy**) before copying any data.

#### MEMORY\_OBJECT\_COPY\_DELAY

Use copy-on-write technique. This strategy allows the kernel to efficiently copy large amounts of data and guarantees that the memory manager will not externally modify the data. It is the most commonly used copy strategy.

#### MEMORY\_OBJECT\_COPY\_TEMPORARY

Mark the object as temporary. This had the same effect as the MEMORY\_OBJECT\_COPY\_DELAY strategy and has the additional attribute that when the last mapping of the memory object is removed, the object is destroyed without returning any in-memory pages.

#### NOTES

memory\_object\_set\_attributes the old form of is memory object change attributes. When used to change the cache or copy strategy attributes, it has the same effect (with the omission of a possible reply) as memory object change attributes. The difference between these two calls is the *ready* attribute. The use of this old call with the *ready* attribute set has the same basic effect as the new **memory object ready** call. However, the use of this old call informs the kernel that this is an old form memory manager that expects memory object data write messages instead of the new memory\_object\_data\_return messages implied by memory\_object\_ready. Changing a memory object to be not ready does not affect data and unlock requests already in progress. Such requests will not be aborted or reissued.

Sharing cached data among all the clients of a memory object can have a major impact on performance, especially if it can be extended across successive, as well as concurrent, uses. For example, the memory objects that represent program images can be used regularly by different programs. By retaining the data for these memory objects in cache, the number of secondary storage accesses can be reduced significantly.

#### **RETURN VALUE**

Since this function does not receive a reply message, it has no return value. Only message transmission errors apply.

## **RELATED INFORMATION**

Functions: memory\_object\_change\_attributes, memory\_object\_get\_attributes, memory\_object\_ready.

memory\_object\_copy, memory\_object\_init,

# memory\_object\_supply\_completed

Server Interface — Indicates completion of a data supply call

#### LIBRARY

Not declared anywhere.

## **SYNOPSIS**

kern\_return\_t memory\_object\_supply\_completed

(mach_port_t	reply_port,
mach_port_t	memory_control,
vm_offset_t	offset,
vm_size_t	length,
kern_return_t	result,
vm_offset_t	error_offset);

# seqnos\_memory\_object\_supply\_completed Sequence Number form

kern\_return\_t seqnos\_memory\_object\_supply\_completed

reply_port,	(mach_port_t
seqno,	mach_port_seqno_t
memory_control,	mach_port_t
offset,	vm_offset_t
length,	vm_size_t
result,	kern_return_t
error_offset);	vm_offset_t

## DESCRIPTION

A memory\_object\_supply\_completed function is called as the result of a kernel message confirming the kernel's action in response to a memory\_object\_data\_supply call from the memory manager.

When the kernel accepts the pages, it calls memory\_object\_supply\_completed using explicitly (asynchronously) the port provided in the memory\_object\_data\_supply call. Because the data supply call can provide multiple pages, not all of which the kernel may necessarily accept and some of which the kernel may have to return to the manager (if precious), the kernel provides this response. If the kernel does not accept all of the pages in the data supply message, it will indicate so in the completion response. If the pages not accepted are precious, they will be returned (in memory\_object\_data\_return messages) before it sends this completion message. The completion call includes the offset and length values from the supply request to distinguish it from other supply requests.

## PARAMETERS

#### reply\_port

[in reply port] The port specified to the corresponding **memory\_object\_data\_supply** call.

seqno

[in scalar] The sequence number of this message relative to the port named in the **memory\_object\_data\_supply** call.

#### memory\_control

[in memory-cache-control port] The memory cache control port to be used for a response by the memory manager. If the memory object has been supplied to more than one kernel, this parameter identifies the kernel that is making the call.

#### offset

[in scalar] The offset within the memory object from the corresponding data supply call

#### length

[in scalar] The number of bytes accepted. The number converts to an integral number of memory object pages.

#### result

[in scalar] A kernel return code indicating the result of the supply operation, possibly KERN\_SUCCESS. KERN\_MEMORY\_PRESENT is currently the only error returned; other errors (invalid arguments, for example) abort the data supply operation.

#### error\_offset

[in scalar] The offset within the memory object where the first error occurred.

#### **RETURN VALUE**

Any return value other than KERN\_SUCCESS or MIG\_NO\_REPLY will cause **mach\_msg\_server** to remove the memory cache control port reference.

#### **RELATED INFORMATION**

Functions: memory\_object\_data\_supply, memory\_object\_server, seqnos\_memory\_object\_server.

# memory\_object\_terminate

Server Interface — Relinquishes access to a memory object

#### LIBRARY

Not declared anywhere.

#### **SYNOPSIS**

kern\_return\_t memory\_object\_terminate

(mach\_port\_t mach\_port\_t mach\_port\_t memory\_object, memory\_control, memory\_object\_name);

## seqnos\_memory\_object\_terminate Sequence Number form

#### kern\_return\_t seqnos\_memory\_object\_terminate

(mach\_port\_t mach\_port\_seqno\_t mach\_port\_t mach\_port\_t memory\_object, seqno, memory\_control, memory\_object\_name);

#### DESCRIPTION

A **memory\_object\_terminate** function is called as the result of a kernel message notifying a memory manager that no mappings of the specified memory object remain. The kernel makes this call to allow the memory manager to clean up data structures associated with the de-allocated mappings. The call provides receive rights to the memory cache control and name ports so that the memory manager can destroy the ports (via **mach\_port\_deallocate**). The kernel also relinquishes its send rights for all three ports.

The kernel terminates a memory object only after all address space mappings of the object have been de-allocated, or upon explicit request by the memory manager.

#### PARAMETERS

```
memory_object
```

[in abstract-memory-object port] The abstract memory object port that represents the memory object data, as supplied to the kernel in a **vm\_map** call.

seqno

[in scalar] The sequence number of this message relative to the abstract memory object port. memory\_control

[in memory-cache-control port] The memory cache control port to be used for a response by the memory manager. If the memory object has been supplied to more than one kernel, this parameter identifies the kernel that is making the call.

memory\_object\_name

[in memory-cache-name port] The memory cache name port used by the kernel to refer to the memory object data in response to **vm\_region** calls.

#### NOTES

If a client thread calls **vm\_map** to map a memory object while the kernel is calling **memory\_object\_terminate** for the same memory object, the **memory\_object\_init** call may appear before the **memory\_object\_terminate** call. This sequence is indistinguishable from the case where another kernel is issuing a **memory\_object\_init** call. In other words, the control and name ports included in the initialization will be different from those included in the termination. A memory manager must be aware that this sequence can occur even when all mappings of a memory object take place on the same host.

#### **RETURN VALUE**

Any return value other than KERN\_SUCCESS or MIG\_NO\_REPLY will cause **mach\_msg\_server** to remove the memory cache control and name port references.

#### **RELATED INFORMATION**

Functions:memory\_object\_destroy,memory\_object\_init,mach\_port\_deallocate,memory\_object\_server,seqnos\_memory\_object\_server.memory\_object\_server,

# CHAPTER 6 Thread Interface

This chapter discusses the specifics of the kernel's thread interfaces. This includes status functions related to threads. Properties associated with threads, such as special ports, are included here as well. Functions that apply to more than one thread appear in the task interface chapter.

# catch\_exception\_raise

Server Interface — Handles the occurrence of an exception within a thread

#### LIBRARY

Not declared anywhere.

## **SYNOPSIS**

kern_return_t catch_exception_raise	
(mach_port_t	exception_port,
mach_port_t	thread,
mach_port_t	task,
int	exception,
int	code,
int	subcode);

#### DESCRIPTION

A **catch\_exception\_raise** function is called by **exc\_server** as the result of a kernel message indicating that an exception occurred within a thread. *exception\_port* is the port named via **thread\_set\_special\_port** or **task\_set\_special\_port** as the port that responds when the thread takes an exception.

#### SECURITY

There are no security limitations on this kernel outcall.

# PARAMETERS

```
exception_port
```

[in exception port] The port to which the exception notification was sent.

#### thread

[in thread port] The port to the thread taking the exception.

task

[in task port] The port to the task containing the thread taking the exception.

#### exception

[in scalar] The type of the exception, as defined in **<mach/excep-tion.h>**. The machine independent values raised by all implementations are: I

I

#### EXC\_BAD\_ACCESS

Could not access memory. *code* contains *kern\_return\_t* describing error. *subcode* contains bad memory address.

#### EXC\_BAD\_INSTRUCTION

Instruction failed. Illegal or undefined instruction or operand.

#### EXC\_ARITHMETIC

Arithmetic exception; exact nature of exception is in *code* field.

#### EXC\_EMULATION

Emulation instruction. Emulation support instruction encountered. Details in *code* and *subcode* fields.

#### EXC\_SOFTWARE

Software generated exception; exact exception is in *code* field. Codes 0 - 0xFFFF reserved to hardware; codes 0x10000 - 0x1FFFF reserved for OS emulation (Unix).

#### EXC\_BREAKPOINT

Trace, breakpoint, etc. Details in *code* field.

code

[in scalar] A code indicating a particular instance of *exception*.

#### subcode

[in scalar] A specific type of *code*.

#### NOTES

When an exception occurs in a thread, the thread sends an exception message to its exception port, blocking in the kernel waiting for the receipt of a reply. It is assumed that some task is listening (most likely with **mach\_msg\_server**) to this port, using the **exc\_server** function to decode the messages and then call the linked in **catch\_exception\_raise**. It is the job of **catch\_exception\_raise** to handle the exception and decide the course of action for *thread*. The state of the blocked thread can be examined with **thread\_get\_state**.

If the thread should continue from the point of exception, **catch\_exception\_raise** would return KERN\_SUCCESS. This causes a reply message to be sent to the kernel, which will allow the thread to continue from the point of the exception.

If some other action should be taken by *thread*, the following actions should be performed by **catch\_exception\_raise**:

• **thread\_suspend**. This keeps the thread from proceeding after the next step.

- **thread\_abort**. This aborts the message receive operation currently blocking the thread.
- **thread\_set\_state**. Set the thread's state so that it continues doing something else.
- thread\_resume. Let the thread start running from its new state.
- Return a value other than KERN\_SUCCESS so that no reply message is sent. (Actually, the kernel uses a send once right to send the exception message, which **thread\_abort** destroys, so replying to the message is harmless.)

The thread can always be destroyed with thread\_terminate.

A thread can have two exception ports active for it: its thread exception port and the task exception port. If an exception message is sent to the thread exception port (if it exists), and a reply message contains a return value other than KERN\_SUCCESS, the kernel will then send the exception message to the task exception port. If that exception message receives a reply message with other than a return value of KERN\_SUCCESS, the thread is terminated. Note that this behavior cannot be obtained by using the **catch\_exception\_raise** interface called by **exc\_server** and **mach\_msg\_server**, since those functions will either return a reply message with a KERN\_SUCCESS value, or none at all.

#### **RETURN VALUE**

A return value of KERN\_SUCCESS indicates that the thread is to continue from the point of exception. A return value of MIG\_NO\_REPLY indicates that the exception was handled directly and the thread was restarted or terminated by the exception handler. A return value of MIG\_DESTROY\_REQUEST causes the kernel to try another exception handler (or terminate the thread). Any other value will cause **mach\_msg\_server** to remove the task and thread port references.

#### **RELATED INFORMATION**

Functions:	exc_server,	thread_abort,	task_get_special_port,
thread_get_spe	cial_port,	thread_get_state,	thread_resume,
task_set_specia	ıl_port,	thread_set_special_port,	thread_set_state,
thread_suspend, thread_terminate.			

# mach\_thread\_self

System Trap — Returns the thread self port

#### LIBRARY

#include <mach/mach\_traps.h>

## **SYNOPSIS**

# DESCRIPTION

The **mach\_thread\_self** function returns send rights to the thread's own kernel port.

## SECURITY

The requesting task must hold *thsv\_get\_thread\_kernel\_port* permission to its own thread port.

## PARAMETERS

None

#### **RETURN VALUE**

[thread-self port] Send rights to the thread's port.

## **RELATED INFORMATION**

Functions: thread\_info, thread\_set\_special\_port.

# receive\_samples

Server Interface — Handles the occurrence of a PC sampling message

#### LIBRARY

Not declared anywhere.

## **SYNOPSIS**

kern\_return\_t receive\_samples (mach\_port\_t

sample\_array\_t mach\_msg\_type\_number\_t sample\_port, samples, samplesCnt);

#### DESCRIPTION

A **receive\_samples** function is called by **prof\_server** as the result of a kernel message indicating that a set of program counter samples has been gathered. *sample\_port* is the port named via **task\_sample** or **thread\_sample**.

#### SECURITY

There are no security limitations on this kernel outcall.

## PARAMETERS

sample\_port

[in sample port] The port to which the sample message was sent.

sample

[pointer to in array of *vm\_address\_t*] An array of PC sample values.

sampleCnt

[in scalar] The number of values in sample.

#### NOTES

This interface is machine word length specific because of the virtual addresses in the *samples* parameter.

## **RETURN VALUE**

Irrelevant.

## **RELATED INFORMATION**

Functions: task\_sample, thread\_sample, prof\_server.'

# swtch

System Trap — Attempt a context switch

#### LIBRARY

Not declared anywhere.

#### **SYNOPSIS**

boolean\_t swtch ();

# DESCRIPTION

The **swtch** function attempts to context switch the current thread off the processor.

This function is useful in user level lock management routines. If the current thread cannot make progress because of some lock, it would execute the **swtch** function. When this returns, the thread should once again try to make progress by attempting to obtain its lock.

This function returns a flag indicating whether there is anything else for the processor to do. If there is nothing else, the thread can spin waiting for its lock, instead of continuing to call **swtch**.

# SECURITY

The requesting task must hold thsv\_can\_swtch permission to its own thread port

#### PARAMETERS

None

## **RETURN VALUE**

TRUE

There are other threads that the processor could run.

#### FALSE

The processor has nothing better to do.

## **RELATED INFORMATION**

Functions: swtch\_pri, thread\_abort, thread\_switch.

# swtch\_pri

System Trap — Attempt a context switch to low priority

#### LIBRARY

Not declared anywhere.

## **SYNOPSIS**

boolean\_t **swtch\_pri** (int

priority);

## DESCRIPTION

The **swtch\_pri** function attempts to context switch the current thread off the processor. The thread's priority is lowered to the minimum possible value during this time. The priority of the thread will be restored when it is awakened.

This function is useful in user level lock management routines. If the current thread cannot make progress because of some lock, it would execute the **swtch\_pri** function. When this returns, the thread should once again try to make progress by attempting to obtain its lock.

This function returns a flag indicating whether there is anything else for the processor to do. If there is nothing else, the thread can spin waiting for its lock, instead of continuing to call **swtch\_pri**.

## SECURITY

The requesting task must hold *thsv\_can\_swtch\_pri* permission to its own thread port.

#### PARAMETERS

```
priority
```

[in scalar] Currently not used.

## **RETURN VALUE**

TRUE

There are other threads that the processor could run.

FALSE

The processor has nothing better to do.

# **RELATED INFORMATION**

Functions: swtch, thread\_abort, thread\_depress\_abort, thread\_switch.

# thread\_abort

Function — Aborts a thread

#### **SYNOPSIS**

kern\_return\_t **thread\_abort** (mach\_port\_t

target\_thread);

#### DESCRIPTION

The **thread\_abort** function aborts page faults and any message primitive calls (**mach\_msg, mach\_msg\_receive**, and **mach\_msg\_send**) in use by *target\_thread*. (Note, though, that the message calls retry interrupted message operations unless MACH\_SEND\_INTERRUPT and MACH\_RCV\_INTERRUPT are specified.) Priority depressions are also aborted. The call returns a code indicating that it was interrupted. The call is interrupted even if the thread (or the task containing it) is suspended. If it is suspended, the thread receives the interrupt when it resumes.

If its state is not modified before it resumes, the thread will retry an aborted page fault. The Mach message trap returns either MACH\_SEND\_INTERRUPTED or MACH\_RCV\_INTERRUPTED, depending on whether the send or the receive side was interrupted. Note, though, that the Mach message trap is contained within the **mach\_msg** library routine, which, by default, retries interrupted message calls.

The basic purpose of **thread\_abort** is to let one thread cleanly stop another thread (*target\_thread*). The target thread is stopped in such a manner that its future execution can be controlled in a predictable way.

## SECURITY

The requesting task must hold *thsv\_abort\_thread* permission to *target\_thread*.

## PARAMETERS

target\_thread

[in thread port] The thread to be aborted.

## NOTES

By way of comparison, the **thread\_suspend** function keeps the target thread from executing any further instructions at the user level, including the return from a system call. The **thread\_get\_state** function returns the thread's user state, while **thread\_set\_state** allows modification of the user state.

A problem occurs if a suspended thread had been executing within a system call. In this case, the thread has, not only a user state, but an associated kernel state. (The kernel state cannot be changed with **thread\_set\_state**.) As a result, when the thread resumes, the system call can return, producing a change in the user state and, possibly, user memory.

For a thread executing within a system call, **thread\_abort** aborts the kernel call from the thread's point of view. Specifically, it resets the kernel state so that the thread will resume execution at the system call return, with the return code value set to one of the interrupted codes. The system call itself may be completed entirely, aborted entirely or be partially completed, depending on when the abort is received. As a result, if the thread's user state has been modified by **thread\_set\_state**, it will not be altered un-predictably by any unexpected system call side effects.

For example, to simulate a POSIX signal, use the following sequence of calls:

- **thread\_suspend** To stop the thread.
- **thread\_abort** To interrupt any system call in progress and set the return value to "interrupted". Because the thread is already stopped, it will not return to user code.
- **thread\_set\_state** To modify the thread's user state to simulate a procedure call to the signal handler.
- **thread\_resume** To resume execution at the signal handler. If the thread's stack is set up correctly, the thread can return to the interrupted system call. Note that the code to push an extra stack frame and change the registers is highly machine dependent.

#### CAUTIONS

As a rule, do not use **thread\_abort** on a non-suspended thread. This operation is very risky because it is difficult to know which system trap, if any, is executing and whether an interrupt return will result in some useful action by the thread.

**thread\_abort** will abort any non-atomic operation (such as a multi-page **memory\_object\_data\_supply**) at an arbitrary point in a non-restartable way. Such problems can be avoided by using **thread\_abort\_safely**.

#### **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: mach\_msg, thread\_get\_state, thread\_info, thread\_set\_state, thread\_suspend, thread\_terminate, thread\_abort\_safely.

# thread\_create/thread\_create\_secure

Function — Creates a thread within a task

#### SYNOPSIS

kern\_return\_t **thread\_create** (mach\_port\_t mach\_port\_t\*

parent\_task,
child thread);

kern\_return\_t **thread\_create\_secure** (mach\_port\_t mach\_port\_t\*

parent\_task,
child\_thread);

#### DESCRIPTION

The **thread\_create** function creates a new thread within *parent\_task*. The new thread has a suspend count of one and no processor state.

The new thread holds a send right for its thread kernel port. A send right for the thread's kernel port is also returned to the calling task or thread in *child\_thread*. The new thread's exception port is set to MACH\_PORT\_NULL.

The **thread\_create\_secure** function creates a new thread within *parent\_task* only if the task had been created by **task\_create\_secure** and the *parent\_task*'s task structure is in an EMPTY state. The state of *child\_thread*'s task structure is changed from EMPTY to THREAD\_CREATED.

## **SECURITY**

For the **thread\_create** function, the requesting task must have *tsv\_add\_thread* permission to *parent\_task*. For the **thread\_create\_secure** function, the requesting task must have *tsv\_add\_thread\_secure* permission to *parent\_task*.

## PARAMETERS

parent\_task

[in task port] The port for the task that is to contain the new thread.

child\_thread

[out thread port] The kernel-assigned name for the new thread.

## NOTES

To get a new thread running, first use **thread\_set\_state** to set a processor state for the thread. Then, use **thread\_resume** to schedule the thread for execution.

## **RETURN VALUE**

I

Only generic errors apply.

## **RELATED INFORMATION**

Functions:task\_create,task\_create\_secure,task\_threads,thread\_get\_special\_port,thread\_get\_state,thread\_resume,thread\_resume\_secure,thread\_set\_special\_port,thread\_set\_state,thread\_set\_state\_secure,thread\_suspend,thread\_terminate.

# thread\_depress\_abort

Function — Cancel thread priority depression

## **SYNOPSIS**

kern\_return\_t **thread\_depress\_abort** (mach\_port\_t

thread);

## DESCRIPTION

The **thread\_depress\_abort** function cancels any priority depression effective for *thread* caused by a **swtch\_pri** or **thread\_switch** call.

## SECURITY

The requesting task must hold *thsv\_abort\_thread\_depress* permission to *thread*.

## PARAMETERS

thread

[in thread port] Thread whose priority depression is canceled.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: swtch, swtch\_pri, thread\_abort, thread\_switch.

# thread\_get\_special\_port

**Function** — Returns a send right to a special port

#### **SYNOPSIS**

kern_return_t <b>thread_get_special_port</b> (mach_port_t int mach_port_t*	thread, which_port, special_port);
thread_get_exception_port Macro form	
<pre>kern_return_t thread_get_exception_port</pre>	thread, special_port) _PORT,
thread_get_kernel_port Macro form	
kern_return_t <b>thread_get_kernel_port</b> (mach_port_t mach_port_t*	thread, special_port)
$\Rightarrow$ thread_get_special_port (thread, THREAD_KERNEL_PO special_port)	RT,

## **DESCRIPTION**

The thread\_get\_special\_port function returns a send right for a special port belonging to *thread*.

The thread kernel port is a port for which the kernel holds the receive right. The kernel uses this port to identify the thread.

If one thread has a send right for the kernel port of another thread, it can use the port to perform kernel operations for the other thread. Send rights for a kernel port normally are held only by the thread to which the port belongs, or by the task that contains the thread. Using the mach\_msg function, however, any thread can pass a send right for its kernel port to another thread.

# **SECURITY**

The requesting task must hold thsv\_get\_thread\_exception\_port or thsv\_get\_thread\_kernel\_port permission to thread to get, respectively, the exception port or the kernel port.

# PARAMETERS

#### thread

[in thread port] The thread for which to return the port's send right.

#### which\_port

[in scalar] The special port for which the send right is requested. Valid values are:

#### THREAD\_EXCEPTION\_PORT

[exception port] The thread's exception port. Used to receive exception messages from the kernel.

#### THREAD\_KERNEL\_PORT

[thread-self port] The port used to name the thread. Used to invoke operations that affect the thread. This is the port returned by **mach\_thread\_self**.

#### special\_port

[out thread-special port] The returned value for the port.

## **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: mach\_thread\_self, task\_get\_special\_port, task\_set\_special\_port, thread\_create, thread\_set\_special\_port.

# thread\_get\_state

**Function** — Returns the execution state for a thread

#### **SYNOPSIS**

kern_return_t thread_get_state	
(mach_port_t	target_thread,
int	flavor,
thread_state_t	old_state,
mach_msg_type_number_t*	old_stateCnt);

#### DESCRIPTION

The **thread\_get\_state** function returns the execution state (for example, the machine registers) for *target\_thread*. *flavor* specifies the type of state information returned.

The format of the data returned is machine specific; it is defined in <mach/thread\_status.h>.

## SECURITY

The requesting task must hold *thsv\_get\_thread\_state* permission to *target\_thread*.

#### PARAMETERS

#### target\_thread

[in thread port] The thread for which the execution state is to be returned. The calling thread cannot specify itself.

#### flavor

[in scalar] The type of execution state to be returned. Valid values correspond to supported machined architectures.

#### old\_state

[out array of *int*] Array of state information for the specified thread.

#### old\_stateCnt

[pointer to in/out scalar] On input, the maximum size of the state array; on output, the returned size of the state array (in units of sizeof (*int*)). The maximum size is defined by THREAD\_STATE\_MAX.

## **RETURN VALUE**

Only generic errors apply.

# **RELATED INFORMATION**

Functions: task\_info, thread\_info, thread\_set\_state.

# thread\_info

Function — Returns information about a thread

#### **SYNOPSIS**

kern_return_t <b>thread_info</b>	
(mach_port_t	target_thread,
int	flavor,
thread_info_t	thread_info,
mach_msg_type_number_t*	thread_infoCnt);

#### DESCRIPTION

The thread\_info function returns an information array of type *flavor*.

# SECURITY

The requesting task must hold *thsv\_get\_thread\_info* permission to *target\_thread*.

#### PARAMETERS

[in thread port] The thread for which the information is to be returned.

#### flavor

[in scalar] The type of information to be returned. Valid values are:

#### THREAD\_BASIC\_INFO

Returns basic information about the thread, such as the thread's run state and suspend count. The returned structure is **thread\_basic\_info** of size THREAD\_BASIC\_INFO\_COUNT.

#### THREAD\_SCHED\_INFO

Returns scheduling information about the thread, such as priority and scheduling policy. The returned structure is **thread\_sched\_info** of size THREAD\_SCHED\_INFO\_SIZE.

#### thread\_info

[out array of *int*] Information about the specified thread.

#### thread\_infoCnt

[pointer to in/out scalar] On input, the size of the info buffer; on output, the returned size of the information structure (in units of sizeof *(int)*). The maximum size is defined by THREAD\_INFO\_MAX.

target\_thread

## **RETURN VALUE**

Only generic errors apply.

# **RELATED INFORMATION**

Functions:task\_info,task\_threads,thread\_get\_special\_port,thread\_get\_state,thread\_set\_special\_port,thread\_set\_state.

Data Structures: thread\_basic\_info, thread\_sched\_info.

# thread\_resume/thread\_resume\_secure

Function — Resumes a thread

## **SYNOPSIS**

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kern\_return\_t **thread\_resume** (mach\_port\_t

target\_thread);

kern\_return\_t **thread\_resume\_secure** (mach\_port\_t

target\_thread);

#### **DESCRIPTION**

The **thread\_resume** function decrements the suspend count for *target\_thread* by one. The thread is resumed if its suspend count goes to zero. If the suspend count is still positive, **thread\_resume** must be repeated until the count reaches zero.

The **thread\_resume\_secure** function decrements the suspend count for *target\_thread* by one. The state of *target\_thread*'s associated task structure is changed from THREAD\_STATE\_SET to TASK\_READY state.

# SECURITY

The **thread\_resume** function requires that the requesting task hold *thsv\_resume\_thread* permission to *target\_thread*. The **thread\_resume\_secure** function requires that the requesting task hold *thsv\_resume\_thread* and *thsv\_initiate\_secure* permission to *target\_thread* and *target\_thread*'s associated thread structure must be in the THREAD\_STATE\_SET state.

#### PARAMETERS

target\_thread

[in thread port] The thread to be resumed.

#### **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions:task\_resume,task\_suspend,thread\_create,thread\_create\_secure, thread\_info, thread\_suspend, thread\_terminate.

# thread\_sample

Function — Perform periodic PC sampling for a thread

#### **SYNOPSIS**

kern_return_t thread_enable_pc_sampling	
(mach_port_t	thread,
int	*ticks;
sampled_pc_flavor_t	flavor);
kern_return_t thread_disable_pc_sampling	
(mach_port_t	thread,
int	*sample_cnt;
sampled_pc_flavor_t	flavor);
kern_return_t thread_get_sampled_pcs	
(mach_port_t	thread,
unsigned	*seqno;
sampled_pc_t	sampled_pcs[],
int	*sample_cnt);

#### DESCRIPTION

These functions cause the program counter (PC) of the specified *thread* to be sampled periodically (whenever the thread happens to be running at the time of the kernel's "hardclock" interrupt). The set of PC sample values obtained are saved in buffers.

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#### **SECURITY**

These functions require that the requesting task hold *thsv\_sample\_thread* permission to *thread*.

## PARAMETERS

```
thread
```

[in thread port] Thread whose PC is to be sampled.

ticks

[out scalar] The kernel's idea of clock granularity (ticks per second). Don't trust this.

#### flavor

[in structure] The sampling flavor, which can be any of the following flavors defined in pc\_sample.h.

SAMPLED\_PC\_PERIODIC, SAMPLED\_PC\_VM\_ZFILL\_FAULTS,

```
SAMPLED_PC_VM_REACTIVATION_FAULTS,
SAMPLED_PC_VM_PAGIN_FAULTS,
SAMPLED_PC_VM_COM_FAULTS,
SAMPLED_PC_VM_FAUTLS_ANY,
SAMPLED_PC_VM_FAULTS.
```

#### seqno

[out scalar] The sequence number of the sampled PC's. This is useful for determining when a collector thread has missed a sample.

## sampled\_pcs

[out structure] The sampled PCs for *thread*. A sample contains three fields: a thread-specific unique identifier, a PC value and the type of sample as per flavor.

#### sample\_cnt

[out scaler] The number of sample elements in the kernel for the named task or thread.

#### **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: task\_enable\_pc\_sampling, task\_get\_sampled\_pcs.

task\_disable\_pc\_sampling,

# thread\_set\_special\_port

Function — Sets a special port for a thread

#### **SYNOPSIS**

kern\_return\_t **thread\_set\_special\_port** (mach\_port\_t int mach\_port\_t

thread, which\_port, special\_port);

#### thread\_set\_exception\_port

Macro form

kern\_return\_t **thread\_set\_exception\_port** (mach\_port\_t mach\_port\_t

thread, special\_port)

⇒ thread\_set\_special\_port (thread, THREAD\_EXCEPTION\_PORT, special\_port)

#### thread\_set\_kernel\_port

Macro form

kern_return_t thread_set_kernel_por	t
(mach_port_t	thread,
mach_port_t	special_port)

⇒ thread\_set\_special\_port (thread, THREAD\_KERNEL\_PORT, special\_port)

#### DESCRIPTION

The thread\_set\_special\_port function sets a special port belonging to thread.

#### SECURITY

The requesting task must hold *thsv\_set\_thread\_exception\_port* or *thsv\_set\_thread\_kernel\_port* permission to *thread* to set, respectively, the thread's exception port or kernel port.

## PARAMETERS

thread

[in thread port] The thread for which to set the port.

which\_port

[in scalar] The special port to be set. Valid values are:

#### THREAD\_EXCEPTION\_PORT

[exception port] The thread's exception port. Used to receive exception messages from the kernel.

#### THREAD\_KERNEL\_PORT

[thread-self port] The thread's kernel port. Used by the kernel to receive messages from the thread.

#### special\_port

[in thread-special port] The value for the port.

## **RETURN VALUE**

Only generic errors apply.

# **RELATED INFORMATION**

Functions: mach\_thread\_self, task\_get\_special\_port, task\_set\_special\_port, thread\_create, thread\_get\_special\_port.

# thread\_set\_state/thread\_set\_state\_secure

Function — Sets the execution state for a thread

#### **SYNOPSIS**

kern_return_t thread_set_state	
(mach_port_t	target_thread,
int	flavor,
thread_state_t	new_state,
mach_msg_type_number_t	new_stateCnt);
kern_return_t thread_set_state_secure	
kern_return_t <b>thread_set_state_secure</b> (mach_port_t	target_thread,
	target_thread, flavor,
(mach_port_t	•

#### DESCRIPTION

The **thread\_set\_state** function sets the execution state (for example, the machine registers) for *target\_thread*. *flavor* specifies the type of state to set. The **thread\_set\_state\_secure** function changes the state of *target\_thread*'s associated task structure from THREAD\_CREATED to THREAD\_STATE\_SET.

The format of the state to set is machine specific; it is defined in **<mach/ thread\_status.h**>. For **thread\_set\_state\_secure** the state may be limited to ensure that the new child task is started at a valid entry point.

#### **SECURITY**

For **thread\_set\_state** the requesting task must hold *thsv\_set\_thread\_state* permission to *target\_thread*. For **thread\_set\_state\_secure** the requesting task must hold *thsv\_set\_thread\_state* and *tsv\_initiate\_secure* permission to *target\_thread* and *target\_thread*'s associated thread structure must be in the THREAD\_CREATED state.

#### PARAMETERS

target\_thread

[in thread port] The thread for which to set the execution state. The calling thread cannot specify itself.

flavor

[in scalar] The type of state to set. Valid values correspond to supported machine architecture features. new\_state

[pointer to in array of *int*] Array of state information for the specified thread.

new\_stateCnt

[in scalar] The size of the state array (in units of size of (*int*)). The maximum size is defined by THREAD\_STATE\_MAX.

# **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: task\_info, thread\_get\_state, thread\_info.

# thread\_suspend

Function — Suspends a thread

#### **SYNOPSIS**

kern\_return\_t **thread\_suspend** (mach\_port\_t

target\_thread);

## DESCRIPTION

The **thread\_suspend** function increments the suspend count for *target\_thread* and prevents the thread from executing any more user-level instructions.

In this context, a user-level instruction can be either a machine instruction executed in user mode or a system trap instruction, including a page fault. If a thread is currently executing within a system trap, the kernel code may continue to execute until it reaches the system return code or it may suspend within the kernel code. In either case, the system trap returns when the thread resumes.

To resume a suspended thread, use **thread\_resume**. If the suspend count is greater than one, **thread\_resume** must be repeated that number of times.

## SECURITY

The requesting task must hold *thsv\_suspend\_thread* permission to *target\_thread*.

## PARAMETERS

target\_thread

[in thread port] The thread to be suspended.

#### CAUTIONS

Unpredictable results may occur if a program suspends a thread and alters its user state so that its direction is changed upon resuming. Note that the **thread\_abort** function allows a system call to be aborted only if it is progressing in a predictable way.

#### **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: task\_resume, task\_suspend, thread\_abort, thread\_get\_state, thread\_info, thread\_resume, thread\_set\_state, thread\_terminate.

# thread\_switch

System Trap — Cause context switch with options

#### LIBRARY

Not declared anywhere.

#### **SYNOPSIS**

kern\_return\_t thread\_switch

(mach\_port\_t int int new\_thread, option, time);

## DESCRIPTION

The **thread\_switch** function provides low-level access to the scheduler's context switching code. *new\_thread* is a hint that implements hand-off scheduling. The operating system will attempt to switch directly to the new thread (bypassing the normal logic that selects the next thread to run) if possible. Since this is a hint, it may be incorrect; it is ignored if it doesn't specify a thread on the same host as the current thread or if the scheduler cannot switch to that thread (i.e., not runable or already running on another processor). In this case, the normal logic to select the next thread to run is used; the current thread may continue running if there is no other appropriate thread to run.

The *option* argument specifies the interpretation and use of *time*. The possible values (from **<mach/thread\_switch.h>**) are:

#### SWITCH\_OPTION\_NONE

The *time* argument is ignored.

#### SWITCH\_OPTION\_WAIT

The thread is blocked for the specified *time*. This wait cannot be canceled by **thread\_resume**; only **thread\_abort** can terminate this wait.

#### SWITCH\_OPTION\_DEPRESS

The thread's priority is depressed to the lowest possible value for *time*. The priority depression is aborted when *time* has passed, when the current thread is next run (either via hand-off scheduling or because the processor set has nothing better to do), or when **thread\_abort** or **thread\_depress\_abort** is applied to the current thread. Changing the thread's priority (via **thread\_priority**) will not affect this depression.

The minimum time and units of time can be obtained as the *min\_timeout* value from the HOST\_SCHED\_INFO flavor of **host\_info**.

# SECURITY

The requesting task must hold *thsv\_switch\_thread* and *thsv\_depress\_pri* permission to *new\_thread*.

#### PARAMETERS

#### new\_thread

[in thread port] Thread to which the processor should switch context.

option

[in scalar] Options applicable to the context switch.

time

[in scalar] Time duration during which the thread should be affected by *option*.

#### NOTES

**thread\_switch** is often called when the current thread can proceed no further for some reason; the various options and arguments allow information about this reason to be transmitted to the kernel. The *new\_thread* argument (hand-off scheduling) is useful when the identity of the thread that must make progress before the current thread runs again is known. The SWITCH\_OPTION\_WAIT option is used when the amount of time that the current thread must wait before it can do anything useful can be estimated and is fairly short, especially when the identity of the thread for which this thread must wait is not known.

## CAUTIONS

Users should beware of calling **thread\_switch** with an invalid hint (e.g., THREAD\_NULL) and no option. Because the time-sharing scheduler varies the priority of threads based on usage, this may result in a waste of CPU time if the thread that must be run is of lower priority. The use of the SWITCH\_OPTION\_DEPRESS option in this situation is highly recommended.

**thread\_switch** ignores policies. Users relying on the preemption semantics of a fixed time policy should be aware that **thread\_switch** ignores these semantics; it will run the specified *new\_thread* independent of its priority and the priority of any threads that could run instead.

#### **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: swtch, swtch\_pri, thread\_abort, thread\_depress\_abort.

# thread\_terminate

Function — Destroys a thread

## **SYNOPSIS**

kern\_return\_t **thread\_terminate** (mach\_port\_t

target\_thread);

## DESCRIPTION

The **thread\_terminate** function kills creates *target\_thread*.

# SECURITY

The requesting task must hold *thsv\_terminate\_thread* permission to *target\_thread*.

## PARAMETERS

target\_thread

[in thread port] The thread to be destroyed.

#### **RETURN VALUE**

Only generic errors apply.

# **RELATED INFORMATION**

Functions: task\_terminate, task\_threads, thread\_create, thread\_resume, thread\_suspend.

# thread\_wire

Function — Marks the thread as privileged with respect to kernel resources

#### LIBRARY

#include <mach/mach\_host.h>

## **SYNOPSIS**

kern\_return\_t **thread\_wire** (mach\_port\_t mach\_port\_t boolean\_t

host\_priv, thread, wired);

#### DESCRIPTION

The **thread\_wire** function marks the thread as "wired". A "wired" thread is always eligible to be scheduled and can consume physical memory even when free memory is scarce. This property should be assigned to threads in the default page-out path. Threads not in the default page-out path should not have this property to prevent the kernel's free list of pages from being exhausted.

## SECURITY

The requesting task must hold *hpsv\_wire\_thread* permission to *host\_priv* and *thsv\_wire\_thread\_into\_memory* to *thread*.

#### PARAMETERS

host\_priv

[in host-control port] The privileged control port for the host on which the thread executes.

thread

[in thread port] The thread to be wired.

wired

[in scalar] TRUE if the thread is to be wired.

# **RETURN VALUE**

#### KERN\_INVALID\_HOST

*host\_priv* is not the control port for the host on which *thread* executes.

# **RELATED INFORMATION**

Functions: **vm\_wire**.

Thread Interface

# CHAPTER 7 Task Interface

This chapter discusses the specifics of the kernel's task interfaces. This includes functions that return status information for a task. Also included are functions that operate upon all or a set of threads within a task.

# mach\_ports\_lookup

Function — Returns an array of well-known system ports.

#### **SYNOPSIS**

kern\_return\_t **mach\_ports\_lookup** (mach\_port\_t mach\_port\_array\_t\* mach\_msg\_type\_number\_t\*

target\_task, init\_port\_set, init\_port\_count);

#### DESCRIPTION

The **mach\_ports\_lookup** function returns an array of the well-known system ports that are currently registered for the specified task. Note that the task holds only send rights for the ports.

Registered ports are those ports that are used by the run-time system to initialize a task. To register system ports for a task, use the **mach\_ports\_register** function.

#### SECURITY

The requesting task must hold tsv\_lookup\_ports permission to target\_task.

## PARAMETERS

target\_task

[in task port] The task whose currently registered ports are to be returned.

init\_port\_set

[out pointer to dynamic array of registered ports] The returned array of ports.

init\_port\_count

[out scalar] The number of returned port rights.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: mach\_ports\_register.

# mach\_ports\_register

Function — Registers an array of well-known system ports

#### **SYNOPSIS**

kern\_return\_t mach\_ports\_register (mach\_port\_t target\_task, mach\_port\_array\_t init\_port\_set, mach\_msg\_type\_number\_t init\_port\_array\_count);

#### DESCRIPTION

The **mach\_ports\_register** function registers an array of well-known system ports for the specified task. The task holds only send rights for the registered ports. The valid well-known system ports are:

- The port for the Network Name Server.
- The port for the Environment Manager.
- The port for the Service server.

Each port must be placed in a specific slot in the array. The slot numbers are defined (in **mach.h**) by the global constants NAME\_SERVER\_SLOT, ENVIRONMENT\_SLOT, and SERVICE\_SLOT.

A task can retrieve the currently registered ports by using the **mach\_ports\_lookup** function.

#### SECURITY

The requesting task must hold *tsv\_register\_ports* permission to *target\_task*.

#### PARAMETERS

target\_task

[in task port] The task for which the ports are to be registered.

#### init\_port\_set

[in pointer to array of registered ports] The array of ports to register.

#### init\_port\_array\_count

[in scalar] The number of ports in the array. Note that while this is a variable, the kernel accepts only a limited number of ports. The maximum number of ports is defined by the global constant TASK\_PORT\_REGISTER\_MAX.

#### NOTES

When a new task is created (with **task\_create**), the child task can inherit the parent's registered ports. Note that child tasks do not automatically acquire rights to these ports. They must use **mach\_ports\_lookup** to get them. It is intended that port registration be used only for task initialization, and then only by runtime support modules.

A parent task has three choices when passing registered ports to child tasks:

- The parent task can do nothing. In this case, all child tasks inherit access to the same ports that the parent has.
- The parent task can use **mach\_ports\_register** to modify its set of registered ports before creating child tasks. In this case, the child tasks get access to the modified set of ports. After creating its child tasks, the parent can use **mach\_ports\_register** again to reset its registered ports.
- The parent task can first create a specific child task and then use mach\_ports\_register to modify the child's inherited set of ports, before starting the child's thread(s). The parent must specify the child's task port, rather than its own, on the call to mach\_ports\_register.

Tasks other than the Network Name Server and the Environment Manager should not need access to the Service port. The Network Name Server port is the same for all tasks on a given machine. The Environment port is the only port likely to have different values for different tasks.

Registered ports are restricted to those ports that are used by the run-time system to initialize a task. A parent task can pass other ports to its child tasks through:

- An initial message (see mach\_msg).
- The Network Name Server, for public ports.
- The Environment Manager, for private ports.

#### **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: mach\_msg, mach\_ports\_lookup.

# mach\_task\_self

System Trap — Returns the task self port

#### LIBRARY

#include <mach/mach\_traps.h>

## **SYNOPSIS**

## DESCRIPTION

The mach\_task\_self function returns send rights to the task's kernel port.

# SECURITY

The requesting task must hold *tsv\_get\_task\_kernel\_port* permission to the requesting task's task port.

## PARAMETERS

None

## NOTES

The include file **<mach\_init.h>** included by **<mach.h>** redefines this function call to simply return the value **mach\_task\_self\_**, cached by the Mach run-time.

# **RETURN VALUE**

[task-self port] Send rights to the task's port.

## **RELATED INFORMATION**

Functions: task\_info.

# task\_change\_sid

Function — Changes the SID of a task

#### **SYNOPSIS**

kern\_return\_t **task\_change\_sid** (mach\_port\_t security id t

target\_task,
 new\_sid);

#### DESCRIPTION

The *task\_change\_sid* function changes the security identifier (SID) of *target\_task* to *new\_sid*. Currently, only the authentication identifier (AID) portion of the SID is allowed to change. Hence the mandatory identifier (MID) field of *new\_sid* must be either 0 or the same as the MID field of the *target\_task*'s SID.

## SECURITY

The following permissions are required:

- the requesting task must hold tsv\_change\_sid permission to target\_task's task port
- the requesting task must hold *tsv\_make\_sid* permission to *new\_sid*.
- The *target\_task* must hold *tsv\_transition\_sid* to *new\_sid*.

## PARAMETERS

#### target\_task

[in task port] The port for the task whose SID is being changed.

#### new\_sid

[in security id] The new SID with which *target\_task* will be labeled.

#### **RETURN VALUE**

Generic errors apply.

#### **RELATED INFORMATION**

Functions: None

# task\_create/task\_create\_secure

Function — Creates a task

#### SYNOPSIS

kern\_return\_t **task\_create** (mach\_port\_t boolean\_t mach\_port\_t\*

kern\_return\_t task\_create\_secure

(mach\_port\_t boolean\_t mach\_port\_t\* security\_id\_t parent\_task, inherit\_memory, child\_task, subj\_sid);

parent\_task,

child\_task);

inherit\_memory,

#### DESCRIPTION

The **task\_create** and **task\_create\_secure** functions create a new task from *parent\_task* and return the name of the new task in *child\_task*. The child task acquires shared or copied parts of the parent's address space (see **vm\_inherit**). The child task initially contains no threads.

The child task receives the three following special ports, which are created or copied for it at task creation:

- task\_kernel\_port The port by which the kernel knows the new child task. The child task holds a send right for this port. The port name is also returned to the calling task.
- task\_bootstrap\_port The port to which the child task can send a message requesting return of any system service ports that it needs (for example, a port to the Network Name Server or the Environment Manager). The child task inherits a send right for this port from the parent task. The child task can use task\_set\_special\_port to change this port.
- task\_exception\_port A default exception port for the child task, inherited from the parent task. The exception port is the port to which the kernel sends exception messages. Exceptions are synchronous interruptions to the normal flow of program control caused by the program itself. Some exceptions are handled transparently by the kernel, but others must be reported to the program. The child task, or any one of its threads, can change the default exception port to take an active role in exception handling (see task\_set\_special\_port).

The child task also inherits the following ports:

- [sample port] The port to which PC sampling messages are to be sent.
- [registered ports] Ports to system services.

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In addition to creating a new task, **task\_create\_secure** assigns the specified security identifier to the new task. Because of the necessity to control what the parent task may do to the child task via *child\_task* the newly created task structure state is set to EMPTY to ensure that **thread\_create**, **thread\_set\_state** and **thread\_resume** sequence uses the secure variants of these requests. This assures the proper start up sequence upon a cross context task creation. **task\_create** sets the created task structure state to TASK\_READY and does not require special permissions to or processing sequences for the parent task to initiate processing in the child task.

#### SECURITY

For **task\_create** the requesting task must hold *tsv\_create\_task* permission to *parent\_task*. For **task\_create\_secure** the following permissions are required:

- the requesting task must hold tsv\_create\_task\_secure permission to parent\_task's task port and
- the requesting task must hold tsv\_cross\_context\_create to child\_task's task port.
- The *parent\_task* must hold *tsv\_cross\_context\_inherit* to *child\_task*'s task port.

The permission to inherited memory in tasks created with the use of **task\_create\_secure** is as determined by the system's security policy. It will be based on the relationship between the new task's security identity and the security identifier associated with the memory.

For **task\_create** and for the case where no subject security identifier is provided on a **task\_create\_secure**, the child task is created with a subject security identifier that is the same as *parent\_task*'s subject security identifier.

#### PARAMETERS

#### parent\_task

[in task port] The port for the task from which to draw the child task's port rights, resource limits, and address space.

#### inherit\_memory

[in scalar] Address space inheritance indicator. If true, the child task inherits the address space of the parent task. If false, the kernel assigns the child task an empty address space.

#### child\_task

[out task port] The kernel-assigned port name for the new task.

#### subj\_sid

[in security id] The security identifier to be associated with the child task.

## **RETURN VALUE**

Generic errors apply.

## **RELATED INFORMATION**

Functions: task\_get\_special\_port, task\_resume, task\_set\_special\_port, task\_suspend, task\_terminate, task\_threads, thread\_create, thread\_create\_secure, thread\_resume, thread\_resume\_secure, thread\_set\_state, thread\_set\_state\_secure, vm\_inherit, task\_sample, norma\_task\_create.

# task\_get\_emulation\_vector

Function — Return user-level handlers for system calls.

#### **SYNOPSIS**

kern\_return\_t **task\_get\_emulation\_vector** (mach\_port\_t int\*

emulation\_vector\_t\* mach\_msg\_type\_number\_t\* task, vector\_start, emulation\_vector, emulation\_vector\_count);

#### DESCRIPTION

The **task\_get\_emulation\_vector** function returns the user-level syscall handler entrypoint addresses.

#### SECURITY

The requesting task must hold *tsv\_get\_emulation* permission to *task*.

#### PARAMETERS

task

[in task port] The port for the task for which the system call handler addresses are desired.

#### vector\_start

[out scalar] The syscall number corresponding to the first element of *emulation\_vector*.

#### emulation\_vector

[out pointer to dynamic array of *vm\_offset\_t*] Pointer to the returned array of routine entrypoints for the system calls starting with syscall number *vector\_start*.

emulation\_vector\_count

[out scalar] The number of entries filled by the kernel.

#### NOTES

This interface is machine word length specific because of the virtual addresses in the *emulation\_vector* parameter

#### **RETURN VALUE**

Only generic errors apply.

# **RELATED INFORMATION**

Functions: task\_set\_emulation, task\_set\_emulation\_vector.

# task\_get\_special\_port

Function — Returns a send right to a special port

#### **SYNOPSIS**

kern\_return\_t **task\_get\_special\_port** (mach\_port\_t int mach\_port\_t\*

task, which\_port, special\_port);

#### task\_get\_bootstrap\_port

Macro form

kern\_return\_t **task\_get\_bootstrap\_port** (mach\_port\_t mach\_port\_t\*

task, special\_port)

⇒ task\_get\_special\_port (task, TASK\_BOOTSTRAP\_PORT, special\_port)

#### task\_get\_exception\_port

Macro form

kern\_return\_t **task\_get\_exception\_port** (mach\_port\_t mach\_port\_t\*

task, special\_port)

⇒ task\_get\_special\_port (task, TASK\_EXCEPTION\_PORT, special\_port)

#### task\_get\_kernel\_port

Macro form

kern\_return\_t **task\_get\_kernel\_port** (mach\_port\_t *task,* mach\_port\_t\* *special\_port*)

⇒ task\_get\_special\_port (task, TASK\_KERNEL\_PORT, special\_port)

## DESCRIPTION

The **task\_get\_special\_port** function returns a send right for a special port belonging to *task*.

If one task has a send right for the kernel port of another task, it can use the port to perform kernel operations for the other task. Send rights for a kernel port normally are held only by the task to which the port belongs, or by the task's parent task. Using the **mach\_msg** function, however, any task can pass a send right for its kernel port to another task.

## SECURITY

The requesting task must hold *tsv\_get\_task\_boot\_port*, *tsv\_get\_task\_exception\_port* or *tsv\_get\_task\_kernel\_port* permission to *task* to get, respectively, the target task's boot port, exception port or kernel port.

## PARAMETERS

#### task

[in task port] The port for the task for which to return the port's send right.

#### which\_port

[in scalar] The special port for which the send right is requested. Valid values are:

#### TASK\_KERNEL\_PORT

[task-self port] The port used to control this task. Used to send messages that affect the task. This is the port returned by **mach\_task\_self**.

#### TASK\_BOOTSTRAP\_PORT

[bootstrap port] The task's bootstrap port. Used to send messages requesting return of other system service ports.

#### TASK\_EXCEPTION\_PORT

[exception port] The task's exception port. Used to receive exception messages from the kernel.

#### special\_port

[out task-special port] The returned value for the port.

#### **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: mach\_task\_self, task\_create, task\_set\_special\_port, thread\_get\_special\_port, thread\_set\_special\_port, mach\_task\_self.

# task\_info

Function — Returns information about a task

#### SYNOPSIS

kern\_return\_t **task\_info** (mach\_port\_t int task\_info\_t mach\_msg\_type\_number\_t\*

target\_task, flavor, task\_info, task\_infoCnt);

## DESCRIPTION

The **task\_info** function returns an information array of type *flavor*.

## SECURITY

The requesting task must hold *tsv\_get\_task\_info* permission to *target\_task*.

## PARAMETERS

#### target\_task

[in task port] The port for the task for which the information is to be returned.

flavor

[in scalar] The type of information to be returned. Valid values are:

#### TASK\_BASIC\_INFO

Returns basic information about the task, such as the task's suspend count and number of resident pages. The structure returned is **task\_basic\_info**, whose size is given by TASK\_BASIC\_INFO\_COUNT.

#### TASK\_SECURE\_INFO

Returns basic information about the task, such as the task's suspend count, number of resident pages and security identifier. The structure returned is **task\_basic\_secure\_info**, whose size is given by TASK\_BASIC\_SECURE\_INFO\_COUNT.

#### TASK\_THREAD\_TIMES\_INFO

Returns system and user space run-times for live threads. The structure returned is **task\_thread\_times\_info**, whose size is given by TASK\_THREAD\_TIMES\_INFO\_COUNT.

#### task\_info

[out array of *int*] Information about the specified task.

task\_infoCnt

[pointer to in/out scalar] On input, the maximum size of the info buffer; on output, the returned size of the information structure (in units of sizeof (*int*)). The maximum size is defined by TASK\_INFO\_MAX.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: task\_get\_special\_port, task\_set\_special\_port, task\_threads, thread\_info, thread\_get\_state, thread\_set\_state.

Data Structures: task\_basic\_info, task\_thread\_times\_info.

# task\_resume

Function — Resume a task

## **SYNOPSIS**

kern\_return\_t **task\_resume** (mach\_port\_t

task);

## DESCRIPTION

The **task\_resume** function decrements the suspend count for *task*. If the task's suspend count goes to zero, the function resumes any suspended threads within the task. To resume a given thread, the thread's own suspend count must also be zero.

## SECURITY

The requesting task must hold *tsv\_resume\_task* permission to *task*.

#### PARAMETERS

task

[in task port] The port for the task to be resumed.

## **RETURN VALUE**

Only generic errors apply.

# **RELATED INFORMATION**

Functions: task\_create, task\_info, task\_suspend, task\_terminate, thread\_info, thread\_resume, thread\_suspend.

# task\_sample

Function — Perform periodic PC sampling for a task

#### **SYNOPSIS**

	kern_return_t <b>task_enable_pc_sampling</b>
task,	(mach_port_t
*ticks;	int
flavor);	sampled_pc_flavor_t
	kern_return_t task_disable_pc_sampling
task,	(mach_port_t
*sample_cnt;	int
flavor);	sampled_pc_flavor_t
	kern_return_t task_get_sampled_pcs
task,	(mach_port_t
*seqno;	unsigned
sampled_pcs[],	sampled_pc_t
*sample_cnt);	int

# DESCRIPTION

These functions cause the program counter (PC) of the specified *task* to be sampled periodically (whenever one of the task's threads happens to be running at the time of the kernel's "hardclock" interrupt). The set of PC sample values obtained are saved in buffers.

## SECURITY

These functions require that the requesting task hold *tsv\_sample\_task* permission to *task*.

## PARAMETERS

thread

[in thread port] Thread whose PC is to be sampled

ticks

[out scalar] the kernel's idea of clock granularity (ticks per second). Don't trust this.

flavor

[in structure] The sampling flavor, which can be any of the following flavors defined in pc\_sample.h.

SAMPLED\_PC\_PERIODIC, SAMPLED\_PC\_VM\_ZFILL\_FAULTS, SAMPLED\_PC\_VM\_REACTIVATION\_FAULTS, SAMPLED\_PC\_VM\_PAGIN\_FAULTS, SAMPLED\_PC\_VM\_COM\_FAULTS, SAMPLED\_PC\_VM\_FAUTLS\_ANY, SAMPLED\_PC\_VM\_FAULTS.

#### seqno

[out scalar] The sequence number of the sampled PC's. This is useful for determining when a collector thread has missed a sample.

#### sampled\_pcs

[out structure] The sampled PCs for threads in *task*. A sample contains three fields: a thread-specific unique identifier, a PC value and the type of sample as per flavor.

#### sample\_cnt

[out scaler] The number of sample elements in the kernel for the named task or thread.

#### **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: thread\_enable\_pc\_sampling, thread\_disable\_pc\_sampling, thread\_get\_sampled\_pcs.

## task\_set\_emulation

Function — Establish a user-level handler for a system call.

#### **SYNOPSIS**

kern\_return\_t task\_set\_emulation

(mach\_port\_t vm\_address\_t int task,
routine\_entry\_pt,
syscall\_number);

## DESCRIPTION

The **task\_set\_emulation** function establishes a handler within the task for a particular system call. When a thread executes a system call with this particular number, the system call will be redirected to the specified routine within the task's address space. This is expected to be an address within the transparent emulation library.

These emulation handler addresses are inherited by child processes.

## SECURITY

The requesting task must hold *tsv\_set\_emulation* permission to *task*.

## PARAMETERS

task

[in task port] The port for the task for which to establish the system call handler.

routine\_entry\_pt

[in scalar] The address within the task of the handler for this particular system call.

syscall\_number

[in scalar] The number of the system call to be handled by this handler.

### NOTES

This interface is machine word length specific because of the virtual address parameter.

### **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: task\_set\_emulation\_vector, task\_get\_emulation\_vector.

## task\_set\_emulation\_vector

Function — Establishes user-level handlers for system calls.

#### SYNOPSIS

kern\_return\_ttask\_set\_emulation\_vector(mach\_port\_ttask,intvector\_start,emulation\_vector\_temulation\_vector;mach\_msg\_type\_number\_temulation\_vector\_count);

## DESCRIPTION

The **task\_set\_emulation\_vector** function establishes a handler within the task for a set of system calls. When a thread executes a system call with one of these numbers, the system call will be redirected to the corresponding routine within the task's address space. This is expected to be an address within the transparent emulation library.

These emulation handler addresses are inherited by child processes.

## SECURITY

The requesting task must hold *tsv\_set\_emulation* permission to *task*.

## PARAMETERS

task

[in task port] The port for the task for which to establish the system call handler.

vector\_start

[in scalar] The syscall number corresponding to the first element of *emulation\_vector*.

emulation\_vector

[in pointer to array of *vm\_offset\_t*] An array of routine entrypoints for the system calls starting with syscall number *vector\_start*.

emulation\_vector\_count

[in scalar] The number of elements in *emulation\_vector*.

### NOTES

This interface is machine word length specific because of the virtual addresses in the *emulation\_vector* parameter.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: task\_set\_emulation, task\_get\_emulation\_vector.

## task\_set\_special\_port

**Function** — Sets a special port for a task

### **SYNOPSIS**

kern_return_t <b>task_set_special_port</b>	
(mach_port_t	task,
int	which_port,
mach_port_t	special_port);
task_set_bootstrap_port Macro form	
kern_return_t task_set_bootstrap_port	
(mach_port_t	task,
mach_port_t	special_port)

⇒ task\_set\_special\_port (task, TASK\_BOOTSTRAP\_PORT, special\_port)

### task\_set\_exception\_port

Macro form

kern_return_t task_set_exception_port	
(mach_port_t	task,
mach_port_t	special_port)

⇒ task\_set\_special\_port (*task*, TASK\_EXCEPTION\_PORT, *special\_port*).

### task\_set\_kernel\_port

Macro form

kern_return_t task_set_kernel_port	
(mach_port_t	task,
mach_port_t	special_port)

⇒ task\_set\_special\_port (*task*, TASK\_KERNEL\_PORT, *special\_port*)

## **DESCRIPTION**

The **task\_set\_special\_port** function sets a special port belonging to *task*.

## **SECURITY**

The requesting *tsv\_set\_task\_boot\_port*, task must hold tsv\_set\_task\_exception\_port or tsv\_set\_task\_kernel\_port permission to task to set, respectively, *task*'s boot port, exception port or kernel port.

## **PARAMETERS**

task

[in task port] The task for which to set the port.

which\_port

[in scalar] The special port to be set. Valid values are:

## TASK\_BOOTSTRAP\_PORT

[bootstrap port] The task's bootstrap port. Used to send messages requesting return of other system service ports.

#### TASK\_EXCEPTION\_PORT

[exception port] The task's exception port. Used to receive exception messages from the kernel.

#### TASK\_KERNEL\_PORT

[task-self port] The task's kernel port. Used by the kernel to receive messages from the task. This is the port returned by **mach\_task\_self**.

special\_port

[in task-special port] The value for the port.

## **RETURN VALUE**

Only generic errors apply.

### **RELATED INFORMATION**

Functions: task\_create, task\_get\_special\_port, exception\_raise, mach\_task\_self, thread\_get\_special\_port, thread\_set\_special\_port.

## task\_suspend

Function — Suspends a task

### **SYNOPSIS**

kern\_return\_t **task\_suspend** (mach\_port\_t

task);

## DESCRIPTION

The **task\_suspend** function increments the suspend count for *task* and stops all threads within the task. As long as the suspend count is positive, no newly-created threads can execute. The function does not return until all of the task's threads have been suspended.

## SECURITY

The requesting task must hold *tsv\_suspend\_task* permission to *task*.

## PARAMETERS

task

[in task port] The port for the task to be suspended.

## NOTES

To resume a suspended task and its threads, use **task\_resume**. If the suspend count is greater than one, you must issue **task\_resume** that number of times.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: task\_create, task\_info, task\_resume, task\_terminate, thread\_suspend.

## task\_terminate

Function — Destroys a task

## **SYNOPSIS**

kern\_return\_t **task\_terminate** (mach\_port\_t

task);

## DESCRIPTION

The **task\_terminate** function kills *task* and all its threads, if any. The kernel frees all resources that are in use by the task. The kernel destroys any port for which the task holds the receive right.

### **SECURITY**

The requesting task must hold *tsv\_terminate\_task* permission to *task*.

## PARAMETERS

task

[in task port] The port for the task to be destroyed.

### **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: task\_create, task\_suspend, task\_resume, thread\_terminate, thread\_suspend.

## task\_threads

Function — Returns a list of the threads within a task

### **SYNOPSIS**

kern\_return\_t **task\_threads** (mach\_port\_t thread\_array\_t\* mach\_msg\_type\_number\_t\*

task, thread\_list, thread\_count);

## DESCRIPTION

The **task\_threads** function returns a list of the threads within *task*. The calling task or thread also receives a send right to the kernel port for each listed thread.

## SECURITY

The requesting task must hold *tsv\_get\_task\_threads* permission to *task*.

## PARAMETERS

task

[in task port] The port for the task for which the thread list is to be returned.

#### thread\_list

[out pointer to dynamic array of thread ports] The returned list of threads within *task*, in no particular order.

#### thread\_count

[out scalar] The returned count of threads in thread\_list.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: thread\_create, thread\_terminate, thread\_suspend.

Task Interface

# CHAPTER 8 Host Interface

This chapter discusses the specifics of the kernel's host interfaces. Included are functions that return status information for a host, such as kernel statistics.

Note that hosts are named both by a name port, which allows the holder to request information about the host, and a control port, which provides full control access. The control port for a host is provided to the bootstrap task for that host.

## host\_adjust\_time

Function —Gradually change the time

### LIBRARY

#include <mach/mach\_host.h>

## **SYNOPSIS**

kern\_return\_t **host\_adjust\_time** (mach\_port\_t time\_value\_t time\_value\_t\*

*host\_priv, new\_adjustment, old\_adjustment);* 

## DESCRIPTION

The **host\_adjust\_time** function arranges for the time on a specified host to be gradually changed by an adjustment value.

### **SECURITY**

The requesting task must hold *hpsv\_set\_time* permission to *host\_priv*.

## PARAMETERS

host\_priv

[in host-control port] The control port the host for which the time is to be set.

*new\_adjustment* [in structure] New adjustment value.

old\_adjustment

[out structure] Old adjustment value.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: host\_get\_time, host\_set\_time.

Data Structures: time\_value.

## host\_get\_boot\_info

Function — Return operator boot information

#### LIBRARY

#include <mach/mach\_host.h>

### **SYNOPSIS**

kern\_return\_t **host\_get\_boot\_info** (mach\_port\_t

kernel\_boot\_info\_t

priv\_host, boot\_info);

## DESCRIPTION

The **host\_get\_boot\_info** function returns the boot-time information string supplied by the operator when *priv\_host* was initialized. The constant KERNEL\_BOOT\_INFO\_MAX (in **mach/host\_info.h**) should be used to dimension storage for the returned string.

## SECURITY

The requesting task must hold *hpsv\_get\_boot\_info* permission to *priv\_host*.

## PARAMETERS

priv\_host

[in host-control port] The control port for the host for which information is to be obtained.

#### boot\_info

[out array of char] Character string providing the operator boot info

### **RETURN VALUE**

Only generic errors apply.

### **RELATED INFORMATION**

Functions: host\_info.

## host\_get\_special\_port

Function — Return a send right to a special port

### LIBRARY

#include <mach.h>

## **SYNOPSIS**

kern\_return\_t **host\_get\_special\_port** (mach\_port\_t int mach\_port\_t\*

host, port\_label, special\_port);

#### DESCRIPTION

The **host\_get\_special\_port** function returns a send right to *special\_port* as requested in *port\_label*.

## **SECURITY**

The requesting task must have *hsv\_get\_special\_port* to *host*. Depending on the value of *port\_label*, the requesting task must also hold one of the following permissions to *host*:

- hsv\_get\_audit\_port
- hsv\_get\_authentication\_port
- hsv\_get\_crypto\_port
- hsv\_get\_host\_control\_port
- hsv\_get\_negotiation\_server\_port
- hsv\_get\_network\_server\_port
- hsv\_get\_security\_master\_port
- hsv\_get\_security\_client\_port

*hsv\_get\_host\_control\_port* is also used to control access to the master device port.

## PARAMETERS

host\_name\_port

[in host-name port] The host name port to which the request is sent.

port\_label

[in scalar] Specifies which special port the function should return. This parameter can take on one of the following values:

- AUDIT\_SERVER\_PORT
- AUTHENTICATION\_SERVER\_PORT
- CRYPTO\_SERVER\_PORT
- HOST\_CONTROL\_PORT
- MASTER\_DEVICE\_PORT
- NEGOTIATION\_SERVER\_PORT
- NETWORK\_SECURITY\_SERVER\_PORT
- SECURITY\_SERVER\_CLIENT\_PORT
- SECURITY\_SERVER\_MASTER\_PORT

### special\_port

[out port] A send right to the requested port.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: host\_set\_special\_port.

## host\_get\_time

Function — Return the current time.

## LIBRARY

#include <mach/mach\_host.h>

## **SYNOPSIS**

kern\_return\_t **host\_get\_time** (mach\_port\_t time\_value\_t\*

*host, current\_time*);

### **DESCRIPTION**

The host\_get\_time function returns the current time as seen by that host.

## **SECURITY**

The requesting task must hold *hsv\_get\_time* permission to *host*.

## PARAMETERS

host

[in host-name port] The name port of the host from which the time is to be obtained.

current\_time

[out structure] Returned time value.

## **RETURN VALUE**

Only generic errors apply.

### **RELATED INFORMATION**

Functions: host\_adjust\_time, host\_set\_time.

Data Structures: time\_value.

## host\_info

Function — Returns information about a host

### LIBRARY

#include <mach/mach\_host.h>

### **SYNOPSIS**

kern\_return\_t **host\_info** 

— —	
(mach_port_t	host,
int	flavor,
host_info_t	host_info,
mach_msg_type_number_t*	<pre>host_infoCnt);</pre>

## **DESCRIPTION**

The **host\_info** function returns selected information about a host, as specified by *flavor*.

## SECURITY

The requesting task must hold *hsv\_get\_host\_info* permission to *host*.

## PARAMETERS

#### host

[in host-name port] The name port for the host for which information is to be obtained.

### flavor

[in scalar] The type of statistics desired.

## HOST\_BASIC\_INFO

Basic information (number of processors, amount of memory). The returned structure is **host\_basic\_info** of size HOST\_BASIC\_INFO\_COUNT.

## HOST\_LOAD\_INFO

Scheduling statistics. The returned structure is **host\_load\_info** of size HOST\_LOAD\_INFO\_COUNT.

### HOST\_PROCESSOR\_SLOTS

An array of the processor slot numbers (natural-sized units) for active processors.

### HOST\_SCHED\_INFO

Basic restrictions of the kernel's scheduling, minimum quantum and time-out value. The returned structure is **host\_sched\_info** of size HOST\_SCHED\_INFO\_COUNT

#### host\_info

[out array of *int*] Statistics about the specified host.

#### host\_infoCnt

[pointer to in/out scalar] On input, the maximum size of the info buffer; on output, the size of the information structure (in units of sizeof *(int)*).

### NOTES

This interface is machine word length specific because of the memory size returned by HOST\_BASIC\_INFO.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: host\_get\_boot\_info, host\_kernel\_version, host\_processors,
processor\_info.

Data Structures: host\_basic\_info, host\_load\_info, host\_sched\_info

## host\_kernel\_version

Function — Returns kernel version information for a host

### LIBRARY

#include <mach/mach\_host.h>

### **SYNOPSIS**

kern_return_t host_kernel_version	
(mach_port_t	host,
kernel_version_t	version);

## DESCRIPTION

The **host\_kernel\_version** function returns the version string compiled into the kernel executing on *host* at the time it was built. This describes the version of the kernel. The constant KERNEL\_VERSION\_MAX (in **mach/host\_info.h**) should be used to dimension storage for the returned string if the *kernel\_version\_t* declaration is not used.

## SECURITY

The requesting task must hold *hsv\_get\_host\_version* permission to *host*.

## PARAMETERS

host

[in host-name port] The name port for the host for which information is to be obtained.

#### version

[out array of *char*] Character string describing the kernel version executing on *host* 

### **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: host\_info.

## host\_reboot

Function — Reboot this host

## LIBRARY

#include <mach/mach\_host.h>

## **SYNOPSIS**

kern\_return\_t **host\_reboot** (mach\_port\_t int

*host\_priv, options*);

## DESCRIPTION

The host\_reboot function reboots the specified host.

## SECURITY

The requesting task must hold hpsv\_reboot \_host permission to host\_priv.

## PARAMETERS

host\_priv

[in host-control port] The control port the host to be re-booted.

options

[in scalar] Reboot options. See **<sys/reboot.h>** for details.

### **NOTES**

If successful, this call will not return.

## **RETURN VALUE**

Only generic errors apply.

## host\_set\_special\_port

Function — Sets special kernel ports

## LIBRARY

#include <mach.h>

## **SYNOPSIS**

kern_return_t host_set_special_port	
(mach_port_t	host,
int	port_label,
mach_port_t	port_value);

## DESCRIPTION

The **host\_set\_special\_port** function supplies a port to the specified host for use as the port selected by the *port\_label*.

## SECURITY

The requesting task must have *hsv\_set\_special\_port* to *host*. Depending on the value of *port\_label*, the requesting task must also hold one of the following permissions to *host*:

- hsv\_set\_audit\_port
- *hsv\_set\_authentication\_port*
- hsv\_set\_crypto\_port
- hsv\_set\_negotiation\_port
- hsv\_set\_network\_ss\_port
- hsv\_set\_security\_master\_port
- *hsv\_set\_security\_client\_port*

## PARAMETERS

#### host

[in host-name port] The name port for the host for which the specified port will be set.

#### port\_label

[in scalar] A label for which the special port will be set. This parameter can take on one of the following values:

- AUDIT\_SERVER\_PORT
- AUTHENTICATION\_SERVER\_PORT
- CRYPTO\_SERVER\_PORT

- NEGOTIATION\_SERVER\_PORT
- NETWORK\_SECURITY\_SERVER\_PORT
- SECURITY\_SERVER\_MASTER\_PORT
- SECURITY\_SERVER\_CLIENT\_PORT

port\_value

[in port] A port for the kernel to use for the selected operation.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: host\_get\_special\_port

## host\_set\_time

Function — Sets the time

### LIBRARY

#include <mach/mach\_host.h>

## **SYNOPSIS**

kern\_return\_t **host\_set\_time** (mach\_port\_t time\_value\_t

host\_priv,
new\_time);

## DESCRIPTION

The host\_set\_time function establishes the time on the specified host.

## SECURITY

The requesting task must hold *hpsv\_set\_time* permission to *host\_priv*.

## PARAMETERS

host\_priv

[in host-control port] The control port for the host for which the time is to be set.

### new\_time

[in structure] Time to be set.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: host\_adjust\_time, host\_get\_time.

Data Structures: time\_value.

## mach\_host\_self

System Trap — Returns the host self port

## LIBRARY

#include <mach/mach\_traps.h>

## **SYNOPSIS**

## SECURITY

The requesting task must hold *hsv\_get\_host\_name* permission to the processor's host name port.

## DESCRIPTION

The mach\_host\_self function returns send rights to the current host's name port.

## PARAMETERS

None

## **RETURN VALUE**

[host-name port] Send rights to the host's name port.

## **RELATED INFORMATION**

Functions: host\_info.

# CHAPTER 9 Processor Management and Scheduling Interface

This chapter discusses the specifics of the kernel's processor and processor set interfaces. This includes functions to control processors, change their assignments, assign tasks and threads to processors, and processor status returning functions.

Note that processor sets have two ports that name them: a name port which allows information to be requested about them, and a control port which allows full access. The control port for a processor set is provided to the creator of the set.

Processors have only a single port that names them. The host control port is needed to obtain these processor ports.

## host\_processor\_set\_priv

**Function** — Translates a processor set name port into a processor set control port

## LIBRARY

#include <mach/mach\_host.h>

### **SYNOPSIS**

kern\_return\_t host\_processor\_set\_priv

(mach_port_t	
mach_port_t	
mach_port_t*	

*host\_priv, set\_name, processor\_set*);

## DESCRIPTION

The **host\_processor\_set\_priv** function returns send rights for the control port for a specified processor set currently existing on *host\_priv*.

### SECURITY

The requesting task must hold hpsv\_pset\_ctrl\_port permission to host\_priv.

## PARAMETERS

host\_priv

[in host-control port] The control port for the host for which the processor set is desired.

set\_name

[in processor-set-name port] The name port for the processor set desired.

processor\_set

[out processor-set-control port] The returned processor set control port.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: host\_processor\_sets, processor\_set\_create, processor\_set\_tasks, processor\_set\_threads.

## host\_processor\_sets

Function — Returns processor set ports for a host

#### LIBRARY

#include <mach/mach\_host.h>

### **SYNOPSIS**

kern\_return\_t host\_processor\_sets

(mach_port_t	host,
processor_set_name_array_t*	processor_set_list,
mach_msg_type_number_t*	processor_set_count);

## DESCRIPTION

The **host\_processor\_sets** function returns send rights for the name ports for each processor set currently existing on *host*.

## SECURITY

The requesting task must hold *hsv\_pset\_names* permission to *host*.

## PARAMETERS

host

[in host-name port] The name port for the host for which the processor sets are desired.

processor\_set\_list

[out pointer to dynamic array of processor-set-name ports] The set of processor set name ports for those currently existing on *host*; no particular order is guaranteed.

processor\_set\_count

[out scalar] The number of processor set names returned.

### NOTES

If control ports to the processor sets are needed, use **host\_processor\_set\_priv**.

*processor\_set\_list* is automatically allocated by the kernel, as if by **vm\_allocate**. It is good practice to **vm\_deallocate** this space when it is no longer needed.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions:host\_processor\_set\_priv,processor\_set\_tasks, processor\_set\_threads.

processor\_set\_create,

## host\_processors

Function — Gets processor ports for a host

#### LIBRARY

#include <mach/mach\_host.h>

### **SYNOPSIS**

kern\_return\_t host\_processors

(mach\_port\_t processor\_array\_t\* mach\_msg\_type\_number\_t\* host\_priv, processor\_list, processor\_count);

## **DESCRIPTION**

The **host\_processors** function returns an array of send right ports for each processor existing on *host\_priv*.

## SECURITY

The requesting task must hold *hpsv\_get\_host\_processors* permission to *host\_priv*.

## PARAMETERS

host\_priv

[in host-control port] The control port for the desired host.

processor\_list

[out pointer to dynamic array of processor ports] The set of processors existing on *host\_priv*; no particular order is guaranteed.

processor\_count

[out scalar] The number of ports returned in *processor\_list*.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: processor\_start, processor\_exit, processor\_info, processor\_control.

## processor\_assign

Function — Assign a processor to a processor set

### LIBRARY

#include <mach/mach\_host.h>

## **SYNOPSIS**

kern\_return\_t **processor\_assign** (mach\_port\_t mach\_port\_t boolean\_t

processor, new\_set, wait);

#### DESCRIPTION

The **processor\_assign** function assigns *processor* to the set *new\_set*. After the assignment is completed, the processor only executes threads that are assigned to that processor set. Any previous assignment of the processor is nullified. The master processor cannot be reassigned.

The *wait* argument indicates whether the caller should wait for the assignment to be completed or should return immediately. Dedicated kernel threads are used to perform processor assignment, so setting *wait* to FALSE allows assignment requests to be queued and performed quicker, especially if the kernel has more than one dedicated internal thread for processor assignment.

All processors take clock interrupts at all times. Redirection of other device interrupts away from processors assigned to other than the default processor set is machine dependent.

### SECURITY

The requesting task must hold *psv\_assign\_processor\_to\_set* permission to *processor* and *pssv\_assign\_processor* to *new\_set*.

#### PARAMETERS

processor

[in processor port] The processor to be assigned.

#### new\_set

[in processor-set-control port] The control port for the processor set into which the processor is to be assigned.

wait

[in scalar] True if the call should wait for the completion of the assignment.

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: processor\_set\_create, processor\_set\_info, task\_assign, thread\_assign.

## processor\_control

Function — Do something to a processor

### LIBRARY

#include <mach/mach\_host.h>

## **SYNOPSIS**

 kern\_return\_t processor\_control
 (mach\_port\_t
 processor,

 processor\_info\_t
 cmd,

 mach\_msg\_type\_number\_t
 count);

## DESCRIPTION

The **processor\_control** function allows privileged software to control a processor in a multi-processor that so allows it. The interpretation of *cmd* is machine dependent.

### SECURITY

The requesting task must hold *psv\_may\_control\_processor* permission to *processor*.

### PARAMETERS

processor

[in processor port] The processor to be controlled.

cmd

[pointer to in array of *int*] An array containing the command to be applied to the processor.

```
count
```

[in scalar] The size of the *cmd* array.

### **NOTES**

These operations are machine dependent. They may do nothing.

## **RETURN VALUE**

#### KERN\_FAILURE

The operation was not performed. A likely reason is that it is not supported on this processor.

## **RELATED INFORMATION**

Functions: processor\_start, processor\_exit, processor\_info, host\_processors.

## processor\_exit

Function — Exit a processor

### LIBRARY

#include <mach/mach\_host.h>

## **SYNOPSIS**

kern\_return\_t **processor\_exit** (mach\_port\_t

processor);

## DESCRIPTION

The **processor\_exit** function allows privileged software to exit a processor in a multi-processor that so allows it. An exited processor is removed from the processor set to which it was assigned and ceases to be active. The interpretation of this operation is machine dependent.

## **SECURITY**

The requesting task must hold *psv\_may\_control\_processor* permission to *processor*.

## PARAMETERS

processor

[in processor port] The processor to be controlled.

#### **NOTES**

This operation is machine dependent. It may do nothing.

### **CAUTIONS**

The ability to restart an exited processor is machine dependent.

## **RETURN VALUE**

#### KERN\_FAILURE

The operation was not performed. A likely reason is that it is not supported on this processor.

### **RELATED INFORMATION**

Functions: processor\_control, processor\_start, processor\_info, host\_processors.

## processor\_get\_assignment

Function — Get current assignment for a processor

### LIBRARY

#include <mach/mach\_host.h>

### **SYNOPSIS**

kern\_return\_t **processor\_get\_assignment** (mach\_port\_t mach\_port\_t\*

processor; assigned\_set);

### DESCRIPTION

The **processor\_get\_assignment** function returns the name port for the processor set to which a desired processor is currently assigned.

## SECURITY

The requesting task must hold *psv\_get\_processor\_assignment* permission to *processor*.

## PARAMETERS

processor

[in processor port] The processor whose assignment is desired.

#### new\_set

[out processor-set-name port] The name port for the processor set to which *processor* is currently assigned.

## **RETURN VALUE**

#### KERN\_FAILURE

processor is either shut down or off-line.

## **RELATED INFORMATION**

Functions: processor\_assign, processor\_set\_create, processor\_info, task\_assign, thread\_assign.

## processor\_info

Function — Returns information about a processor.

### LIBRARY

#include <mach/mach\_host.h>

## **SYNOPSIS**

kern_return_t processor_info	
(mach_port_t	processor,
int	flavor,
mach_port_t*	host,
processor_info_t	processor_info,
mach_msg_type_number_t*	processor_infoCnt);

## DESCRIPTION

The **processor\_info** function returns selected information for a processor as an array, as specified by *flavor*.

## **SECURITY**

The requesting task must hold psv\_get\_processor\_info permission to processor.

### PARAMETERS

processor

[in processor port] A processor port for which information is desired.

flavor

[in scalar] The type of information requested.

#### PROCESSOR\_BASIC\_INFO

Basic information, slot number, running status, etc. The returned structure is **processor\_basic\_info** of size PROCESSOR\_BASIC\_INFO\_COUNT.

host

[out host-name port] The host on which the processor resides. This is the host name port.

#### processor\_info

[out array of *int*] Information about the processor.

processor\_infoCnt

[pointer to in/out scalar] On input, the maximum size of the info buffer; on output, the returned size of the info structure (in units of sizeof *(int)*).

# **RETURN VALUE**

Only generic errors apply.

# **RELATED INFORMATION**

Functions: processor\_start, processor\_exit, processor\_control, host\_processors.

Data Structures: processor\_basic\_info.

#### processor\_set\_create

Function — Creates a new processor set

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t **processor\_set\_create** (mach\_port\_t mach\_port\_t\* mach\_port\_t\*

host\_name, new\_set, new\_name);

#### DESCRIPTION

The **processor\_set\_create** function creates a new processor set and returns the two ports associated with it. The port returned in *new\_set* is the control port representing the set. It is used to perform operations such as assigning processors, tasks or threads. The port returned in *new\_name* is the name port which identifies the set, and is used to obtain information about the set.

#### **SECURITY**

The requesting task must hold *hsv\_create\_pset* permission to *host\_name*.

## PARAMETERS

#### host\_name

[in host-name port] The name port for the host on which the set is to be created.

#### new\_set

[out processor-set-control port] Control port used for performing operations on the new set.

new\_name

[out processor-set-name port] Name port used to identify the new set and obtain information about it.

## **RETURN VALUE**

Only generic errors apply.

# **RELATED INFORMATION**

Functions: processor\_set\_destroy, processor\_set\_info, processor\_assign, task\_assign, thread\_assign.

# processor\_set\_default

Function — Returns the default processor set

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t **processor\_set\_default** (mach\_port\_t mach\_port\_t\*

host,
default\_set\_name);

#### DESCRIPTION

The **processor\_set\_default** function returns the name port for the default processor set for the specified host. The default processor set is used by all threads, tasks and processors that are not explicitly assigned to other sets.

#### **SECURITY**

The requesting task must hold *psv\_get\_default\_pset\_name* permission to *host*.

# PARAMETERS

host

[in host-name port] The name port for the host for which the default processor set is desired.

#### default\_set\_name

[out processor-set-name port] The returned name port for the default processor set.

#### **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: processor\_set\_info, thread\_assign, task\_assign.

# processor\_set\_destroy

Function — Destroys a processor set

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t **processor\_set\_destroy** (mach\_port\_t

processor\_set);

## DESCRIPTION

The **processor\_set\_destroy** function destroys the specified processor set. Any assigned processors, tasks or threads are re-assigned to the default set. The object port (not the name port) for the processor set is required.

## SECURITY

The requesting task must hold *pssv\_destroy\_pset* permission to *processor\_set*.

#### PARAMETERS

processor\_set

[in processor-set-control port] The control port for the processor set to be destroyed.

#### **RETURN VALUE**

KERN\_FAILURE

An attempt was made to destroy the default processor set.

#### **RELATED INFORMATION**

Functions: processor\_set\_create, processor\_assign, task\_assign, thread\_assign.

# processor\_set\_info

Function — Returns information about a processor set.

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t **processor\_set\_info** (mach\_port\_t processor\_set\_name, int flavor, mach\_port\_t\* host, processor\_set\_info\_t processor\_set\_info, mach\_msg\_type\_number\_t\* infoCnt);

#### DESCRIPTION

The **processor\_set\_info** function returns selected information for a processor set as an array, as specified by *flavor*.

## SECURITY

The requesting task must hold *pssv\_get\_pset\_info* permission to *processor\_set\_name*.

## PARAMETERS

processor\_set\_name

[in processor-set-control port] A processor set control port for which information is desired.

#### flavor

[in scalar] The type of information requested.

#### PROCESSOR\_SET\_BASIC\_INFO

Basic information concerning the processor set. The returned structure is defined by **processor\_set\_basic\_info**, whose size is defined by **PROCESSOR\_SET\_BASIC\_INFO\_COUNT**.

#### PROCESSOR\_SET\_SCHED\_INFO

Scheduling information. The returned structure is defined by **processor\_set\_sched\_info**, whose size is defined by **PROCESSOR\_SET\_SCHED\_INFO\_COUNT**.

#### host

[out host-name port] The name port for the host on which the processor resides.

#### processor\_set\_info

[out array of *int*] Information about the processor set.

#### *infoCnt*

[pointer to in/out scalar] On input, the maximum size of the info buffer; on output, the returned size of the info structure (in units of sizeof *(int)*).

#### **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: processor\_set\_create, processor\_set\_default, processor\_assign, task\_assign, thread\_assign.

Data Structures: processor\_set\_basic\_info, processor\_set\_sched\_info.

## processor\_set\_max\_priority

Function — Sets the maximum scheduling priority for a processor set

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

ssor_set,
priority,
threads);
,

#### DESCRIPTION

The **processor\_set\_max\_priority** function sets the maximum scheduling priority for *processor\_set*. The maximum priority of a processor set is used only when creating new threads. A new thread's maximum priority is set to that of its assigned processor set. When assigned to a processor set, a thread's maximum priority is reduced, if necessary, to that of its new processor set; its current priority is also reduced, as needed. Changing the maximum priority of a processor set does not affect the priority of the currently assigned threads unless *change\_threads* is TRUE. If this priority change violates the maximum priority of some threads, their maximum priorities will be reduced to match.

## SECURITY

The requesting task must hold *pssv\_chg\_pset\_max\_pri* permission to *processor\_set*.

#### PARAMETERS

#### processor\_set

[in processor-set-control port] The control port for the processor set whose maximum scheduling priority is to be set.

#### priority

[in scalar] The new priority for the processor set.

#### change\_threads

[in scalar] True if the maximum priority of existing threads assigned to this processor set should also be changed.

#### **RETURN VALUE**

Only generic errors apply.

# **RELATED INFORMATION**

Functions: thread\_max\_priority, thread\_priority, thread\_assign.

# processor\_set\_policy\_disable

Function — Disables a scheduling policy for a processor set

--

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern_return_t processor_set_policy_disable	
(mach_port_t	processor_set,
int	policy,
boolean_t	change_threads);

-- --

#### DESCRIPTION

The **processor\_set\_policy\_disable** function restricts the set of scheduling policies allowed for *processor\_set*. The set of scheduling policies allowed for a processor set is the set of policies allowed to be set for threads assigned to that processor set. The current set of permitted policies can be obtained from **processor\_set\_info**. Timesharing may not be forbidden for any processor set. This is a compromise to reduce the complexity of the assign operation; any thread whose policy is forbidden by its target processor set has its policy reset to timesharing. Disabling a scheduling policy for a processor set has no effect on threads currently assigned to that processor set unless *change\_threads* is TRUE, in which case their policies will be reset to timesharing.

#### SECURITY

The requesting task must hold *pssv\_invalidate\_scheduling\_policy* permission to *processor\_set*.

#### PARAMETERS

processor\_set

[in processor-set-control port] The control port for the processor set for which a scheduling policy is to be disabled.

policy

[in scalar] Policy to be disabled. The values currently defined are POLICY\_TIMESHARE and POLICY\_FIXEDPRI.

change\_threads

[in scalar] If true, causes the scheduling policy for all threads currently running with *policy* to POLICY\_TIMESHARE.

# **RETURN VALUE**

Only generic errors apply.

# **RELATED INFORMATION**

Functions: processor\_set\_policy\_enable, thread\_policy.

# processor\_set\_policy\_enable

Function — Enables a scheduling policy for a processor set

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t **processor\_set\_policy\_enable** (mach\_port\_t int

processor\_set,
 policy);

#### DESCRIPTION

The **processor\_set\_policy\_enable** function extends the set of scheduling policies allowed for *processor\_set*. The set of scheduling policies allowed for a processor set is the set of policies allowed to be set for threads assigned to that processor set. The current set of permitted policies can be obtained from **processor\_set\_info**.

## SECURITY

The requesting task must hold *pssv\_define\_new\_scheduling\_policy* permission to *processor\_set*.

## PARAMETERS

#### processor\_set

[in processor-set-control port] The control port for the processor set for which a scheduling policy is to be enabled.

policy

[in scalar] Policy to be enabled. The values currently defined are POLICY\_TIMESHARE and POLICY\_FIXEDPRI.

#### **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: processor\_set\_policy\_disable, thread\_policy.

## processor\_set\_tasks

Function — Returns a list of tasks assigned to a processor set

## LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t **processor\_set\_tasks** (mach\_port\_t processor\_set, task\_array\_t\* task\_list, mach\_msg\_type\_number\_t\* task\_count);

# DESCRIPTION

The **processor\_set\_tasks** function returns send rights to the kernel ports for each task currently assigned to *processor\_set*.

# SECURITY

The requesting task must hold *pssv\_observe\_pset\_processes* permission to *processor\_set*.

## PARAMETERS

processor\_set

[in processor-set-control port] A processor set control port for which information is desired.

task\_list

[out pointer to dynamic array of task ports] The returned set of port rights naming the tasks currently assigned to *processor\_set*.

task\_count

[out scalar] The number of tasks returned in *task\_list*.

#### **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: processor\_set\_threads, task\_assign, thread\_assign.

## processor\_set\_threads

Function — Returns a list of threads assigned to a processor set

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t **processor\_set\_threads** (mach\_port\_t thread\_array\_t\* mach\_msg\_type\_number\_t\*

processor\_set, thread\_list, thread\_count);

#### DESCRIPTION

The **processor\_set\_threads** function returns send rights to the kernel ports for each thread currently assigned to *processor\_set*.

#### **SECURITY**

The requesting task must hold *pssv\_observe\_pset\_processes* permission to *processor\_set*.

#### PARAMETERS

processor\_set

[in processor-set-control port] A processor set control port for which information is desired.

#### thread\_list

[out pointer to dynamic array of thread ports] The returned set of ports naming the threads currently assigned to *processor\_set*.

#### thread\_count

[out scalar] The number of threads returned in *thread\_list*.

#### **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: processor\_set\_tasks, task\_assign, thread\_assign.

## processor\_start

Function — Start a processor

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t **processor\_start** (mach\_port\_t

processor);

#### DESCRIPTION

The **processor\_start** function allows privileged software to start a processor in a multi-processor that so allows it. A newly started processor is assigned to the default processor set. The interpretation of this operation is machine dependent.

# SECURITY

The requesting task must hold *psv\_may\_control\_processor* permission to *processor*.

#### PARAMETERS

processor

[in processor port] The processor to be controlled.

## NOTES

This operation is machine dependent. It may do nothing.

#### CAUTIONS

The ability to restart an exited processor is machine dependent.

# **RETURN VALUE**

#### KERN\_FAILURE

The operation was not performed. A likely reason is that it is not supported on this processor.

#### **RELATED INFORMATION**

Functions: processor\_control, processor\_exit, processor\_info, host\_processors.

# task\_assign

Function — Assign a task to a processor set

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t **task\_assign** (mach\_port\_t mach\_port\_t boolean t

task, processor\_set, assign\_threads);

#### DESCRIPTION

The **task\_assign** function assigns *task* to the set *processor\_set*. After the assignment is completed, newly created threads within this task will be assigned to this processor set. Any previous assignment of the task is nullified.

If *assign\_threads* is TRUE, existing threads within the task will also be assigned to the processor set.

# **SECURITY**

The requesting task must hold *tsv\_assign\_task\_to\_pset* permission to *task* and *pssv\_assign\_task* to *processor\_set*.

## PARAMETERS

task

[in task port] The port for the task to be assigned.

#### processor\_set

[in processor-set-control port] The control port for the processor set into which the task is to be assigned.

#### assign\_threads

[in scalar] True if this assignment should apply as well to the threads within the task.

# **RETURN VALUE**

Only generic errors apply.

# **RELATED INFORMATION**

Functions: task\_assign\_default, task\_get\_assignment, processor\_set\_create, processor\_set\_info, processor\_assign, thread\_assign.

## task\_assign\_default

Function — Assign a task to the default processor set

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t **task\_assign\_default** (mach\_port\_t boolean\_t

task, assign\_threads);

#### DESCRIPTION

The **task\_assign\_default** function assigns *task* to the default processor set. After the assignment is completed, newly created threads within this task will be assigned to this processor set. Any previous assignment of the task is nullified.

If *assign\_threads* is TRUE, existing threads within the task will also be assigned to the processor set.

#### SECURITY

The requesting task must hold tsv\_assign\_task\_to\_pset permission to task and pssv\_assign\_task permission to the default processor set.

### PARAMETERS

task

[in task port] The port for the task to be assigned.

assign\_threads

[in scalar] True if this assignment should apply as well to the threads within the task.

#### **NOTES**

This variant of **task\_assign** exists because the control port for the default processor set is privileged, and therefore not available to most tasks.

## **RETURN VALUE**

Only generic errors apply.

# **RELATED INFORMATION**

Functions: task\_assign, task\_get\_assignment, processor\_set\_create, processor\_set\_info, thread\_assign, processor\_assign.

## task\_get\_assignment

Function — Returns the processor set to which a task is assigned

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t **task\_get\_assignment** (mach\_port\_t mach\_port\_t\*

task,
processor\_set);

#### DESCRIPTION

The **task\_get\_assignment** function returns the name port to the processor set to which *task* is currently assigned. This port can only be used to obtain information about the processor set.

#### **SECURITY**

The requesting task must hold *tsv\_get\_task\_assignmnet* permission to *task*.

# PARAMETERS

task

[in task port] The port for the task whose assignment is desired.

processor\_set

[out processor-set-name port] The name port for the processor set into which the task is assigned.

#### **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: task\_assign, task\_assign\_default, processor\_set\_create, processor\_set\_info, thread\_assign, processor\_assign.

# task\_priority

Function — Sets the scheduling priority for a task

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t **task\_priority** (mach\_port\_t int

boolean\_t

task, priority, change\_threads);

# DESCRIPTION

The **task\_priority** function sets the scheduling priority for *task*. The priority of a task is used only when creating new threads. A new thread's priority is set to that of the enclosing task's priority. Changing the priority of a task does not affect the priority of the enclosed threads unless *change\_threads* is TRUE. If this priority change violates the maximum priority of some threads, as many threads as possible will be changed and an error code will be returned.

## SECURITY

The requesting task must hold *tsv\_chg\_task\_priority* permission to *task*.

# PARAMETERS

task

[in task port] The task whose scheduling priority is to be set.

priority

[in scalar] The new priority for the task.

change\_threads

[in scalar] True if priority of existing threads within the task should also be changed.

## **RETURN VALUE**

#### KERN\_FAILURE

*change\_threads* was TRUE and the attempt to change the priority of some existing thread within the task failed because the new priority would violate that thread's maximum priority.

## **RELATED INFORMATION**

Functions:thread\_max\_priority,processor\_set\_max\_priority.

thread\_priority,

# thread\_assign

Function — Assign a thread to a processor set

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t thread\_assign

(mach\_port\_t mach\_port\_t *thread, processor\_set*);

## DESCRIPTION

The **thread\_assign** function assigns *thread* to the set *processor\_set*. After the assignment is completed, the thread executes only on processors that are assigned to that processor set. Any previous assignment of the thread is nullified.

# SECURITY

The requesting task must hold *thsv\_assign\_thread\_to\_pset* permission to *thread* and *pssv\_assign\_thread* to *processor\_set*.

## PARAMETERS

thread

[in thread port] The thread to be assigned.

#### processor\_set

[in processor-set-control port] The control port for the processor set into which the thread is to be assigned.

#### **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions:thread\_assign\_default,thread\_get\_assignment,processor\_set\_create, processor\_set\_info, task\_assign, processor\_assign.

# thread\_assign\_default

Function — Assign a thread to the default processor set

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t **thread\_assign\_default** (mach\_port\_t

thread);

# DESCRIPTION

The **thread\_assign\_default** function assigns *thread* to the default processor set. After the assignment is completed, the thread executes only on processors that are assigned to that processor set. Any previous assignment of the thread is nullified.

## SECURITY

The requesting task must hold *thsv\_assign\_thread\_to\_pset* permission to *thread* and *pssv\_assign\_thread* permission to the default processor set.

## PARAMETERS

thread

[in thread port] The thread to be assigned.

#### **NOTES**

This variant of **thread\_assign** exists because the control port for the default processor set is privileged, and therefore not available to most tasks.

#### **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: thread\_assign, thread\_get\_assignment, processor\_set\_create, processor\_set\_info, task\_assign, processor\_assign.

# thread\_get\_assignment

Function — Returns the processor set to which a thread is assigned

## LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t **thread\_get\_assignment** (mach\_port\_t *thread,* mach\_port\_t\* *processor\_set*);

## DESCRIPTION

The **thread\_get\_assignment** function returns the name port to the processor set to which *thread* is currently assigned. This port can only be used to obtain information about the processor set.

# SECURITY

The requesting task must hold *thsv\_get\_thread\_assignment* permission to *thread*.

## PARAMETERS

thread

[in thread port] The thread whose assignment is desired.

#### processor\_set

[out processor-set-name port] The name port for the processor set into which the thread is assigned.

#### **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: thread\_assign, thread\_assign\_default, processor\_set\_create, processor\_set\_info, task\_assign, processor\_assign.

# thread\_max\_priority

Function — Sets the maximum scheduling priority for a thread

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern_return_t <b>thread_max_priority</b>	
(mach_port_t	
mach_port_t	
int	

thread, processor\_set, priority);

#### DESCRIPTION

The **thread\_max\_priority** function sets the maximum scheduling priority for *thread*.

Threads have three priorities associated with them by the system:

- A priority value which can be set by the thread to any value up to a maximum priority. Newly created threads obtain their priority from their task.
- A maximum priority value which can be raised only via privileged operation so that users may not unfairly compete with other users in their processor set. Newly created threads obtain their maximum priority from that of their assigned processor set.
- A scheduled priority value which is used to make scheduling decisions for the thread. This value is determined on the basis of the user priority value by the scheduling policy (for timesharing, this means adding an increment derived from CPU usage).

This function changes the maximum priority for the thread. Because this function requires the presentation of the corresponding processor set control port, this call can reset the maximum priority to any legal value.

#### SECURITY

The requesting task must hold *thsv\_set\_max\_thread\_priority* permission to *thread*.

#### PARAMETERS

#### thread

[in thread port] The thread whose maximum scheduling priority is to be set.

processor\_set

[in processor-set-control port] The control port for the processor set to which the thread is currently assigned.

priority

[in scalar] The new maximum priority for the thread.

#### **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions:thread\_priority,processor\_set\_max\_priority.

thread\_policy,

task\_priority,

# thread\_policy

Function — Sets the scheduling policy to apply to a thread

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern_return_t thread_policy	
(mach_port_t	
int	
int	

#### DESCRIPTION

The thread\_policy function sets the scheduling policy to be applied to thread.

thread, policy, data);

#### **SECURITY**

The requesting task must hold *thsv\_set\_thread\_policy* permission to *thread*.

#### PARAMETERS

thread

[in thread port] The thread scheduling policy is to be set.

policy

[in scalar] Policy to be set. The values currently defined are POLICY\_TIMESHARE and POLICY\_FIXEDPRI.

#### data

[in scalar] Policy specific data. Currently, this value is used only for POLICY\_FIXEDPRI, in which case it is the quantum to be used (in milliseconds); to be meaningful, this value must be a multiple of the basic system quantum (which can be obtained from **host\_info**).

## **RETURN VALUE**

#### KERN\_FAILURE

The processor set to which *thread* is currently assigned does not permit *policy*.

#### **RELATED INFORMATION**

Functions: processor\_set\_policy\_enable, processor\_set\_policy\_disable.

# thread\_priority

Function — Sets the scheduling priority for a thread

#### LIBRARY

#include <mach/mach\_host.h>

#### **SYNOPSIS**

kern\_return\_t thread\_priority

(mach_port_t	thread,
int	priority,
boolean_t	set_max);

# DESCRIPTION

The thread\_priority function sets the scheduling priority for thread.

#### SECURITY

The requesting task must hold *thsv\_set\_thread\_priority* to *thread*. If *set\_max* is true, the requesting task must also hold *thsv\_set\_max\_thread\_priority* to *thread*.

#### PARAMETERS

thread

[in thread port] The thread whose scheduling priority is to be set.

#### priority

[in scalar] The new priority for the thread.

#### set\_max

[in scalar] True if the thread's maximum priority should also be set.

# NOTES

Threads have three priorities associated with them by the system:

- A priority value which can be set by the thread to any value up to a maximum priority. Newly created threads obtain their priority from their task.
- A maximum priority value which can be raised only via privileged operation so that users may not unfairly compete with other users in their processor set. Newly created threads obtain their maximum priority from that of their assigned processor set.
- A scheduled priority value which is used to make scheduling decisions for the thread. This value is determined on the basis of the user priority value by

the scheduling policy (for timesharing, this means adding an increment derived from CPU usage).

This function changes the priority and optionally the maximum priority (if *set\_max* is TRUE) for *thread*. Priorities range from 0 to 31, where lower numbers denote higher priorities. If the new priority is higher than the priority of the current thread, preemption may occur as a result of this call. This call will fail if *priority* is greater than the current maximum priority of the thread. As a result, this call can only lower the value of a thread's maximum priority.

#### **RETURN VALUE**

KERN\_FAILURE

The requested operation would violate the thread's maximum priority.

#### **RELATED INFORMATION**

Functions:	thread_max_priority,	thread_policy,	task_priority,
processor_se	t_max_priority.		

# CHAPTER 10 Kernel Device Interface

This chapter discusses the specifics of the device interfaces to in-kernel device drivers. These interfaces provide read, write and status interfaces to devices.

# device\_close

Function — De-establish a connection to a device.

#### LIBRARY

#include <device/device.h>

#### **SYNOPSIS**

kern\_return\_t **device\_close** (mach\_port\_t

device);

# DESCRIPTION

The **device\_close** function decrements the open count for the named device. If this count reaches zero, the close operation of the device driver is invoked, closing the device.

## SECURITY

The requesting task must hold *dsv\_close\_device* permission to *device*.

#### PARAMETERS

device

[in device port] A device port to the device to be closed.

## **RETURN VALUE**

D\_NO\_SUCH\_DEVICE

No device with that name, or the device is not operational.

#### **RELATED INFORMATION**

Functions: device\_open.

# device\_get\_status

Function — Return the current device status

#### LIBRARY

#include <device/device.h>

#### **SYNOPSIS**

kern\_return\_t device\_get\_status

(mach_port_t	device,
int	flavor,
dev_status_t	status,
mach_msg_type_number_t*	status_count);

#### DESCRIPTION

The **device\_get\_status** function returns status information pertaining to an open device. The possible values for *flavor* as well as the meaning of the returned status information is device dependent.

## SECURITY

The requesting task must hold *dsv\_get\_device\_status* permission to *device*.

## PARAMETERS

device

[in device port] A device port to the device to be interrogated.

flavor

[in scalar] The type of status information requested.

status

[out array of *int*] The returned device status.

status\_count

[pointer to in/out scalar] On input, the reserved size of *status*; on output, the size of the returned device status.

## **RETURN VALUE**

D\_DEVICE\_DOWN Device has been shut down

#### D\_NO\_SUCH\_DEVICE

No device with that name, or the device is not operational.

D\_OUT\_OF\_BAND

Out-of-band condition occurred on device (such as typing control-C)

# **RELATED INFORMATION**

Functions: device\_set\_status.

# device\_map

Function — Establish a memory manager representing a device

# LIBRARY

#include <device/device.h>

## **SYNOPSIS**

kern\_return\_t device\_map

(mach_port_t	device,
vm_prot_t	prot,
vm_offset_t	offset,
vm_size_t	size,
mach_port_t*	pager,
int	unmap);

## DESCRIPTION

The **device\_map** function establishes a memory manager that presents a memory object representing a device. The resulting port is suitable for use as the memory manager port in a **vm\_map** call. This call is device dependent.

# SECURITY

The requesting task must hold *dsv\_map\_device* permission to *device*.

# PARAMETERS

device	[in device port] A device port to the device to be mapped.
prot	[in scalar] Protection for the device memory.
offset	[in scalar] An offset within the device memory object, in bytes.
size	[in scalar] The size of the device memory object.
pager	[out abstract-memory-object port] The returned abstract memory object port to a memory manager that represents the device.
иптар	[in scalar] Unused.

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## NOTES

Port rights are maintained as follows:

Abstract memory object port: The device pager has all rights.

Memory cache control port: The device pager has only send rights.

Memory cache name port:

The device pager has only send rights. The name port is not even recorded.

Regardless of how the object is created, the control and name ports are created by the kernel and passed through the memory management interface.

## CAUTIONS

The device memory manager assumes that access to its memory objects will not be propagated to more that one host, and therefore provides no consistency guarantees beyond those made by the kernel.

In the event that more than one host attempts to use a device memory object, the device pager will only record the last set of port names. [This can happen with only one host if a new mapping is being established while termination of all previous mappings is taking place.] Currently, the device pager assumes that its clients adhere to the initialization and termination protocols in the memory management interface; otherwise, port rights or out-of-line memory from erroneous messages may be allowed to accumulate.

## **RETURN VALUE**

D\_DEVICE\_DOWN

Device has been shut down

D\_NO\_SUCH\_DEVICE

No device with that name, or the device is not operational.

#### D\_READ\_ONLY

Data cannot be written to this device.

## **RELATED INFORMATION**

Functions: vm\_map, evc\_wait.

# device\_open

**Function** — Establish a connection to a device.

# LIBRARY

#include <device/device.h> (device\_open)

#include <device\_request.h> (device\_open\_request)

#include <device\_reply.h> (ds\_device\_open\_reply)

# **SYNOPSIS**

kern_return_t device_open	
(mach_port_t	master_port,
dev_mode_t	mode,
dev_name_t	name,
mach_port_t*	device);

# device\_open\_request

Asynchronous Function form — Asynchronously request a connection to a device

kern\_return\_t device\_open\_request

(mach_port_t	master_port,
mach_port_t	reply_port,
dev_mode_t	mode,
dev_name_t	name);

# ds\_device\_open\_reply

Asynchronous Server Interface form — Receive the reply from an asynchronous open

kern\_return\_t ds\_device\_open\_reply

(mach_port_t	reply_port,
kern_return_t	return_code,
mach_port_t	device);

# DESCRIPTION

The **device\_open** function opens a device object. The open operation of the device is invoked, if the device is not already open. The open count for the device is incremented.

# SECURITY

The requesting task must hold *dsv\_open\_device* permission to *master\_port*.

# PARAMETERS

#### master\_port

[in device-master port] The master device port. This port is provided to the bootstrap task.

#### reply\_port

[in reply port] The port to which a reply is to be sent when the device is open.

#### mode

[in scalar] Opening mode. This is the bit-wise OR of the following values:

### D\_READ

Read access

### D\_WRITE

Write access

# D\_NODELAY

Do not delay on open

#### пате

[pointer to in array of *char*] Name of the device to open.

### return\_code

[in scalar] Status of the open.

### device

[out device port] The returned device port.

# **RETURN VALUE**

**device\_open\_request** returns only message transmission errors. The return value supplied to **ds\_device\_open\_reply** is irrelevant. The *return\_code* returned by **ds\_device\_open\_reply** or the error return from **device\_open** is one of the following:

D\_WOULD\_BLOCK

The device is busy, but D\_NOWAIT was specified in mode.

## D\_ALREADY\_OPEN

The device is already open in a mode incompatible with mode.

### D\_NO\_SUCH\_DEVICE

No device with that name, or the device is not operational.

D\_DEVICE\_DOWN The device has been shut down.

D\_READ\_ONLY Data cannot be written to this device.

# **RELATED INFORMATION**

Functions: device\_close, device\_reply\_server.

# device\_read

Function — Read a sequence of bytes from a device object.

### LIBRARY

#include <device/device.h> (device\_read)

#include <device\_request.h> (device\_read\_request)

#include <device\_reply.h> (ds\_device\_read\_reply)

# **SYNOPSIS**

kern\_return\_t **device\_read**(mach\_port\_t device,
dev\_mode\_t mode,
recnum\_t recnum,
int bytes\_wanted,
io\_buf\_ptr\_t\* data,
mach\_msg\_type\_number\_t\* data\_count);

# device\_read\_request

kern\_return

Asynchronous Function form — Asynchronously read data

_t device_read_request	
(mach_port_t	device,
mach_port_t	reply_port,
dev_mode_t	mode,
recnum_t	recnum,
int	<i>bytes_wanted</i> );

# ds\_device\_read\_reply

Asynchronous Server Interface form — Receive the reply from an asynchronous read

ke	rn_	_return_	_t ds	_d	evice_	_read	_rep	ly
----	-----	----------	-------	----	--------	-------	------	----

(mach_port_t	reply_port,
kern_return_t	return_code,
io_buf_ptr_t	data,
mach_msg_type_number_t	data_count);

# DESCRIPTION

The **device\_read** function reads a sequence of bytes from a device object. The meaning of *recnum* as well as the specific operation performed is device dependent.

# SECURITY

The requesting task must hold dsv\_read\_device permission to device.

# PARAMETERS

### device

[in device port] A device port to the device to be read.

### reply\_port

[in reply port] The port to which the reply message is to be sent.

#### mode

[in scalar] I/O mode value. Meaningful options are:

### D\_NOWAIT

Do not wait if data is unavailable.

#### recnum

[in scalar] Record number to be read.

### bytes\_wanted

[in scalar] Size of data transfer.

### return\_code

[in scalar] The return status code from the read.

### data

[out pointer to dynamic array of bytes] Returned data bytes.

#### data\_count

[out scalar] Number of returned data bytes.

# **RETURN VALUE**

**device\_read\_request** returns only message transmission errors. A return value supplied to **ds\_device\_read\_reply** other than KERN\_SUCCESS or MIG\_NO\_REPLY will cause **mach\_msg\_server** to de-allocate the returned data. The *return\_code* returned by **ds\_device\_read\_reply** or the error return from **device\_read** is one of the following:

D\_DEVICE\_DOWN Device has been shut down.

D\_INVALID\_RECNUM Invalid record (block) number.

D\_INVALID\_SIZE Invalid IO size.

# D\_IO\_ERROR

Hardware IO error.

D\_NO\_SUCH\_DEVICE No device with that name, or the device is not operational.

D\_OUT\_OF\_BAND Out-of-band condition occurred on device (such as typing control-C).

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D\_WOULD\_BLOCK Operation would block, but D\_NOWAIT set.

# **RELATED INFORMATION**

Functions: device\_read\_inband, device\_reply\_server.

# device\_read\_inband

Function — Read a sequence of bytes "inband" from a device object.

# **LIBRARY**

#include <device.h> (device\_read\_inband)

#include <device request.h> (device read request inband)

#include <device\_reply.h> (ds\_device\_read\_reply\_inband)

# **SYNOPSIS**

kern_return_t device_read_inband	
(mach_port_t	device,
dev_mode_t	mode,
recnum_t	recnum,
int	bytes_wanted,
io_buf_ptr_inband_t*	data,
mach_msg_type_number_t*	data_count);

# device\_read\_request\_inband

Asynchronous Function form — Asynchronously read data

# kern\_return\_t device\_read\_request\_inband

(mach_port_t	device,
mach_port_t	reply_port,
dev_mode_t	mode,
recnum_t	recnum,
int	<i>bytes_wanted</i> );

# ds\_device\_read\_reply\_inband

Asynchronous Server Interface form — Receive the reply from an asynchronous read

kern\_return\_t ds\_device\_read\_reply\_inband

(mach_port_t	reply_port,
kern_return_t	return_code,
io_buf_ptr_inband_t	data,
mach_msg_type_number_t	data_count);

# DESCRIPTION

The device\_read function reads a sequence of bytes from a device object. The meaning of recnum as well as the specific operation performed is device dependent. This call differs from device\_read in that the returned bytes are returned "inband" in the reply IPC message.

# **SECURITY**

The requesting task must hold dsv\_read\_device permission to device.

# PARAMETERS

device

[in device port] A device port to the device to be read.

### reply\_port

[in reply port] The port to which the reply message is to be sent.

mode

[in scalar] I/O mode value. Meaningful options are:

### D\_NOWAIT

Do not wait if data is unavailable.

recnum

[in scalar] Record number to be read.

bytes\_wanted

[in scalar] Size of data transfer.

### return\_code

[in scalar] The return status code from the read.

### data

[out array of bytes] Returned data bytes.

### data\_count

[out scalar] Number of returned data bytes.

# **RETURN VALUE**

**device\_read\_request\_inband** returns only message transmission errors. The return value supplied to **ds\_device\_read\_reply\_inband** is irrelevant. The *return\_code* returned by **ds\_device\_read\_reply\_inband** or the error return from **device\_read\_inband** is one of the following:

### D\_DEVICE\_DOWN

Device has been shut down

### D\_INVALID\_RECNUM Invalid record (block) number

D\_INVALID\_SIZE

Invalid IO size

D\_IO\_ERROR Hardware IO error

D\_NO\_SUCH\_DEVICE No device with that name, or the device is not operational.

D\_OUT\_OF\_BAND Out-of-band condition occurred on device (such as typing control-C)

D\_WOULD\_BLOCK Operation would block, but D\_NOWAIT set

# **RELATED INFORMATION**

Functions: device\_read, device\_reply\_server.

# device\_set\_filter

Function — Names an input filter for a device

### LIBRARY

#include <device/device.h>

#include <device/net\_status.h>

# **SYNOPSIS**

kern\_return\_t **device\_set\_filter**(mach\_port\_t device,
mach\_port\_t receive\_port,
mach\_msg\_type\_name\_t receive\_port\_type,
int priority,
filter\_array\_t filter,
mach\_msg\_type\_number\_t filter\_count);

# DESCRIPTION

The **device\_set\_filter** function provides a means by which selected data appearing at a device interface can be selected and routed to a port.

The filter command list consists of an array of up to NET\_MAX\_FILTER (16bit) values to be applied to incoming messages to determine if those messages should be given to a particular input filter.

Each filter command list specifies a sequences of actions which leave a boolean value on the top of an internal stack. Each 16-bit value of the command list specifies a data (push) operation (high order NETF\_NBPO bits) as well as a binary operator (low order NETF\_NBPA bits).

The value to be pushed onto the stack is chosen as follows.

#### NETF\_PUSHLIT

Use the next 16-bit value of the filter as the value.

### NETF\_PUSHZERO

Use 0 as the value.

### NETF\_PUSHWORD+N

Use 16-bit value *N* of the "data" portion of the message as the value.

# NETF\_PUSHHDR+N

Use 16-bit value *N* of the "header" portion of the message as the value.

### NETF\_PUSHIND

Pops the top 32-bit value from the stack and then uses it as an index to the 16-bit value of the "data" portion of the message to be used as the value.

### NETF\_PUSHHDRIND

Pops the top 32-bit value from the stack and then uses it as an index to the 16-bit value of the "header" portion of the message to be used as the value.

### NETF\_PUSHSTK+N

Use 32-bit value N of the stack (where the top of stack is value 0) as the value.

### NETF\_NOPUSH

Don't push a value.

The unsigned value so chosen is promoted to a 32-bit value before being pushed.

Once a value is pushed (except for the case of NETF\_NOPUSH), the top two 32-bit values of the stack are popped and a binary operator applied to them (with the old top of stack as the second operand). The result of the operator is pushed on the stack. These operators are:

### NETF\_NOP

Don't pop off any values and do no operation.

### NETF\_EQ

Perform an equal comparison.

### NETF\_LT

Perform a less than comparison.

### NETF\_LE

Perform a less than or equal comparison.

### NETF\_GT

Perform a greater than comparison.

### NETF\_GE

Perform a greater than or equal comparison.

### NETF\_AND

Perform a bit-wise boolean AND operation.

### NETF\_OR

Perform a bit-wise boolean inclusive OR operation.

### NETF\_XOR

Perform a bit-wise boolean exclusive OR operation.

#### NETF\_NEQ

Perform a not equal comparison.

#### NETF\_LSH

Perform a left shift operation.

### NETF\_RSH

Perform a right shift operation.

### NETF\_ADD

Perform an addition.

#### NETF\_SUB

Perform a subtraction.

### NETF\_COR

Perform an equal comparison. If the comparison is TRUE, terminate the filter list. Otherwise, pop the result of the comparison off the stack.

### NETF\_CAND

Perform an equal comparison. If the comparison is FALSE, terminate the filter list. Otherwise, pop the result of the comparison off the stack.

### NETF\_CNOR

Perform a not equal comparison. If the comparison is FALSE, terminate the filter list. Otherwise, pop the result of the comparison off the stack.

### NETF\_CNAND

Perform a not equal comparison. If the comparison is TRUE, terminate the filter list. Otherwise, pop the result of the comparison off the stack.

The scan of the filter list terminates when the filter list is emptied, or a NETF\_C... operation terminates the list. At this time, if the final value of the top of the stack is TRUE, then the message is accepted for the filter.

# SECURITY

The requesting task must hold *dsv\_set\_device\_filter* permission to *device*.

# PARAMETERS

device

[in device port] A device port

receive\_port

[in filter port] The port to receive the input data that is selected by the filter.

receive\_port\_type

[in scalar] IPC type of the send right provided to the device; either MACH\_MSG\_TYPE\_MAKE\_SEND, MACH\_MSG\_TYPE\_MOVE\_SEND or MACH\_MSG\_TYPE\_COPY\_SEND.

### priority

[in scalar] Used to order multiple receivers.

# filter

[pointer to in array of *filter\_t*] The address of an array of filter values.

### filter\_count

[in scalar] The size of the *filter* array (in 16-bit values).

# **RETURN VALUE**

- D\_DEVICE\_DOWN Device has been shut down
- D\_INVALID\_OPERATION No filter port was supplied.

### D\_NO\_SUCH\_DEVICE

No device with that name, or the device is not operational.

# device\_set\_status

Function — Sets device status.

# LIBRARY

#include <device/device.h>

# **SYNOPSIS**

kern_return_t device_set_status	
(mach_port_t	device,
int	flavor,
dev_status_t	status,
mach_msg_type_number_t	<pre>status_count);</pre>

# DESCRIPTION

The **device\_set\_status** function sets device status. The possible values of *flavor* as well as the corresponding meanings are device dependent.

### SECURITY

The requesting task must hold *dsv\_set\_device\_status* permission to *device*.

# PARAMETERS

device

[in device port] A device port to the device to be manipulated.

#### flavor

[in scalar] The type of status information to set.

#### status

[pointer to in array of *int*] The status information to set.

status\_count

[in scalar] The size of the status information.

# **RETURN VALUE**

D\_DEVICE\_DOWN Device has been shut down

### D\_IO\_ERROR

Hardware IO error

D\_NO\_SUCH\_DEVICE No device with that name, or the device is not operational.

D\_OUT\_OF\_BAND Out-of-band condition occurred on device (such as typing control-C)

D\_READ\_ONLY Data cannot be written to this device.

# **RELATED INFORMATION**

Functions: device\_get\_status.

# device\_write

Function — Write a sequence of bytes to a device object.

### LIBRARY

#include <device/device.h> (device\_write)

#include <device\_request.h> (device\_write\_request)

#include <device\_reply.h> (ds\_device\_write\_reply)

## **SYNOPSIS**

kern\_return\_tdevice\_write(mach\_port\_tdevice,dev\_mode\_tmode,recnum\_trecnum,io\_buf\_ptr\_tdata,mach\_msg\_type\_number\_tdata\_count,int\*bytes\_written);

# device\_write\_request

Asynchronous Function form — Asynchronously write data

kern_return_t device_write_request	
(mach_port_t	device,
mach_port_t	reply_port,
dev_mode_t	mode,
recnum_t	<i>recnum</i> ,
io_buf_ptr_t	data,
mach_msg_type_number_t	data_count);

# ds\_device\_write\_reply

Asynchronous Server Interface form — Receive the reply from an asynchronous write

kern_	_return_	_t <b>ds</b> _	_devi	ce_	write	_repl	y
		(ma	ch_p	ort_	_t		

kern return t

int

reply\_port, return\_code, bytes\_written);

# DESCRIPTION

The **device\_write** function writes a sequence of bytes to a device object. The meaning of *recnum* as well as the specific operation performed is device dependent.

# SECURITY

The requesting task must hold *dsv\_write\_device* permission to *device*.

# PARAMETERS

### device

[in device port] A device port to the device to be written.

### reply\_port

[in reply port] The port to which the reply message is to be sent.

#### mode

[in scalar] I/O mode value. Meaningful options are:

D\_NOWAIT

Do not wait for I/O completion.

#### recnum

[in scalar] Record number to be written.

### data

[pointer to in array of bytes] Data bytes to be written.

#### data\_count

[in scalar] Number of data bytes to be written.

#### return\_code

[in scalar] The return status code from the write.

### bytes\_written

[out scalar] Size of data transfer.

# **RETURN VALUE**

**device\_write\_request** returns only message transmission errors. The return value supplied to **ds\_device\_write\_reply** is irrelevant. The *return\_code* returned by **ds\_device\_write\_reply** or the error return from **device\_write** is one of the following:

D\_DEVICE\_DOWN Device has been shut down

D\_INVALID\_RECNUM Invalid record (block) number

D\_INVALID\_SIZE Invalid IO size

D\_IO\_ERROR Hardware IO error

### D\_NO\_SUCH\_DEVICE No device with that name, or the device is not operational.

# D\_OUT\_OF\_BAND

Out-of-band condition occurred on device (such as typing control-C)

# D\_READ\_ONLY

Data cannot be written to this device.

# D\_WOULD\_BLOCK

Operation would block, but D\_NOWAIT set

# **RELATED INFORMATION**

Functions: device\_write\_inband, device\_reply\_server.

# device\_write\_inband

Function — Write a sequence of bytes "inband" to a device object.

# **LIBRARY**

#include <device/device.h> (device\_write\_inband)

#include <device/device request.h> (device write request inband)

#include <device\_reply.h> (ds\_device\_write\_reply\_inband)

# **SYNOPSIS**

kern_return_t device_write_inband	
(mach_port_t	device,
dev_mode_t	mode,
recnum_t	recnum,
io_buf_ptr_inband_t	data,
mach_msg_type_number_t	data_count,
int*	bytes_written);

# device\_write\_request\_inband

Asynchronous Function form — Asynchronously write data

kern\_return\_t device\_write\_request\_inband

(mach_port_t	device,
mach_port_t	reply_port,
dev_mode_t	mode,
recnum_t	recnum,
io_buf_ptr_inband_t	data,
mach_msg_type_number_t	data_count);

# ds\_device\_write\_reply\_inband

Asynchronous Server Interface form — Receive the reply from an asynchronous write

kern\_return\_t ds\_device\_write\_reply\_inband

(mach_port_t	reply_port,
kern_return_t	return_code,
int	bytes_written);

# DESCRIPTION

The device\_write\_inband function writes a sequence of bytes to a device object. The meaning of recnum as well as the specific operation performed is device dependent. This call differs from device\_write in that the bytes to be written are sent "inband" in the request IPC message.

# SECURITY

The requesting task must hold *dsv\_write\_device* permission to *device*.

## PARAMETERS

device

[in device port] A device port to the device to be written.

### reply\_port

[in reply port] The port to which the reply message is to be sent.

mode

[in scalar] I/O mode value. Meaningful options are:

### D\_NOWAIT

Do not wait for I/O completion.

recnum

[in scalar] Record number to be written.

data

[pointer to in array of bytes] Data bytes to be written.

#### data\_count

[in scalar] Number of data bytes to be written.

### return\_code

[in scalar] The return status code from the write.

### bytes\_written

[out scalar] Size of data transfer.

# **RETURN VALUE**

**device\_write\_request\_inband** returns only message transmission errors. The return value supplied to **ds\_device\_write\_reply\_inband** is irrelevant. The *return\_code* returned by **ds\_device\_write\_reply\_inband** or the error return from **device\_write\_inband** is one of the following:

### D\_DEVICE\_DOWN

Device has been shut down

### D\_INVALID\_RECNUM Invalid record (block) number

### D\_INVALID\_SIZE Invalid IO size

D\_IO\_ERROR Hardware IO error

D\_NO\_SUCH\_DEVICE No device with that name, or the device is not operational.

D\_OUT\_OF\_BAND Out-of-band condition occurred on device (such as typing control-C)

D\_READ\_ONLY Data cannot be written to this device.

D\_WOULD\_BLOCK Operation would block, but D\_NOWAIT set

# **RELATED INFORMATION**

Functions: device\_write, device\_reply\_server.

# evc\_wait

System Trap — Wait for a kernel (device) signalled event

# LIBRARY

Not declared anywhere.

# **SYNOPSIS**

kern\_return\_t evc\_wait (unsigned int

event);

# DESCRIPTION

The **evc\_wait** function causes the invoking thread to wait until the specified kernel (device) generated event occurs. Device drivers (typically mapped devices intended to be supported by user space drivers) may supply an event service.

The event service defines one or more event objects, named by task local event IDs. Each of these event objects has an associated event count, initially zero. Whenever the associated event occurs (typically a device interrupt), the event count is incremented. If this count is zero when **evc\_wait** is called, the calling thread waits for the next event to occur. Only one thread may be waiting for the event to occur. If the count is non-zero when **evc\_wait** is called, the count is simply decremented without causing the thread to wait. The event count guarantees that no events are lost.

# SECURITY

No restrictions defined.

# PARAMETERS

event

[in scalar] The task local event ID of the kernel event object.

# NOTES

The typical use of this service is within user space device drivers. When a device interrupt occurs, the (in this case, simple) kernel device driver would place device status in a shared (with the user device driver) memory window (established by **device\_map**) and signal the associated event. The user space device driver would normally be waiting with **evc\_wait**. The user thread then wakes, processes the device status, typically interacting with the device via its shared memory window, then waits for the next interrupt.

# **RETURN VALUE**

KERN\_NO\_SPACE There is already a thread waiting for this event.

# **RELATED INFORMATION**

Functions: device\_map.

**Kernel Device Interface** 

# CHAPTER 11 Security Server Interface

This chapter discusses the specifics of the interface between the DTOS kernel and the Security Server. Interfaces labeled as **Function** are kernel interfaces, where interfaces labeled as **Server Interface** are interfaces to the security server.

# avc\_cache\_control, avc\_cache\_control\_trap

**Function** — provides interface to the kernel access vector cache for flushing and preloading the cache.

# LIBRARY

#include <mach/mach\_interface.h>
#include <sys/security.h>

# **SYNOPSIS**

kern_return_t avc_cache_control	
(mach_port_t	HostName,
int	ControlWord,
int	PolicyID,
vector_table_t	VectorTable,
int	VectorTableSize,
aid_relevance_table_t	AidvTable,
int	AidvTableSize);
kern_return_t avc_cache_control_trap	
(int	ControlWord,
int	PolicyID,
vector_table_t	VectorTable,
int	VectorTableSize,
aid_relevance_table_t	AidvTable,
int	AidvTableSize);

# DESCRIPTION

The **avc\_cache\_control** function is called by the Security Server whenever it needs to flush the access vector cache or to load required permissions into the access vector cache. One example is when the Security Server switches policies. The **avc\_cache\_control\_trap** function is a system call version of the **avc\_cache\_control** function. It is used to circumvent some limitations in the MIG messaging scheme with regards to in-line data of greater than 1 (4k) page in length.

# **SECURITY**

The client must hold *flush\_permission* permission to the *HostName* port.

# PARAMETERS

HostName

[in mach\_port\_t] The host name port.

### ControlWord

[in int] The control word that describes the operations to be performed by this invocation of **avc\_cache\_control**. The control word format is defined in sys/security.h It is a bit mask with the following functions:

AVC\_FLUSH\_CACHE: to flush the avc vector cache.

AVC\_CLEAR\_CACHE: to remove all cache entries (including wired)

AVC\_RELOAD\_INITIAL\_STATE: reinitialize cache to initial state values.

AVC\_VECTOR\_TABLE: the vector table is present.

AVC\_AIDV\_TABLE: the aid relevance table is present.

#### PolicyId

[in int ] The new policy ID. The *PolicyId* will be incremented with each flush or clear of the avc cache. It may be used to verify that security computations apply to the current policy.

### **VectorTable**

[in vector\_table\_t] The table that contains an array of pairs with associated access vectors to load into, or flush from, the cache.

#### *VectorTableSize*

[in int] The size (in int's) of the VectorTable.

### AidvTable

[in aid\_relevance\_table\_t] The aid relevance table that the kernel will use.

### AidvTableSize

[in int] The size of the aid relevance table specified by the *AidvTable* parameter.

# **RETURN VALUE**

- 0 The operation was successful.
- 1 The operation was not successful.

# **RELATED INFORMATION**

none

# extract\_aid

Macro-Returns the authentication identifier field of the security identifier.

# LIBRARY

#include <sys/security.h>

# **SYNOPSIS**

authentication\_id\_t **extract\_aid** (security\_id\_t

sid);

# DESCRIPTION

The **extract\_aid** macro returns the authentication identifier field of the security identifier *sid*.

# SECURITY

None.

# PARAMETERS

sid

[in security\_id] The input security identifier.

# **RETURN VALUE**

Authentication identifier.

# **RELATED INFORMATION**

Functions: extract\_mid, make\_sid.

# extract\_mid

Macro-Returns the mandatory identifier field of the security identifier.

# LIBRARY

#include <sys/security.h>

# **SYNOPSIS**

authentication\_id\_t **extract\_mid** (security\_id\_t

sid);

# DESCRIPTION

The **extract\_mid** macro returns the mandatory identifier field of the security identifier *sid*.

# SECURITY

None.

# PARAMETERS

sid

[in security\_id] The input security identifier.

# **RETURN VALUE**

Mandatory identifier.

### **RELATED INFORMATION**

Functions: extract\_aid, make\_sid.

# make\_sid

**Macro**—Builds a security identifier using a mandatory identifier and an authentication identifier.

# LIBRARY

#include <**sys/security.h**>

## **SYNOPSIS**

security_id_t make_sid	
(mandatory_id_t	mid,
authentication_id_t	aid);

# **DESCRIPTION**

The **make\_sid** macro returns a security identifier whose MID and AID fields have the values given in *mid* and *aid*.

# SECURITY

None.

# PARAMETERS

mid

[in mandatory\_id] The input mandatory identifier.

aid

[in authentication\_id] The input authentication identifier.

# **RETURN VALUE**

Security identifier.

# **RELATED INFORMATION**

Functions: extract\_mid, extract\_aid.

# SSI\_compute\_access\_vector

Server Interface- Requests an access vector for a source sid to a target sid

### LIBRARY

#include <sys/security.h>

# **SYNOPSIS**

kern_return_t SSI_compute access vector	
(mach_port_t	SSPort,
security_id_t	SourceSID,
security_id_t	TargetSID,
int	Permission,
int *	RequestID,
mach_access_vector_data_t *	AccessVector,
mach_access_vector_data_t *	CacheControlVector,
mach_access_vector_data_t *	NotificationVector,
mach_access_vector_data_t *	AIDRelevanceVector,
unsigned int *	Timeout,
unsigned int *	PolicyId,
int *	Status);

# DESCRIPTION

The **SSI\_compute\_access\_vector** function is called by a client (possibly the Kernel), when a security fault has occurred. The Security Server uses the provided security identifiers to compute the associated permission information. The request may also be made by any task that has access to the Security Server's general service port.

The decision logic used to compute the permissions between *SourceSID* to *TargetSID* is determined by the system's specific security policy.

# SECURITY

The client must hold the service permission *ss\_kern\_compute\_av* or *ss\_gen\_compute\_av* respectively to *SSPort* depending on whether it is the client or master Security Server port. The Security Server must have *krpsv\_provide\_permission* to the reply port of this request.

# PARAMETERS

### SSPort

[in port] The port from which the Security Server accepts service requests. This is either the client or master Security Server port.

### SourceSID

[in security\_id\_t] The security identifier of the subject which is attempting to make an access.

### Target\_SID

[in security\_id\_t] The security identifier of the object to which the access is being made.

### Permission

[in int] The permission to be checked.

### RequestID

[in/out int \*] A request identifier returned by the Security Server. Not used.

#### AccessVector

[out mach\_access\_vector\_data\_t \*] The access vector which describes the permissions of the *SourceSID* <-> *TargetSID* pair.

#### CacheControlVector

[out mach\_access\_vector\_data\_t \*] An access vector describing the way the access vector cache is to be controlled. Each non-zero bit in the *CacheControlVector* indicates that the corresponding permission bit in the *AccessVector* can be cached.

### NotificationVector

[out mach\_access\_vector\_data\_t \*] An access vector used to control generation of audit information. Each non-zero bit indicates that whenever the corresponding permission bit in the *AccessVector* is used, a audit event will be generated.

### AIDRelevanceVector

[out mach\_access\_vector\_data\_t \*] An access vector describing which permission bits require authentication identifier (AID) verification. Each non-zero bit in this vector indicates that the corresponding permission bit in the *AccessVector* requires cross-AID checks. This output parameter is used by the Kernel to update its internal AID relevance table and keep it consistent with the security policy.

### Timeout

[out unsigned int \*] The absolute clock value at which time the access vector will expire from the cache.

### PolicyId

[out unsigned int \*] A number representing the current revision of the security policy in force. This number will increment everytime a **load\_security\_policy, swap\_security\_server,** or **avc\_cache\_control** with AVC\_FLUSH\_CACHE bit set is performed.

Status

[out int \*] Used to return status information for a security request. Not used.

# **RETURN VALUE**

Generic errors apply.

# **RELATED INFORMATION**

Functions: sec\_access\_provided.

# SSI\_context\_to\_mid

**Server Interface**— Returns the mandatory identifier associated with a security context.

# LIBRARY

#include <sys/security.h>

# **SYNOPSIS**

kern\_return\_t SSI\_context\_to\_mid
 (mach\_port\_t
 mach\_sec\_context\_t
 int
 mandatory\_id\_t \*

SSPort, SecurityContext, SecurityContextLength, MID);

# DESCRIPTION

The **SSI\_context\_to\_mid** function is called by a client when there is a need to get the security identifier that is related to a particular security context. Please refer to **SSI\_mid\_to\_context** for a description of the full security context.

# **SECURITY**

The client must hold ss\_gen\_context\_to\_sid permission to SSPort.

# PARAMETERS

### SSPort

[in mach\_port\_t] The port on which the security server receives requests.

### SecurityContext

[in mach\_sec\_context\_t] The security context to convert. It must be fully specified.

### SecurityContextLength

[in int] The length of the security context in bytes + 1. The maximum value is 256.

### MID

[out mandatory\_id\_t \*] The fully specified mandatory identifier associated with the provided security context.

# **RETURN VALUE**

Generic errors apply.

# **RELATED INFORMATION**

Functions: SSI\_short\_context\_to\_mid, SSI\_mid\_to\_short\_context.

SSI\_mid\_to\_context,

# SSI\_load\_security\_policy

Server Interface—Loads the security policy.

# LIBRARY

#include <sys/security.h>

# **SYNOPSIS**

kern\_return\_t **SSI\_load\_security\_policy** (mach\_port\_t char \* int

SSPort, SecurityPolicyDir,, NameLength);

# DESCRIPTION

The **SSI\_load\_security\_policy** function loads the security policy found in the directory *SecurityPolicyDir*. The database file must be named "database\_file" and the permissions file must be named "permissions\_file". If more than one set of policies is desired, then files describing the policy must be placed in separate directories.

# SECURITY

The client must hold *ss\_gen\_load\_policy* permission to *SSPort*.

# PARAMETERS

SSPort

[in security\_id\_t] The port on which the security server receives requests.

### *SecurityPolicyDir*

[in char \*] The name of the directory which holds the security policy.

NameLength

[in in] The length of the *SecurityPolicyName* in bytes + 1. The maximum value is 1024.

# **RETURN VALUE**

Generic errors apply.

# **RELATED INFORMATION**

Functions: SSI\_transfer\_security\_server\_ports.

# SSI\_record\_name\_server

Server Interface—Provides the name server port right to the security server.

### LIBRARY

#include <sys/security.h>

# **SYNOPSIS**

void SSI\_record\_name\_server

(mach\_port\_t (mach\_port\_t SSPort, NameServerPort);

# DESCRIPTION

The **SSI\_record\_name\_server** function gives the Security Server access to the *NameServerPort*. The Security Server then registers its client port with the name server. This allows clients to look up the port with the name server using the "security\_server\_port" keyword.

# SECURITY

The client must hold ss\_kern\_record\_name\_server permission to SSPort.

# PARAMETERS

#### SSPort

[in mach\_port\_t] The port on which the Security Server receives requests.

#### NameServerPort

[in mach\_port\_t] The port on which the name server receives requests.

# **RETURN VALUE**

Generic errors apply.

# **RELATED INFORMATION.**

Functions: netname\_lookup.

# SSI\_register\_caching\_server

**Server Interface**— Provide a means for programs caching security information to be notified of a flush event.

# LIBRARY

#include <sys/security.h>

### **SYNOPSIS**

void **SSI\_register\_caching\_server** (mach\_port\_t mach\_port\_t

SSPort, FlushNotificationPort);

# DESCRIPTION

The **SSI\_register\_caching\_server** function provides an interface that may be used by other servers caching security information that wish to be notified of a security cache flush event. The supplied port will receive a message containing the policy ID, upon the security server requesting a flush cache. The message format is defined as follows:

simpleroutine flush\_notify (SSPort PolicyId

:mach\_port\_t; :int);

### SECURITY

The client must hold *ss\_gen\_register* permission to the security server client port.

# PARAMETERS

#### SSPort

[in mach\_port\_t] The port on which the Security Server receives client requests.

**FlushNotificationPort** 

[in mach\_port\_t] The port to which the Security Server sends a message when a flush event occurs.

# **RETURN VALUE**

Generic errors apply.

# **RELATED INFORMATION.**

None.

# SSI\_short\_context\_to\_mid

**Server Interface**— Returns the mandatory identifier (MID) associated with a security context specified in the short format.

# LIBRARY

#include <sys/security.h>

### **SYNOPSIS**

kern\_return\_t SSI\_short\_context\_to\_mid

```
(mach_port_t
mach_sec_context_t
int
mandatory_id_t
mandatory_id_t *
```

SSPort, SecurityContext, SecurityContextLength, ParentMID, MID);

# DESCRIPTION

The **SSI\_short\_context\_to\_mid** function is called by a client when there is a need to get the mandatory identifier that is related to a particular security context. This function differs from **SSI\_context\_to\_mid** in that it accepts the short format of the security context and returns a MID whose classifier field is unspecified. This allows "smart" servers to manage this field in a consistent manner with the Security Server. Please refer to **SSI\_mid\_to\_short\_context** for a description of the short security context format.

### SECURITY

The client must hold ss\_gen\_context\_to\_sid permission to SSPort.

### PARAMETERS

SSPort

[in mach\_port\_t] The port on which the Security Server receives requests.

SecurityContext

[in mach\_sec\_context\_t] The short security context to convert.

#### SecurityContextLength

[in int] The length of the security context in bytes + 1. The maximum value is 256.

### ParentMID

[in mandatory\_id\_t ] If the *SecurityContext* contains fields that are unspecified, then the corresponding values are inherited from the context associated with the *ParentMID*.

### MID

[out mandatory\_id\_t \*] The mandatory identifier associated with the provided security context.

# **RETURN VALUE**

Generic errors apply.

# **RELATED INFORMATION**

Functions: SSI\_context\_to\_mid, SSI\_mid\_to\_context.

SSI\_short\_mid\_to\_context,

# SSI\_mid\_to\_context

Server Interface—Returns the security context associated with a mandatory identifier.

### LIBRARY

#include <sys/security.h>

#### **SYNOPSIS**

kern\_return\_t SSI\_mid\_to\_context

(mach\_port\_t mandatory\_id\_t mach\_sec\_context\_t \* int \* SSPort, MID, SecurityContext, SecurityContextLength);

# DESCRIPTION

The **SSI\_mid\_to\_context** function is called by a client when there is a need to get the security context that is related to a particular mandatory identifier. The security context is fully specified following the format "Domain/Type : Level : Categories : Classifier". The Domain/Type field contains either a domain name or a type name. The Level, Categories and Classifier fields have the level name, the comma separated list of category names and the classifier name, respectively.

### SECURITY

The client must hold *ss\_gen\_sid\_to\_context* permission to SSPort.

### PARAMETERS

SSPort

[in mach\_port\_t] The port on which the Security Server receives requests.

#### MID

[in mandatory\_id\_t] The mandatory identifier to convert. It must be fully specified.

#### SecurityContext

[out mach\_sec\_context\_t \*] The full security context associated with the provided security identifier.

### SecurityContextLength

[in/out int \*] The length of the security context in bytes + 1. On input, the variable has the maximum length that the security context can be

(256). On output, it contains the actual length of the security context + 1.

# **RETURN VALUE**

Generic errors apply.

# **RELATED INFORMATION.**

Functions: SSI\_context\_to\_mid, SSI\_mid\_to\_short\_context.

SSI\_short\_context\_to\_mid,

# SSI\_mid\_to\_short\_context

**Server Interface**—Returns the short format of the security context associated with a mandatory identifier (MID).

### LIBRARY

#include <sys/security.h>

### **SYNOPSIS**

kern\_return\_t SSI\_mid\_to\_short\_context

(mach\_port\_t mandatory\_id\_t mach\_sec\_context\_t \* int \* SSPort, MID, SecurityContext, SecurityContextLength);

### DESCRIPTION

The **SSI\_mid\_to\_short\_context** function is called by a client when there is a need to get the short format of the security context that is related to a particular mandatory identifier. This function differs from **SSI\_mid\_to\_context** in that the classifier field of the *MID* need not be specified. The short security context format is "Domain/Type : Level : Categories" where the ":" is a field separator. The Domain/Type field can contain either a domain name or a type name. The Level and Categories fields have the security level name and a list of comma separated category names, respectively. Note that the short security context differs from the full context in that it does not have a classifier field, therefore, the corresponding field in the *MID* is not necessary.

### SECURITY

The client must hold *ss\_gen\_sid\_to\_context* permission to *SSPort*.

# PARAMETERS

```
SSPort
```

[in mach\_port\_t] The port on which the Security Server receives requests.

#### MID

[in mandatory\_id\_t] The mandatory identifier to convert. The classifier field may be unspecified.

#### SecurityContext

[out mach\_sec\_context\_t \*] The short security context associated with the provided mandatory identifier.

SecurityContextLength

[in/out int \*] The length of the security context in bytes + 1. On input, the variable has the maximum length that the security context can be (256). On output, it contains the actual length of the security context + 1.

# **RETURN VALUE**

Generic errors apply.

# **RELATED INFORMATION.**

Functions: SSI\_context\_to\_mid, SSI\_mid\_to\_context.

SSI\_short\_context\_to\_mid,

# SSI\_transfer\_security\_server\_ports

**Server Interface**—Request to transfer Security Server functions to a new program.

### LIBRARY

#include <**sys/security.h**>

### **SYNOPSIS**

kern\_return\_t SSI\_transfer\_security\_server\_ports

```
(mach_port_t
mach_port_t *
mach_port_t *
int *
mach_port_array_t
int *
mach_opaque_table_t
int *
mandatory_id_t
```

### SSPort, Master\_port, Client\_port, Policy\_id, Caching\_control\_ports[], Caching\_control\_port\_count. opaque\_table[], opaque\_table\_count, last\_opaque);

### DESCRIPTION

The **SSI\_transfer\_security\_server\_ports** function wrests control of security services from the current security server, and returns the receive rights for the security services to the calling program.

### **SECURITY**

The client must hold ss\_gen\_transfer permission to SSPort.

### PARAMETERS

SSPort

[in mach\_port\_t] The port on which the Security Server receives requests.

Master\_port

[out mach\_port\_t \*] The Security Server master (kernel) port receive right.

#### Client\_port

[out mach\_port\_t \*] The Security Server client port receive right.

#### Policy\_id

[out int \*] The current policy ID.

### Caching\_control\_ports

[out mach\_port\_array\_t \*] Array of send rights to ports representing other servers in the system that need to be notified of cache flush events.

#### Caching\_control\_port\_count

[out int\*] The number of *caching\_control\_ports* passed in the array.

#### opaque\_table

[out mach\_opaque\_table\_t \*] Array of internal MID to opaque MID translations. This table is used to give the new security server knowledge of the existing opaque MIDs running in the system. The table consists of an array, with each element in the array containing the pairing of an opaque and internal MID.

### opaque\_table\_count

[out int \*] The number of internal<->opaque translations.

#### last\_opaque

[out mandatory\_id\_t \*] The last assigned opaque MID. This is the last opaque MID that had been assigned by the old Security Server.

### **RETURN VALUE**

Generic errors apply.

# **RELATED INFORMATION.**

Functions: SSI\_load\_security\_policy.

# SSI transition domain

Server Interface-Returns a subject SID and an object SID based on the transition domain and MLS level of the input object SID and subject SID, respectively.

### LIBRARY

#include <sys/security.h>

### **SYNOPSIS**

kern\_return\_t SSI\_transition\_domain

SSPort,
InSSID,
InOSID,
OutSSID);

# DESCRIPTION

The **SSI\_transition\_domain** function is called by a client to obtain a subject SID that is of the appropriate domain and security level. If the security policy provides a rule that associates an object type with a transition domain, then the output subject SID corresponds to a security context that has this transition domain. The remaining security context data is the same as that associated with the input subject SID. If no rule is provided by the security policy, the subject SID returned is the same as the input subject SID.

Used in conjunction with the Unix system call execve() or execve\_secure(), this feature allows a client to automatically transition to a new *domain* as a result of executing a file of a particular type. For example, the file /bin/passwd is labeled with the security context passwdTExec:unclassified:none and /etc/passwd is labeled with passwdTFile:unclassi*fied:none*. The security policy database indicates that:

- When a subject executes a file labeled *passwdTExec*, it will transition to passwdD domain
- Only *passwdD* subjects can write to *passwdTFile* files
- passwdD subjects can only access passwdTExec memory
- user subjects cannot write to passwdTExec memory or files

A process labeled with the security context user:unclassified:none invokes execve() on the file /bin/passwd. The Unix server calls SSI\_context\_to\_mid() to convert the input security contexts user:unclassified:none and passwdTExec:unclassified:none into the corresponding subject and object SIDs, respectively. It then calls SSI\_transition\_domain() on these two SIDs to obtain the new transition SID. Finally, the Unix server starts the

new process labeled with the output subject SID. This new process labeled as *passwdD* can write to /etc/passwd which is labeled as *passwdTFile*.

Another process labeled as *user:unclassified:none* tries to call execve\_secure() on vi with a context of *passwdTExec:unclassified:none*, attempting to bypass /bin/passwd and perform arbitrary edits on /etc/passwd. The resulting subject security context is *passwdTExec:unclassified:none*, as expected. However, the executable's object security context is based on vi, which is *user:unclassified:none*. The new process cannot read its own text segment and dies.

# SECURITY

The client must hold *ss\_gen\_transition* permission to *SSPort*.

# PARAMETERS

### SSPort

 $[in \ mach\_port\_t]$  The port on which the Security Server receives requests.

### InSSID

[in security\_id\_t] The input subject security identifier.

#### InOSID

[in security\_id\_t] The input object security identifier.

#### OutSSID

[out security\_id\_t \*] The output subject security identifier.

# **RETURN VALUE**

Generic errors apply.

# **RELATED INFORMATION.**

None.

Security Server Interface

# APPENDIX A MIG Server Routines

This appendix describes server message de-multiplexing routines generated by MIG from the kernel interface definitions of use to a server in handling messages sent from the kernel.

# device\_reply\_server

Function — Handles messages from a kernel device driver

### LIBRARY

libmach\_sa.a, libmach.a

Not declared anywhere.

# **SYNOPSIS**

boolean\_t **device\_reply\_server** (mach\_msg\_header\_t\* mach\_msg\_header\_t\*

in\_msg,
out\_msg);

# DESCRIPTION

The **device\_reply\_server** function is the MIG generated server handling function to handle messages from kernel device drivers. Such messages were sent in response to the various **device\_...\_request**... calls. It is assumed when using those calls that some task is listening for reply messages on the port named as a reply port to those calls. The **device\_reply\_server** function performs all necessary argument handling for a kernel message and calls one of the device server functions to interpret the message.

# PARAMETERS

#### in\_msg

[pointer to in structure] The device driver message received from the kernel.

#### out\_msg

[out structure] A reply message. No messages from a device driver expect a direct reply, so this field is not used.

# **RETURN VALUE**

#### TRUE

The message was handled and the appropriate function was called.

### FALSE

The message did not apply to this device handler interface and no other action was taken.

# **RELATED INFORMATION**

Functions:ds\_device\_open\_reply,ds\_device\_write\_reply\_inband,ds\_device\_read\_reply\_inband.

ds\_device\_write\_reply, ds\_device\_read\_reply,

### exc\_server

Function — Handles kernel messages for an exception handler

### LIBRARY

libmach\_sa.a, libmach.a

Not declared anywhere.

### **SYNOPSIS**

boolean\_t **exc\_server** (mach\_msg\_header\_t\* mach\_msg\_header\_t\*

in\_msg,
out\_msg);

# DESCRIPTION

The exc\_server function is the MIG generated server handling function to handle messages from the kernel relating to the occurrence of an exception in a thread. Such messages are delivered to the exception port set via thread set special port or task set special port. When an exception occurs in a thread, the thread sends an exception message to its exception port, blocking in the kernel waiting for the receipt of a reply. The exc\_server function performs all necessary argument handling for this kernel message and calls catch exception raise, which should handle the exception. If catch\_exception\_raise returns KERN\_SUCCESS, a reply message will be sent, allowing the thread to continue from the point of the exception; otherwise, no reply message is sent and catch\_exception\_raise must have dealt with the exception thread directly.

### PARAMETERS

in\_msg

[pointer to in structure] The exception message received from the kernel.

out\_msg

[out structure] A reply message.

# **RETURN VALUE**

#### TRUE

The message was handled and the appropriate function was called.

#### FALSE

The message did not apply to the exception mechanism and no other action was taken.

# **RELATED INFORMATION**

Functions: thread\_set\_special\_port, catch\_exception\_raise.

task\_set\_special\_port,

# memory\_object\_default\_server

Function — Handles kernel messages for the default memory manager

### LIBRARY

libmach.a only

Not declared anywhere.

# **SYNOPSIS**

boolean\_t **memory\_object\_default\_server** (mach\_msg\_header\_t\* mach\_msg\_header\_t\*

in\_msg,
out\_msg);

# DESCRIPTION

The **memory\_object\_default\_server** function is the MIG generated server handling function to handle messages from the kernel targeted to the default memory manager. This server function only handles messages unique to the default memory manager. Messages that are common to all memory managers are handled by **memory\_object\_server**.

A *memory manager* is a server task that responds to specific messages from the kernel in order to handle memory management functions for the kernel. The **memory\_object\_default\_server** function performs all necessary argument handling for a kernel message and calls one of the default memory manager functions.

# PARAMETERS

in\_msg

[pointer to in structure] The memory manager message received from the kernel.

out\_msg

[out structure] A reply message. No messages to a memory manager expect a direct reply, so this field is not used.

# **RETURN VALUE**

### TRUE

The message was handled and the appropriate function was called.

#### FALSE

The message did not apply to this memory management interface and no other action was taken.

# **RELATED INFORMATION**

Functions:seqnos\_memory\_object\_default\_server,memory\_object\_server,memory\_object\_create,memory\_object\_data\_initialize,default\_pager\_info,default\_pager\_object\_create.

# memory\_object\_server

Function — Handles kernel messages for a memory manager

### LIBRARY

libmach.a only

Not declared anywhere.

# **SYNOPSIS**

boolean\_t **memory\_object\_server** (mach\_msg\_header\_t\* mach\_msg\_header\_t\*

in\_msg,
out\_msg);

# DESCRIPTION

The **memory\_object\_server** function is the MIG generated server handling function to handle messages from the kernel targeted to a memory manager.

A *memory manager* is a server task that responds to specific messages from the kernel in order to handle memory management functions for the kernel. The **memory\_object\_server** function performs all necessary argument handling for a kernel message and calls one of the memory manager functions to interpret the message.

# PARAMETERS

in\_msg

[pointer to in structure] The memory manager message received from the kernel.

out\_msg

[out structure] A reply message. No messages to a memory manager expect a direct reply, so this field is not used.

# **RETURN VALUE**

#### TRUE

The message was handled and the appropriate function was called.

#### FALSE

The message did not apply to this memory management interface and no other action was taken.

# **RELATED INFORMATION**

Functions:memory\_object\_default\_server,memory\_object\_copy,memory\_object\_data\_request,memory\_object\_data\_unlock,memory\_object\_data\_write,memory\_object\_data\_return,memory\_object\_init,memory\_object\_lock\_completed,memory\_object\_change\_completed,memory\_object\_terminate,seqnos\_memory\_object\_server.memory\_object\_terminate,

# notify\_server

Function — Handle kernel generated IPC notifications

### LIBRARY

libmach.a only

Not declared anywhere.

### **SYNOPSIS**

boolean\_t **notify\_server** (mach\_msg\_header\_t\* mach msg header t\*

in\_msg, out\_msg);

# DESCRIPTION

The **notify\_server** function is the MIG generated server handling function to handle messages from the kernel corresponding to IPC notifications. Such messages are delivered to the notification port named in a **mach\_msg** or **mach\_port\_request\_notification** call. The **notify\_server** function performs all necessary argument handling for this kernel message and calls the appropriate handling function. These functions must be supplied by the caller.

# PARAMETERS

in\_msg

[pointer to in structure] The notification message received from the kernel.

out\_msg

[out structure] Not used.

# **RETURN VALUE**

```
TRUE
```

The message was handled and the appropriate function was called.

```
FALSE
```

The message did not apply to the notification mechanism and no other action was taken.

### **RELATED INFORMATION**

Functions:	seqnos_notify_	_server,	mach_msg,
mach_port_request_net_	otification,	do_mach_notify	y_dead_name,
do_mach_notify_msg_	accepted,	do_mach_notif	y_no_senders,

do\_mach\_notify\_port\_deleted, do\_mach\_notify\_send\_once. do\_mach\_notify\_port\_destroyed,

# prof\_server

Function — Handle kernel generated PC sample messages

### LIBRARY

libmach.a only

Not declared anywhere.

# **SYNOPSIS**

boolean\_t **prof\_server** (mach\_msg\_header\_t\* mach\_msg\_header\_t\*

in\_msg,
out\_msg);

# DESCRIPTION

The **prof\_server** function is the MIG generated server handling function to handle messages from the kernel corresponding to program counter (profiling) samples. Such messages are delivered to the task or thread sample port set by **task\_sample** or **thread\_sample**. The **prof\_server** function performs all necessary argument handling for this kernel message and calls the appropriate handling function. These functions must be supplied by the caller.

# PARAMETERS

in\_msg

[pointer to in structure] The sample message received from the kernel.

out\_msg

[out structure] Not used.

# **RETURN VALUE**

#### TRUE

The message was handled and the appropriate function was called.

#### FALSE

The message did not apply to the sample mechanism and no other action was taken.

# **RELATED INFORMATION**

Functions: receive\_samples.

# seqnos\_memory\_object\_default\_server

Function — Handles kernel messages for the default memory manager

### LIBRARY

libmach.a only

Not declared anywhere.

### **SYNOPSIS**

boolean\_t **seqnos\_memory\_object\_default\_server** (mach\_msg\_header\_t\* *in\_msg,* mach\_msg\_header\_t\* *out\_msg*);

# DESCRIPTION

The **seqnos\_memory\_object\_default\_server** function is the MIG generated server handling function to handle messages from the kernel targeted to the default memory manager. This server function only handles messages unique to the default memory manager. Messages that are common to all memory managers are handled by **seqnos\_memory\_object\_server**.

A *memory manager* is a server task that responds to specific messages from the kernel in order to handle memory management functions for the kernel. The **seqnos\_memory\_object\_default\_server** function performs all necessary argument handling for a kernel message and calls one of the default memory manager functions.

### PARAMETERS

in\_msg

[pointer to in structure] The memory manager message received from the kernel.

out\_msg

[out structure] A reply message. No messages to a memory manager expect a direct reply, so this field is not used.

# NOTES

**seqnos\_memory\_object\_default\_server** differs from **memory\_object\_default\_server** in that it supplies message sequence numbers to the server interfaces it calls.

# **RETURN VALUE**

TRUE

The message was handled and the appropriate function was called.

FALSE

The message did not apply to this memory management interface and no other action was taken.

# **RELATED INFORMATION**

Functions: memory\_object\_default\_server, seqnos\_memory\_object\_server, seqnos\_memory\_object\_create, seqnos\_memory\_object\_data\_initialize, seqnos\_default\_pager\_info, seqnos\_default\_pager\_object\_create.

# seqnos\_memory\_object\_server

Function — Handles kernel messages for a memory manager

### LIBRARY

libmach.a only

Not declared anywhere.

### **SYNOPSIS**

boolean\_t seqnos\_memory\_object\_server

(mach_msg_header_t*	in_msg,
mach_msg_header_t*	out_msg);

# DESCRIPTION

The **seqnos\_memory\_object\_server** function is the MIG generated server handling function to handle messages from the kernel targeted to a memory manager.

A *memory manager* is a server task that responds to specific messages from the kernel in order to handle memory management functions for the kernel. The **seqnos\_memory\_object\_server** function performs all necessary argument handling for a kernel message and calls one of the memory manager functions to interpret the message.

# PARAMETERS

[pointer to in structure] The memory manager message received from the kernel.

out\_msg

[out structure] A reply message. No messages to a memory manager expect a direct reply, so this field is not used.

### NOTES

**seqnos\_memory\_object\_server** differs from **memory\_object\_server** in that it supplies message sequence numbers to the server interfaces.

### **RETURN VALUE**

### TRUE

The message was handled and the appropriate function was called.

in\_msg

FALSE

The message did not apply to this memory management interface and no other action was taken.

# **RELATED INFORMATION**

Functions:seqnos\_memory\_object\_default\_server,seqnos\_memory\_object\_copy,seqnos\_memory\_object\_data\_request,seqnos\_memory\_object\_data\_unlock,seqnos\_memory\_object\_data\_write,seqnos\_memory\_object\_data\_return,seqnos\_memory\_object\_init,seqnos\_memory\_object\_lock\_completed,seqnos\_seqnos\_memory\_object\_change\_completed,seqnos\_memory\_object\_terminate,memory\_object\_server.

# seqnos\_notify\_server

Function — Handle kernel generated IPC notifications

### LIBRARY

libmach.a only

Not declared anywhere.

### **SYNOPSIS**

boolean\_t seqnos\_notify\_server

(mach\_msg\_header\_t\*in\_msg,mach\_msg\_header\_t\*out\_msg);

# DESCRIPTION

The **seqnos\_notify\_server** function is the MIG generated server handling function to handle messages from the kernel corresponding to IPC notifications. Such messages are delivered to the notification port named in a **mach\_msg** or **mach\_port\_request\_notification** call. The **seqnos\_notify\_server** function performs all necessary argument handling for this kernel message and calls the appropriate handling function. These functions must be supplied by the caller.

# PARAMETERS

in\_msg

[pointer to in structure] The notification message received from the kernel.

out\_msg

[out structure] Not used.

### NOTES

**seqnos\_notify\_server** differs from **notify\_server** in that it supplies message sequence numbers to the server interfaces.

### **RETURN VALUE**

# TRUE

The message was handled and the appropriate function was called.

### FALSE

The message did not apply to the notification mechanism and no other action was taken.

# **RELATED INFORMATION**

Functions: notify\_server, mach\_msg, i do\_seqnos\_mach\_notify\_dead\_name, do\_seqnos\_mach\_notify\_msg\_accepted, do\_seqnos\_mach\_notify\_no\_senders, do\_seqnos\_mach\_notify\_port\_deleted, do\_seqnos\_mach\_notify\_port\_destroyed, do\_seqnos\_mach\_notify\_send\_once.

mach\_port\_request\_notification,

# APPENDIX B Default Memory Management Interface

In general, the default memory manager is just like any other memory manager, except that it is "trusted" to respond promptly to paging requests in as much as that it is the memory manager of last resort. There are a few special requests issued to the default memory manager having to do with the creation and management of anonymous memory.

# default\_pager\_info

Server Interface — Return default partition information

### LIBRARY

libmach.a only

#include <mach/default\_pager\_object.h>

# **SYNOPSIS**

kern\_return\_t default\_pager\_info

(mach_port_t	pager,
vm_size_t*	total,
vm_size_t*	free);

# seqnos\_default\_pager\_info Sequence Number form

kern\_return\_t seqnos\_default\_pager\_info

(mach_port_t	pager,
mach_port_seqno_t	seqno,
vm_size_t*	total,
vm_size_t*	free);

# DESCRIPTION

A **default\_pager\_info** function is called as the result of a message requesting that the default memory manager return information concerning the default pager's default paging partition. The kernel does not make this call itself (which is why it can be a synchronous call); this request is only issued by (privileged) tasks holding a default memory managed object port.

# PARAMETERS

pager

[in default-pager port] A port to the default memory manager.

seqno

[in scalar] The sequence number of this message relative to the pager port.

total

[out scalar] Total size of the default partition.

free

[out scalar] Free space in the default partition.

# **RETURN VALUE**

The default memory manager should return KERN\_SUCCESS if it returns the desired information and KERN\_FAILURE if it does not support the operation.

# **RELATED INFORMATION**

Functions:vm\_set\_default\_memory\_manager,memory\_object\_default\_server, seqnos\_memory\_object\_default\_server.

# default\_pager\_object\_create

Server Interface — Create a memory object managed by the default pager

### LIBRARY

libmach.a only

#include <mach/default\_pager\_object.h>

### **SYNOPSIS**

kern\_return\_t **default\_pager\_object\_create** (mach\_port\_t memory\_object\_t\* vm\_size\_t

pager, memory\_object, object\_size);

### seqnos\_default\_pager\_object\_create Sequence Number form

kern\_return\_t seqnos\_default\_pager\_object\_create

(mach\_port\_t
mach\_port\_seqno\_t
memory\_object\_t\*
vm\_size\_t

pager, seqno, memory\_object, object\_size);

# **DESCRIPTION**

A **default\_pager\_object\_create** function is called as the result of a message requesting that the default memory manager create and return a (shared) memory object which is suitable for use with **vm\_map**. This memory object has the same properties as does a memory object provided by **vm\_allocate**: its initial contents are zero and the backing contents are temporary in that they do not persist after the memory object is destroyed. The memory object is suitable for use as non-permanent shared memory. The kernel does not make this call itself (which is why it can be a synchronous call); this request is only issued by (privileged) tasks holding a default memory managed object port. This call should be contrasted with the kernel's **memory\_object\_create** message, in which the memory cache object is already created and the identity of the abstract memory object is made known to the default manager.

### PARAMETERS

pager

[in default-pager port] A port to the default memory manager.

seqno

[in scalar] The sequence number of this message relative to the pager port.

memory\_object

[out abstract-memory-object port] The abstract memory object port for the memory object.

object\_size

[in scalar] The maximum size for the memory object.

### **RETURN VALUE**

Return KERN\_SUCCESS if the object was created.

## **RELATED INFORMATION**

Functions:vm\_map,vm\_set\_default\_memory\_manager,memory\_object\_create,memory\_object\_default\_server,seqnos\_memory\_object\_default\_server.

### memory\_object\_create

**Server Interface** — Requests transfer of responsibility for a kernel-created memory object

#### LIBRARY

Not declared anywhere.

#### **SYNOPSIS**

kern\_return\_t memory\_object\_create

(mach\_port\_t mach\_port\_t vm\_size\_t mach\_port\_t mach\_port\_t vm\_size\_t old\_memory\_object, new\_memory\_object, new\_object\_size, new\_control, new\_name, new\_page\_size);

# seqnos\_memory\_object\_create

Sequence Number form

kern_	_return_	_t seqnos_	_memory	_object_	create
-------	----------	------------	---------	----------	--------

(mach\_port\_t mach\_port\_seqno\_t mach\_port\_t vm\_size\_t mach\_port\_t mach\_port\_t vm\_size\_t old\_memory\_object, seqno, new\_memory\_object, new\_object\_size, new\_control, new\_name, new\_page\_size);

## DESCRIPTION

A **memory\_object\_create** function is called as the result of a message from the kernel requesting that the default memory manager accept responsibility for the new memory object created by the kernel. The kernel makes this call only to the system default memory manager.

The new memory object initially consists of zero-filled pages. Only memory pages that are actually written are provided to the memory manager. When processing **memory\_object\_data\_request** calls from the kernel, the default memory manager must use **memory\_object\_data\_unavailable** for any pages that have not been written previously.

The kernel does not expect a reply to this call. The kernel assumes that the default memory manager will be ready to handle data requests to this object and does not need the confirmation of a **memory\_object\_ready** call.

#### PARAMETERS

#### old\_memory\_object

[in default-pager port] An existing abstract memory object provided by the default memory manager.

seqno

[in scalar] The sequence number of this message relative to the old abstract memory object port.

new\_memory\_object

[in abstract-memory-object port] The port representing the new abstract memory object created by the kernel. The kernel provides all port rights (including the receive right) for the new memory object.

new\_object\_size

[in scalar] The expected size for the new object, in bytes.

new\_control

[in memory-cache-control port] The memory cache control port to be used by the memory manager when making cache management requests for the new object.

new\_name

[in memory-cache-name port] The memory cache name port used by the kernel to refer to the new memory object data in response to **vm\_region** calls.

#### new\_page\_size

[in scalar] The page size used by the kernel. All calls involving this kernel must use data sizes that are integral multiples of this page size.

#### NOTES

The kernel requires memory objects to provide temporary backing storage for zero-filled memory created by **vm\_allocate** calls, issued by both user tasks and the kernel itself. The kernel allocates an abstract memory object port to represent the temporary backing storage and uses **memory\_object\_create** to pass the new memory object to the default memory manager, which provides the storage.

The default memory manager is a trusted system component that is identified to the kernel at system initialization time. The default memory manager can also be changed at run time using the **vm\_set\_default\_memory\_manager** call.

The contents of a kernel-created (as opposed to a user-created) memory object can be modified only in main memory. The default memory manager must not change the contents of a temporary memory object, or allow unrelated tasks to access the memory object, control, or name port. The kernel provides the size of a temporary memory object based on the allocated size. Since the object is not mapped by other tasks, the object will not grow by explicit action. However, the kernel may coalesce adjacent temporary objects in such a way that this object may appear to grow. As such, the supplied object size is merely a hint as to the maximum size.

### **RETURN VALUE**

Any return value other than KERN\_SUCCESS or MIG\_NO\_REPLY causes **mach\_msg\_server** to remove the abstract memory object, memory cache control and memory cache name port references.

#### **RELATED INFORMATION**

Functions:default\_pager\_object\_create,<br/>memory\_object\_data\_unavailable,memory\_object\_data\_initialize,<br/>memory\_object\_default\_server,seqnos\_memory\_object\_default\_server.memory\_object\_default\_server.

# memory\_object\_data\_initialize

Server Interface — Writes initial data back to a temporary memory object

#### LIBRARY

Not declared anywhere.

#### **SYNOPSIS**

kern\_return\_t memory\_object\_data\_initialize

(mach\_port\_tmemory\_object,mach\_port\_tmemory\_control,vm\_offset\_toffset,vm\_offset\_tdata,vm\_size\_tdata\_count);

## seqnos\_memory\_object\_data\_initialize

Sequence Number form

kern\_return\_t seqnos\_memory\_object\_data\_initialize

(mach\_port\_tmemory\_object,mach\_port\_seqno\_tseqno,mach\_port\_tmemory\_control,vm\_offset\_toffset,vm\_offset\_tdata,vm\_size\_tdata\_count);

#### DESCRIPTION

A **memory\_object\_data\_initialize** function is called as the result of a kernel message providing the default memory manager with initial data for a kernelcreated memory object. If the memory manager already has supplied data (by a previous **memory\_object\_data\_initialize** or **memory\_object\_data\_return**), it should ignore this call. Otherwise, the call behaves the same as the **memory\_object\_data\_return** call.

The kernel makes this call only to the default memory manager and only on temporary memory objects that it has created with **memory\_object\_create**. Note that the kernel does not make this call on objects created via **memory\_object\_copy**.

## PARAMETERS

memory\_object

[in abstract-memory-object port] The abstract memory object port that represents the memory object data, as supplied by the kernel in a **memory\_object\_create** call.

seqno

[in scalar] The sequence number of this message relative to the abstract memory object port.

#### memory\_control

[in memory-cache-control port] The memory cache control port to be used for a response by the memory manager. If the memory object has been supplied to more than one kernel, this parameter identifies the kernel that is making the call.

```
offset
```

[in scalar] The offset within the memory object.

data

[in pointer to dynamic array of bytes] The data that has been modified while cached in physical memory.

data\_count

[in scalar] The number of bytes to be written, starting at *offset*. The number converts to an integral number of memory object pages.

## **RETURN VALUE**

Any return value other than KERN\_SUCCESS or MIG\_NO\_REPLY causes **mach\_msg\_server** to remove the memory cache control port reference and to de-allocate the returned data.

## **RELATED INFORMATION**

Functions:memory\_object\_create,memory\_object\_data\_return,memory\_object\_default\_server, seqnos\_memory\_object\_default\_server.

# vm\_set\_default\_memory\_manager

Function — Sets the default memory manager.

#### **SYNOPSIS**

kern\_return\_t vm\_set\_default\_memory\_manager (mach\_port\_t mach\_port\_t\* default\_manager);

#### DESCRIPTION

The vm\_set\_default\_memory\_manager function establishes the default memory manager for a host.

## **SECURITY**

The requesting task must hold hpsv\_set\_default\_memory\_mgr permission to host\_priv.

## PARAMETERS

host\_priv

[in host-control port] The control port naming the host for which the default memory manager is to be set.

#### default\_manager

[pointer to in/out default-pager port] A memory manager port to the new default memory manager. If this value is MACH\_PORT\_NULL, the old memory manager is not changed. The old memory manager port is returned in this variable.

#### **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: memory\_object\_create, vm\_allocate.

host\_priv,

**Default Memory Management Interface** 

# APPENDIX C Multicomputer Support

Support for multicomputers is being added to the Mach kernel. This provides transparent support for distributed, non-shared-memory environments. The current support does not handle node failures and so is suitable to multicomputer environments but not yet to net-worked workstation environments.

With this support, a single logical Mach kernel is formed that spans a set of computers. This support transparently distributes Mach IPC and virtual memory. However, each host (called a *node*) within the multicomputer maintains its identity (separate control and name ports, processor sets, devices, etc.).

This appendix describes operations that apply to individual nodes in such a configuration.

# norma\_get\_special\_port

Function — Returns a send right to a node specific port

# LIBRARY

#include <mach/norma\_special\_ports.h>

## **SYNOPSIS**

kern_return_t norma_get_special_port	
(mach_port_t	host_priv,
int	node,
int	which_port,
mach_port_t*	<pre>special_port);</pre>
norma_get_device_port Macro form	
kern_return_t norma_get_device_port	
(mach_port_t	host_priv,
int	node,
mach_port_t*	special_port)
•	
⇒ norma_get_special_port (host_priv, node, NORMA_DEVIC special_port)	CE_PORT,
norma_get_host_paging_port Macro form	
kern_return_t norma_get_host_paging_port	
(mach_port_t	host_priv,
int	node,
mach_port_t*	special_port)
⇒ norma_get_special_port (host_priv, node, NORMA_HOST_PAGING_PORT, special_port)	
norma_get_host_port Macro form	
kern_return_t norma_get_host_port	
(mach_port_t	host_priv,
int	node,
mach_port_t*	special_port)
→ norma_get_special_port (host_priv, node, NORMA_HOST special_port)	
norma_get_host_priv_port Macro form	
kern_return_t norma_get_host_priv_port	
(mach_port_t	host_priv,

int	node,
mach_port_t*	special_port)

⇒ norma\_get\_special\_port (host\_priv, node, NORMA\_HOST\_PRIV\_PORT, *special\_port*)

norma\_get\_nameserver\_port

Macro form

kern_return_t norma_get_nameserver_port		
(mach_port_t	host_priv,	
int	node,	
mach_port_t*	special_port)	

 $\Rightarrow$  **norma\_get\_special\_port** (*host\_priv*, *node*, NORMA\_NAMESERVER\_PORT, special\_port)

#### **DESCRIPTION**

The norma\_get\_special\_port function returns a send right for a special port belonging to node on host\_priv.

Each node maintains a (small) set of node specific ports. The device master port, host paging port, host name and host control port are maintained by the kernel. The kernel also permits a small set of server specified node specific ports; the name server port is an example and is given (by convention) an assigned special port index.

#### PARAMETERS

#### host\_priv

[in host-control port] The control port for the host for which to return the special port's send right.

node

[in scalar] The index of the node for which the port is desired.

#### which port

[in scalar] The index of the special port for which the send right is requested. Valid values are:

NORMA DEVICE PORT [device-master port] The device master port for the node.

#### NORMA HOST PAGING PORT

[default-pager port] The default pager port for the node.

#### NORMA\_HOST\_PORT

[host-name port] The host name port for the node. If the specified node is the current node, this value (unless otherwise set) is the same as would be returned by **mach\_host\_self**.

#### NORMA\_HOST\_PRIV\_PORT

[host-control port] The host control port for the node.

#### NORMA\_NAMESERVER\_PORT

[name-server port] The registered name server port for the node.

#### special\_port

[out norma-special port] The returned value for the port.

#### **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: mach\_host\_self, vm\_set\_default\_memory\_manager.

norma\_set\_special\_port,

# norma\_port\_location\_hint

Function — Guess a port's current location

#### LIBRARY

Not declared anywhere.

#### **SYNOPSIS**

kern\_return\_t norma\_port\_location\_hint

(mach_port_t	task,
mach_port_t	port,
int*	node);

## DESCRIPTION

The **norma\_port\_location\_hint** function returns the best guess of *port*'s current location. The hint is guaranteed to be a node where the port once was; it is guaranteed to be accurate if port has never moved. This can be used to determine residence node for hosts, tasks, threads, etc.

## PARAMETERS

task

[in task port] Task reference (not currently used)

port

[in random port] Send right to the port to locate.

#### node

[out scalar] Port location hint

#### **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: task\_set\_child\_node, norma\_task\_create.

# norma\_set\_special\_port

Function — Sets a node specific special port

# LIBRARY

#include <mach/norma\_special\_ports.h>

## **SYNOPSIS**

]	kern_return_t norma_set_special_port	
	(mach_port_t	host_priv,
	int	node,
	int	which_port,
	mach_port_t	<pre>special_port);</pre>
	_set_device_port Macro form	
]	kern_return_t norma_set_device_port	
	(mach_port_t	host_priv,
	int	node,
	mach_port_t	special_port)
:	⇒ norma_set_special_port (host_priv, node, NORMA_DEV special_port)	ICE_PORT,
norma	_set_host_paging_port	
	Macro form	
]	kern_return_t norma_set_host_paging_port	
	(mach_port_t	host_priv,
	int	node,
	mach_port_t	special_port)
:	⇒ norma_set_special_port (host_priv, node, NORMA_HOST_PAGING_PORT, special_port)	)
norma	_set_host_port	
	Macro form	
]	kern_return_t norma_set_host_port	
	(mach_port_t	host_priv,
	int	node,
	mach_port_t	special_port)
:	⇒ norma_set_special_port (host_priv, node, NORMA_HOS' special_port)	T_PORT,
norma	_set_host_priv_port	
	Macro form	
]	kern_return_t norma_set_host_priv_port	
	(mach_port_t	host_priv,

int	node,
mach_port_t	special_port)

⇒ norma\_set\_special\_port (host\_priv, node, NORMA\_HOST\_PRIV\_PORT, *special\_port*)

#### norma\_set\_nameserver\_port

Macro form

kern_return_t norma_set_nameserver_port		
(mach_port_t	host_priv,	
int	node,	
mach_port_t	special_port)	

⇒ norma\_set\_special\_port (host\_priv, node, NORMA\_NAMESERVER\_PORT, special\_port)

#### **DESCRIPTION**

The norma\_set\_special\_port function sets the special port belonging to node on *host\_priv*.

Each node maintains a (small) set of node specific ports. The device master port, host paging port, host name and host control port are maintained by the kernel. The kernel also permits a small set of server specified node specific ports; the name server port is an example and is given (by convention) an assigned special port index.

#### PARAMETERS

#### host\_priv

[in host-control port] The host for which to set the special port. Currently, this must be the per-node host control port.

node

[in scalar] The index of the node for which the port is to be set.

#### which port

[in scalar] The index of the special port to be set. Valid values are:

## NORMA\_DEVICE\_PORT

[device-master port] The device master port for the node.

#### NORMA\_HOST\_PAGING\_PORT

[default-pager port] The default pager port for the node.

#### NORMA HOST PORT

[host-name port] The host name port for the node.

NORMA\_HOST\_PRIV\_PORT [host-control port] The host control port for the node.

#### NORMA\_NAMESERVER\_PORT

[name-server port] The registered name server port for the node.

special\_port

[in norma-special port] A send right to the new special port.

#### **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions:mach\_host\_self,norma\_get\_special\_port,vm\_set\_default\_memory\_manager.

## norma\_task\_clone

Function — "Clone" a task on a specified node

#### LIBRARY

Not declared anywhere.

#### **SYNOPSIS**

kern\_return\_t norma\_task\_clone

(mach\_port\_t boolean\_t int mach\_port\_t\* parent\_task, inherit\_memory, child\_node, child\_task);

#### DESCRIPTION

The **norma\_task\_clone** function "clones" a new task from *parent\_task* on the specified *node* and returns the name of the new task in *child\_task*. The child task acquires shared parts of the parent's address space (see **vm\_inherit**) regardless of the inheritance set for the parent's memory regions, although the inheritance for the child's regions will be set to that of the parent's regions. The child task initially contains no threads.

By way of comparison, tasks created by the standard **task\_create** primitive are created on the node last set by **task\_set\_child\_node** (by default the *parent\_task*'s node).

Other than being created on a different node, the new task has the same properties as if created by **task\_create**.

#### PARAMETERS

parent\_task

[in task port] The port for the task from which to draw the child task's port rights, resource limits, and address space.

inherit\_memory

[in scalar] Address space inheritance indicator. If true, the child task inherits the address space of the parent task. If false, the kernel assigns the child task an empty address space.

[in scalar] The node index of the node on which to create the child.

child\_task

[out task port] The kernel-assigned port name for the new task.

child\_node

#### NOTES

This call differs from **norma\_task\_create** in that the inheritance set for the parent's memory regions is ignored; the child always shares memory with the parent.

This call is intended to support process migration, where the inheritance semantics of **norma\_task\_create** would break migrated programs that depended upon sharing relationships remaining after migration.

This call is not a true task migration call, in that it does not migrate the port space, threads, and other non-address-space attributes of the task.

## **RETURN VALUE**

Only generic errors apply.

# **RELATED INFORMATION**

Functions: task\_create, norma\_task\_create, task\_set\_child\_node.

## norma\_task\_create

Function — Create a task on a specified node

#### LIBRARY

Not declared anywhere.

#### **SYNOPSIS**

kern\_return\_t norma\_task\_create

(mach\_port\_t boolean\_t int mach\_port\_t\* parent\_task, inherit\_memory, child\_node, child\_task);

#### DESCRIPTION

The **norma\_task\_create** function creates a new task from *parent\_task* on the specified *node* and returns the name of the new task in *child\_task*. The child task acquires shared or copied parts of the parent's address space (see **vm\_inherit**). The child task initially contains no threads.

By way of comparison, tasks created by the standard **task\_create** primitive are created on the node last set by **task\_set\_child\_node** (by default the *parent\_task*'s node).

Other than being created on a different node, the new task has the same properties as if created by **task\_create**.

#### PARAMETERS

parent\_task

[in task port] The port for the task from which to draw the child task's port rights, resource limits, and address space.

inherit\_memory

[in scalar] Address space inheritance indicator. If true, the child task inherits the address space of the parent task. If false, the kernel assigns the child task an empty address space.

#### child\_node

[in scalar] The node index of the node on which to create the child.

#### child\_task

[out task port] The kernel-assigned port name for the new task.

## **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: task\_create, norma\_task\_clone, task\_set\_child\_node.SECURITY

The requesting task must hold hsv\_get\_host\_name permission to the processor's host name port.

# task\_set\_child\_node

Function — Set the node upon which future child tasks will be created

#### LIBRARY

Not declared anywhere.

#### **SYNOPSIS**

kern_return_t task_set_child_node		
(mach_port_t	task,	
int	child_node);	

## DESCRIPTION

The **task\_set\_child\_node** function specifies a node upon which child tasks will be created. This call exists only to allow testing with unmodified servers. Server developers should use **norma\_task\_create** instead.

## PARAMETERS

task

[in task port] The task who's children are to be affected.

node

[in scalar] The index of the node upon which future children should be created.

#### **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: norma\_task\_create, norma\_task\_clone.

Multicomputer Support

# APPENDIX D Intel 386 Support

This appendix describes special kernel interfaces to support the special hardware features of the Intel 386 processor and its successors.

Aside from the special functions listed here, the Intel 386 support also includes special thread state "flavors" (See **mach/thread\_status.h**.).

- i386\_THREAD\_STATE—Basic machine thread state, except for segment and floating registers.
- i386\_REGS\_SEGS\_STATE—Same as i386\_THREAD\_STATE but also sets/gets segment registers.
- i386\_FLOAT\_STATE—Floating point registers.
- i386\_V86\_ASSIST\_STATE—Virtual 8086 interrupt table.

(The i386\_ISA\_PORT\_MAP\_STATE flavor shown in **mach/thread\_status.h** has been disabled.)

# **IO Permission Bitmap**

The 386 supports direct IO instructions. Generally speaking, these instructions are privileged (sensitive to IOPL). Mach, in combination with the processor, allows threads to directly execute these instructions against hardware IO ports for which the thread has permission (those named in its IO permission bitmap). (Note that this is a per-thread property.) The **i386\_io\_port\_add** function enables IO to the port corresponding to the device port supplied to the call. **i386\_io\_port\_remove** disables such IO; **i386\_io\_port\_list** lists the devices to which IO is permitted.

For the sake of supporting the DOS emulator, the kernel supports a special device *iopl*. Access to this device implies access to the speaker, configuration CMOS, game port,

sound blaster, printer and the VGA ports (device *kd0* or *vga*). Attempting to execute an IO instruction against one of these devices when the task holds send rights to the *iopl* device automatically adds these devices to the IO permission bitmap.

#### Virtual 8086 Support

Virtual 8086 mode is supported by Mach, enabled when the EFL\_VM (virtual machine) flag in the thread state $\rightarrow efl$  is set. The various instructions sensitive to IOPL are simulated by the Mach kernel. This includes simulating an interrupt enabled flag and associated instructions.

A virtual 8086 task receives simulated 8086 interrupts by setting an interrupt descriptor table (in task space). This table is set with the i386\_V86\_ASSIST\_STATE status flavor.

[1]	struct i386_v86_assist_state	
[2]	{	
[3]	unsigned int	int_table;
[4]	int	int_count;
[5]	};	
[6]	#define i386_V86_ASSIST_STA	
	(sizeof (struct <b>i386</b> _	v86_assist_state)/sizeof(unsigned int))

The *int\_table* field points to an interrupt table in task space. The table has *int\_count* entries. Each entry of this table has the format shown below.

[1]	struct v86_interrupt_table	
[2]	{	
[3]	unsigned int	count;
[4]	unsigned short	mask;
[5]	unsigned short	vec;
[6]	};	

When the 8086 task has an associated interrupt table and its simulated interrupt enable flag is set, the kernel will scan the table looking for an entry whose *count* is greater than zero and whose *mask* value is not set. If found, the count will be decremented and the task will take a simulated 8086 interrupt to the address given by *vec*. No other simulated interrupts will be generated until the 8086 task executes an *iret* instruction and the (simulated) interrupt enable flag is again set. The generation of the simulated interrupt will turn off the hardware's trace trap flag; executing the *iret* instruction will restore the trace trap flag.

#### **Local Descriptor Table**

Although the 386 (and successors) view the address space as segmented, Mach provides each task with a linear address space (32 bits for the Intel family). The various entries in the system global descriptor table (GDT) are used for system use; in general the entries map all of kernel memory. The thread's local descriptor table (LDT) maps its task space. Segment 2 of this table is used for task code accesses (it permits only read access); segment 3 is used for data accesses (it permits write access, subject to page level protections); both segments, though, map all of the task's address space. Segment 1 of the table is unused. Segment 0 is used as a call gate for system calls (traps).

Each thread may set entries in its LDT to describe various ranges of its underlying address space. There is no way that this mechanism permits a thread to access any more virtual memory than its address space permits; these LDT segment entries merely provide different views of the address space. A segment may be thought of as an automatically relocated portion of the address space; the beginning of a segment can be referenced as address zero given the appropriately set 386 segment register. These local segment descriptors are manipulated with the **i386\_set\_ldt** function and examined with the **i386\_get\_ldt** function.

# i386\_get\_ldt

Function — Return per-thread segment descriptors

#### LIBRARY

#include <mach/i386/mach\_i386.h>

### **SYNOPSIS**

[1]	struct descriptor	
[2]	{	
[3]	unsigned int	low_word;
[4]	unsigned int	high_word;
[5]	};	
[6]	typedef struct descriptor	descriptor_t;
[7]	typedef struct descriptor*	<pre>descriptor_list_t;</pre>
	kern_return_t i386_get_ldt	
	(mach_port_t	thread,
	int	first_selector,
	int	desired_count,
	descriptor_list_t*	desc_list,

mach\_msg\_type\_number\_t\*

#### **DESCRIPTION**

The i386\_get\_ldt function returns per-thread segment descriptors from the thread's local descriptor table (LDT).

returned\_count);

#### **SECURITY**

The requesting task must hold *thsv\_get\_thread\_info* permission to *thread*.

### PARAMETERS

#### thread

[in thread port] Thread whose segment descriptors are to be returned

#### first\_selector

[in scalar] Selector value (segment register value) corresponding to the first segment whose descriptor is to be returned

#### desired count

[in scalar] Number of returned descriptors desired

desc\_list

[unbounded out in-line array of *descriptor\_t*] Array of segment descriptors. The reserved size of this array is supplied as the input value for *returned\_count*.

#### returned\_count

[pointer to in/out scalar] On input, the reserved size of the descriptor array; on output, the number of descriptors returned

## **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: i386\_set\_ldt.

# i386\_io\_port\_add

Function — Permit IO instructions to be performed against a device

#### LIBRARY

#include <mach/i386/mach\_i386.h>

#### **SYNOPSIS**

kern\_return\_t **i386\_io\_port\_add** (mach\_port\_t mach\_port\_t

thread, device);

## DESCRIPTION

The **i386\_io\_port\_add** function adds a device to the IO permission bitmap for a thread, thereby permitting the thread to execute IO instructions against the device.

#### SECURITY

The requesting task must hold *thsv\_set\_thread\_environment* permission to *thread*.

#### PARAMETERS

thread

[in thread port] Thread whose permission bitmap is to be set.

device

[in device port] The device to which IO instructions are to be permitted.

#### NOTES

Normally, the thread must have called **i386\_io\_port\_add** for all devices to which it will execute IO instructions. However, possessing send rights to the *iopl* device port will cause the *iopl* device to be automatically added to the thread's IO map upon first attempted access. This is a backward compatibility feature for the DOS emulator.

## **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: i386\_io\_port\_list, i386\_io\_port\_remove.

# i386\_io\_port\_list

Function — List devices permitting IO

## LIBRARY

#include <mach/i386/mach\_i386.h>

#### **SYNOPSIS**

kern\_return\_t i386\_io\_port\_list

(mach_port_t	thread,
device_list_t*	list,
mach_msg_type_number_t*	count);

## DESCRIPTION

The **i386\_io\_port\_list** function returns a list of the devices named in the thread's IO permission bitmap, namely those permitting IO instructions to be executed against them.

## SECURITY

The requesting task must hold *thsv\_get\_thread\_info* permission to *thread*.

## PARAMETERS

thread

[in thread port] Thread whose permission list is to be returned

list

[out pointer to dynamic array of device ports] Device ports permitting IO

count

[out scalar] The number of ports returned

## **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: i386\_io\_port\_add, i386\_io\_port\_remove.

# i386\_io\_port\_remove

Function — Disable IO instructions against a device

#### LIBRARY

#include <mach/i386/mach\_i386.h>

#### **SYNOPSIS**

kern\_return\_t **i386\_io\_port\_remove** (mach\_port\_t mach\_port\_t

thread, device);

#### DESCRIPTION

The **i386\_io\_port\_remove** function removes the specified device from the thread's IO permission bitmap, thereby prohibiting IO instructions being executed against the device.

#### **SECURITY**

The requesting task must hold *thsv\_set\_thread\_environment* permission to *thread*.

## PARAMETERS

thread

[in thread port] Thread whose permission bitmap is to be cleared

#### device

[in device port] Device whose permission is to be revoked

#### **RETURN VALUE**

Only generic errors apply.

#### **RELATED INFORMATION**

Functions: i386\_io\_port\_add, i386\_io\_port\_list.

# i386\_set\_ldt

Function — Set per-thread segment descriptors

#### LIBRARY

#include <mach/i386/mach\_i386.h>

#### **SYNOPSIS**

- [1] struct **descriptor**
- [2] {
- [3] unsigned int
- [4] unsigned int
- [5] };
- [6] typedef struct descriptor
- [7] typedef struct descriptor\*

descriptor\_t; descriptor\_list\_t;

low\_word;

high\_word;

kern\_return\_t **i386\_set\_ldt** (mach\_port\_t int descriptor\_list\_t mach\_msg\_type\_number\_t

thread, first\_selector, desc\_list, count);

#### DESCRIPTION

The **i386\_set\_ldt** function allows a thread to have a private local descriptor table (LDT) which allows its local segments to map various ranges of its address space.

## SECURITY

The requesting task must hold *thsv\_set\_thread\_environment* permission to *thread*.

#### PARAMETERS

thread

[in thread port] Thread whose segment descriptors are to be set

#### first\_selector

[in scalar] Selector value (segment register value) corresponding to the first segment whose descriptor is to be set

#### desc\_list

[pointer to in array of *descriptor\_t*] Array of segment descriptors. The following forms are permitted:

- Empty descriptor. The ACC\_P flag (segment present) may or may not be set.
- ACC\_CALL\_GATE Converted into a system call gate. The ACC\_P flag must be set.

All other descriptors must have both the ACC\_P flag set and specify user mode access (ACC\_PL\_U).

- ACC\_DATA
- ACC\_DATA\_W
- ACC\_DATA\_E
- ACC\_DATA\_EW
- ACC\_CODE
- ACC\_CODE\_R
- ACC\_CODE\_C
- ACC\_CODE\_CR
- ACC\_CALL\_GATE\_16
- ACC\_CALL\_GATE

count

[in scalar] Number of descriptors to be set

#### **RETURN VALUE**

Only generic errors apply.

## **RELATED INFORMATION**

Functions: i386\_get\_ldt.

# APPENDIX E Data Structures

This appendix discusses the specifics of the various structures used as a part of the kernel's various interfaces. This appendix does not discuss all of the various data types used by the kernel's interfaces, only the fields of the various structures used.

# host\_basic\_info

Structure — Defines basic information about a host

## SYNOPSIS

[1]	struct host_basic_info	
[2]	{	
[3]	int	max_cpus;
[4]	int	avail_cpus;
[5]	vm_size_t	memory_size;
[6]	cpu_type_t	cpu_type;
[7]	cpu_subtype_t	cpu_subtype;
[8]	};	
[9]	typedef struct host_basic_info	<pre>host_basic_info_data_t;</pre>
[10]	typedef struct host_basic_info*	<pre>host_basic_info_t;</pre>

## DESCRIPTION

The **host\_basic\_info** structure defines the basic information available about a host.

## **FIELDS**

max\_cpus

Maximum possible CPUs for which kernel is configured

#### avail\_cpus

Number of CPUs now available

memory\_size

Size of memory, in bytes

cpu\_type

CPU type

*cpu\_subtype* CPU sub-type

### NOTES

This structure is machine word length specific because of the memory size returned.

#### **RELATED INFORMATION**

Functions: host\_info.

Data structures: host\_load\_info, host\_sched\_info.

# host\_load\_info

Structure — Defines load information about a host

## **SYNOPSIS**

[1]	#define CPU_STATE_USER	0
[2]	#define CPU_STATE_SYSTEM	1
[3]	#define CPU_STATE_IDLE	2
[4]	struct host_load_info	
[5]	{	
[6]	long	avenrun[3];
[7]	long	mach_factor[3];
[8]	};	
[9]	typedef struct host_load_info	<pre>host_load_info_data_t;</pre>
[10]	typedef struct host_load_info*	<pre>host_load_info_t;</pre>

#### DESCRIPTION

The **host\_load\_info** structure defines the loading information available about a host. The information returned is exponential averages over three periods of time: 5, 30 and 60 seconds.

## FIELDS

#### avenrun

load average—average number of runnable processes divided by number of CPUs

### mach\_factor

The processing resources available to a new thread—the number of CPUs divided by (1 + the number of threads)

## **RELATED INFORMATION**

Functions: host\_info.

Data structures: host\_basic\_info, host\_sched\_info.

# host\_sched\_info

Structure — Defines scheduling information about a host

## SYNOPSIS

- [1] struct host\_sched\_info
- [2] {
- [3] int
- [4] int
- [5] };
- [6] typedef struct host\_sched\_info[7] typedef struct host\_sched\_info\*

host\_sched\_info\_data\_t; host\_sched\_info\_t;

min\_timeout;

min\_quantum;

# DESCRIPTION

The **host\_sched\_info** structure defines the limiting scheduling information available about a host.

## FIELDS

min\_timeout

Minimum time-out, in milliseconds

min\_quantum

Minimum quantum (period for which a thread can be scheduled if uninterrupted), in milliseconds

## **RELATED INFORMATION**

Functions: host\_info.

Data structures: host\_basic\_info, host\_load\_info.

# mach\_access\_vector

**Structure** — Defines the mach access vector which defines the privileges supported by the Mach kernel.

## SYNOPSIS

[1]	struct mach_access_vector	
[2]	{	
[3]	/* permissions */	
[4]	unsigned char	av_can_receive: 1,
[5]		av_can_send: 1,
[6]		av_hold_receive: 1,
[7]		av_hold_send: 1,
[8]		av_hold_send_once: 1,
[9]		av_interpose: 1,
[10]		av_specify: 1,
[11]		av_transfer_receive: 1;
[12]	unsigned char	av_transfer_rights: 1,
[13]		av_transfer_send: 1,
[14]		<pre>av_transfer_send_once: 1;</pre>
[15]		av_transfer_ool: 1,
[16]		<pre>mosv_map_vm_region: 1;</pre>
[17]		av_set_reply: 1;
[18]		av_unused: 2;
[19]	/* allowed operations */	
[20]	union mach_services	av_service;
[21]	};	
[22]	typedef struct mach_access_vector	<pre>mach_access_vector_data_t;</pre>
[23]	typedef struct mach_access_vector*	<pre>mach_access_vector_t;</pre>

### DESCRIPTION

The **mach\_access\_vector** structure defines the permissions that one security identifier has to another security identifier. The Mach kernel IPC processing is responsible for the enforcement of the permissions upon each attempted use of a port right. In addition the Mach kernel service processing is responsible for the enforcement of the services portion of an access vector before any service is rendered. The general structure provides for 16 permissions and 48 operations. In total it takes two 32 bit words.

# **FIELDS**

```
av_can_receive
```

Indicates that the task has receive permission to the associated port right.

mosv_m	ap_vm_regionControlsdefault_pager_object_create,vm_allocate,
av_trans	<i>fer_ool</i> Indicates that the task has permission to transfer out-of-line data in a message to the target port.
av_trans	<i>fer_send_once</i> Indicates that the task has permission to transfer a <b>SEND ONCE</b> right.
av_trans	<i>fer_send</i> Indicates that the task has permission to transfer a <b>SEND</b> right.
av_trans	<i>fer_receive</i> Indicates that the task has permission to transfer a <b>RECEIVE</b> right.
av_speci	<i>ify</i> Indicates that the task has permission to specify which security identity is to be associated with a message.
av_inter	<i>pose</i> Indicates that the task has permission to receive messages that were to be received by another security identifier.
av_hold_	_send_once Indicates that the task has permission to hold a <b>SEND ONCE</b> right.
av_hold_	_ <i>send</i> Indicates that the task has permission to hold a <b>SEND</b> right.
av_hold_	<i>_receive</i> Indicates that the task has permission to hold a <b>RECEIVE</b> right.
av_can_	<i>send</i> Indicates that the task has permission to send on the associated port right.

vm\_allocate\_secure, vm\_map.

### av\_service

Defines the services that security policy allows the message receiver to do for the message's sender. The kernel interprets this portion of the access vector if and if only the kernel is the receiver of the message.

## NOTES

The contents of an access vector are computed by the Security Server in agreement with a specific security policy and provided to the kernel via interaction with the Security Server. The kernel may cache the access vectors to increase performance. The kernel provides entries to ensure that the cached vectors may be invalidated.

Functions: mach\_msg\_secure.

Data Structures: **mach\_services\_t**.

# mach\_device\_services

**Structure** — Defines the services that a task is allowed to request of a device on a kernel device port.

# SYNOPSIS

[1]	struct mach_device_services	
[2]	{	
[3]	unsigned char	dsv_close_device: 1,
[4]		dsv_get_device_status: 1,
[5]		dsv_map_device: 1,
[6]		dsv_open_device: 1,
[7]		dsv_read_device: 1,
[8]		dsv_set_device_filter: 1,
[9]		dsv_set_device_status: 1,
[10]		dsv_write_device:1;
[11]	unsigned char	dsv_pager_ctrl: 1,
[12]		dsv_pad: 7
[13]	};	
[14]	typedef struct mach_device_servi	ces mach_device_services_data_t;
[15]	typedef struct mach_device_serve	ces* mach_device_services_t;

### **DESCRIPTION**

The **mach\_device\_services** structure defines the services that a requesting task is allowed to make to a kernel device port.

# **SECURITY**

The system security policy specifies the criteria for setting the fields in this vector. The kernel enforces the allowed operations on each device directed kernel request.

## **FIELDS**

A **TRUE** value in a specific field indicates that requesting task is allowed to make the request identified in the field.

### **RELATED INFORMATION**

Functions: See list above.

Data Structures: mach\_access\_vector\_t, mach\_services\_t.

# mach\_generic\_services

**Structure** — General data structure to set the maximum size of an allowed operations vector.

## SYNOPSIS

[1]	struct mach_generic_services	
[2]	{	
[3]	unsigned char	ago_first_8_bits;
[4]	unsigned char	ago_second_8_bits;
[5]	unsigned char	ago_thrid_8_bits;
[6]	unsigned char	ago_forth_8_bits;
[7]	unsigned char	ago_fifth_8_bits;
[8]	unsigned char	ago_sixth_8_bits;
[9]	};	
[10]	typedef struct mach_generic_services	mach_generic_services_data_t;
[11]	<pre>typedef struct mach_generic_services*</pre>	<pre>mach_generic_services_t;</pre>

## DESCRIPTION

The **mach\_generic\_services** structure established the maximum size of the service vectors. This must be taken into consideration when defining the security database for any system built on the DTOS kernel.

# SECURITY

Not Applicable.

## **FIELDS**

The fields of instances of allowed operations vectors are specified by the system security policy.

### **RELATED INFORMATION**

Functions:

Data Structures: mach\_access\_vector\_t, and mach\_services\_t.

# mach\_kernel\_reply\_port\_services

Structure — Defines the services that a task is allowed to request of a kernel host server on a kernel host privilege port.

## **SYNOPSIS**

[1] struct mach\_kernel\_reply\_port\_services

unsigned char

[2] {

[3]

krpsv\_provide\_permission: 1,

[4] [5] }; krpsv pad: 7;

- [6] typedef struct mach\_host\_priv\_services
  - mach\_kernel\_reply\_port\_services;
- [7] typedef struct mach\_host\_priv\_services\*

mach\_kernel\_reply\_port\_services\_t;

## DESCRIPTION

The mach\_kernel\_reply\_port\_services structure defines the services that a requesting task is allowed to make on a kernel reply\_port.

## **SECURITY**

The system security policy specifies the criteria for setting the fields in this vector. The kernel enforces the allowed operations on each reply port that it provides to an external server as a result of a kernel outcall request. The following list indicates which kernel entries are controlled by each service bit.

krpsv\_provide\_permission

Controls reply to sec\_access\_provided service request

### **FIELDS**

A **TRUE** value in a specific field indicates that the requesting task is allowed to make the request identified in the field.

### **RELATED INFORMATION**

Functions: See list above.

Data Structures: mach\_access\_vector\_t and mach\_services\_t.

# mach\_host\_priv\_services

**Structure** — Defines the services that a task is allowed to request of a kernel host server on a kernel host privilege port.

# SYNOPSIS

[1]	struct mach_host_priv_services	
[2]	{	
[3]	unsigned char	hpsv_get_boot_info: 1,
[4]		hpsv_get_host_processors: 1,
[5]		hpsv_pset_ctrl_port: 1,
[6]	hpsv_reboot_host: 1,	
[7]	hpsv_set_default_memory_mgr: 1,	
[8]	hpsv_set_time: 1;	
[9]	hpsv_wire_thread: 1,	
[10]	hpsv_wire_vm: 1;	
[11]	};	
[12]	<pre>typedef struct mach_host_priv_ser</pre>	vices mach_host_priv_services_data_t;
[13]	<pre>typedef struct mach_host_priv_ser</pre>	vices* mach_host_priv_services_t;

# DESCRIPTION

The **mach\_host\_priv\_services** structure defines the services that a requesting task is allowed to make on a kernel host control port.

### SECURITY

The system security policy specifies the criteria for setting the fields in this vector. The kernel enforces the allowed operations on each host privileged port directed kernel request.

### **FIELDS**

A **TRUE** value in a specific field indicates that the requesting task is allowed to make the request identified in the field.

# **RELATED INFORMATION**

Functions: See list above.

Data Structures: mach\_access\_vector\_t and mach\_services\_t.

# mach\_host\_services

**Structure** — Defines the services that a task is allowed to request of a kernel host server on a kernel host name port.

## SYNOPSIS

[1]	struct mach_host_services	
[2]	{	
[3]	unsigned char	hsv_create_pset: 1,
[4]		hsv_flush_permission: 1,
[5]		hsv_get_default_pset_name: 1,
[6]		hsv_get_host_info: 1,
[7]		hsv_get_host_name: 1,
[8]		hsv_get_host_version: 1,
[9]		hsv_get_time: 1,
[10]		hsv_pset_names: 1,
[11]	unsigned char	hsv_get_audit_port: 1,
[12]		hsv_get_security_client_port: 1,
[13]		hsv_get_security_master_port: 1,
[14]		hsv_get_special_port: 1,
[15]		hsv_set_audit_port: 1,
[16]		hsv_set_security_client_port: 1,
[17]		hsv_set_security_master_port: 1,
[18]		hsv_set_special_port: 1,
[19]	unsigned char	hsv_get_crypto_port: 1,
[20]		hsv_get_host_control_port: 1,
[21]		hsv_get_negotiation_port: 1,
[22]		hsv_set_crypto_port: 1,
[23]		hsv_set_negotiation_port: 1,
[24]		hsv_get_authentication_port: 1,
[25]		hsv_set_authentication_port: 1,
[26]		hsv_get_network_ss_port:1;
[27]	unsigned char	hsv_set_network_ss_port:1,
[28]		hsv_pad:7;
[29]	};	
[30]	typedef struct mach_host_services	<pre>mach_host_services_data_t;</pre>
[31]	typedef struct mach_host_services*	mach_host_services_t;

# DESCRIPTION

The **mach\_host\_services** structure defines the services that a requesting task is allowed to make on a kernel host name port.

## SECURITY

The system security policy specifies the criteria for setting the fields in this vector. The kernel enforces the allowed operations on each host name port directed kernel request.

# **FIELDS**

A **TRUE** value in a specific field indicates that the requesting task is allowed to make the request identified in the field.

# **RELATED INFORMATION**

Functions: See list above.

 $Data \ Structures: mach\_access\_vector\_t \ and \ mach\_services\_t.$ 

# mach\_mem\_obj\_services

**Structure** — Defines the services that a task is allowed to request of a kernel host server on memory object ports.

# SYNOPSIS

[1]	struct mach_mem_obj_services		
[2]	{		
[3]	unsigned char	mosv_h	ave_execute: 1,
[4]		mosv_h	ave_read: 1,
[5]		mosv_h	ave_write: 1,
[6]		mosv_u	nused1: 1,
[7]		mosv_p	age_vm_region: 1,
[8]		mosv_p	ad: 3;
[9]	};		
[10]	typedef struct mach_mem_obj_ser	vices	mach_mem_obj_services_data_t;
[11]	typedef struct mach_mem_obj_ser	vices*	mach_mem_obj_services_t;

# DESCRIPTION

The **mach\_mem\_obj\_services** structure defines the services that a requesting task is allowed to make to a kernel processor port.

### SECURITY

The system security policy specifies the criteria for setting the fields in this vector. The kernel enforces the allowed operations on each memory object directed kernel request.

# FIELDS

A **TRUE** value in a specific field indicates that the requesting task is allowed to make the request identified in the field.

### **RELATED INFORMATION**

Functions: See list above.

Data Structures: mach\_access\_vector\_t, mach\_services\_t.

# mach\_mem\_ctrl\_services

**Structure** — Defines the services that a task is allowed to request on a kernel host server on a memory control port.

# SYNOPSIS

[1]	struct mach_mem_ctrl_services	
[2]	{	
[3]	unsigned char	mcsv_change_page_locks: 1
[4]		mcsv_destroy_object: 1,
[5]		mcsv_get_attributes: 1,
[6]		mcsv_invoke_lock_request: 1,
[7]		mcsv_make_page_precious: 1,
[8]		mcsv_provide_data: 1,
[9]		mcsv_remove_page: 1,
[10]		mcsv_revoke_ibac: 1;
[11]	unsigned char	mcsv_save_page: 1,
[12]		mcsv_set_attributes: 1,
[13]		mcsv_set_ibac_port: 1,
[14]		mcsv_supply_ibac: 1,
[15]		osv_pad: 4;
[16]	};	
[17]	typedef struct mach_mem_ctrl_ser	vices mach_mem_ctrl_services_data_t;
[18]	typedef struct mach_mem_ctrl_ser	vices* mach_mem_ctrl_services_t;

### **DESCRIPTION**

The **mach\_mem\_ctrl\_services** structure defines the services that a requesting task is allowed to make to a kernel memory control port.

# SECURITY

The system security policy specifies the criteria for setting the fields in this vector. The kernel enforces the allowed operations on memory control port directed kernel request.

### FIELDS

A **TRUE** value in a specific field indicates that the requesting task is allowed to make the request identified in the field.

## **RELATED INFORMATION**

Functions: See list above

Data Structures: mach\_access\_vector\_t, mach\_services\_t.

**Data Structures** 

# mach\_msg\_header

Structure — Defines the header portion for messages

### **SYNOPSIS**

[1]	typedef struct	
[2]	{	
[3]	mach_msg_bits_t	msgh_bits;
[4]	mach_msg_size_t	msgh_size;
[5]	mach_port_t	msgh_remote_port;
[6]	mach_port_t	msgh_local_port;
[7]	mach_port_seqno_t	msgh_seqno;
[8]	mach_msg_id_t	msgh_id;
[9]	} mach_msg_header_t;	

## DESCRIPTION

A Mach message consists of a fixed size message header, a **mach\_msg\_header\_t**, followed by zero or more data items. Data items are typed. Each item has a type descriptor followed by the actual data (or an address of the data, for out-of-line memory regions).

There are two forms of type descriptors, a **mach\_msg\_type\_t** and a **mach\_msg\_type\_long\_t**. The **mach\_msg\_type\_long\_t** type descriptor allows larger values for these fields. The *msgtl\_header* field in the long descriptor is only used for its in-line, long-form, and de-allocate bits.

### FIELDS

msgh\_bits

This field specifies the following properties of the message:

### MACH\_MSGH\_BITS\_REMOTE\_MASK

Encodes **mach\_msg\_type\_name\_t** values that specify the port rights in the *msgh\_remote\_port* field. The value must specify a send or send-once right for the destination of the message.

### MACH\_MSGH\_BITS\_LOCAL\_MASK

Encodes **mach\_msg\_type\_name\_t** values that specify the port rights in the *msgh\_local\_port* field. If the value doesn't specify a send or send-once right for the message's reply port, it must be zero and *msgh\_local\_port* must be MACH\_PORT\_NULL.

### MACH\_MSGH\_BITS\_COMPLEX

The complex bit must be specified if the message body contains port rights or out-of-line memory regions. If it is not specified, then the message body carries no port rights or memory, no matter what the type descriptors may seem to indicate.

### MACH\_MSGH\_BITS\_REMOTE(bits)

This macro returns the appropriate **mach\_msg\_type\_name\_t** values, given a *msgh\_bits* value.

#### MACH\_MSGH\_BITS\_LOCAL(bits)

This macro returns the appropriate **mach\_msg\_type\_name\_t** values, given a *msgh\_bits* value.

#### MACH\_MSGH\_BITS (remote, local)

This macro constructs a value for *msgh\_bits*, given two **mach\_msg\_type\_name\_t** values.

#### msgh\_size

In the header of a received message, this field contains the message's size. The message size, a byte quantity, includes the message header, type descriptors, and in-line data. For out-of-line memory regions, the message size includes the size of the in-line address, not the size of the actual data region. There are no arbitrary limits on the size of a Mach message, the number of data items in a message, or the size of the data items.

#### msgh\_remote\_port

When sending, specifies the destination port of the message. The field must carry a legitimate send or send-once right for a port. When received, this field is swapped with *msgh\_local\_port*.

#### msgh\_local\_port

When sending, specifies an auxiliary port right, which is conventionally used as a reply port by the recipient of the message. The field must carry a send right, a send-once right, MACH\_PORT\_NULL, or MACH\_PORT\_DEAD. When received, this field is swapped with *msgh\_remote\_port*.

#### msgh\_seqno

The sequence number of this message relative to the port from which it is received. This field is ignored on sent messages.

#### msgh\_id

Not set or read by the **mach\_msg** call. The conventional meaning is to convey an operation or function id.

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# NOTES

Simple messages are provided to handle in-line data. The sender copies the inline data into the message structure, and the receiver usually copies it out.

Non-simple messages are provided to handle out-of-line data. Out-of-line data allows for the sending of port information or data blocks that are very large or of variable size. The kernel maps out-of-line data from the address space of the sender to the address space of the receiver. The kernel copies the data only if the sender or receiver subsequently modifies it. This is an example of copy-on-write data sharing.

### **RELATED INFORMATION**

Functions: mach\_msg, mach\_msg\_receive, mach\_msg\_send.

Data Structures: mach\_msg\_type, mach\_msg\_type\_long.

# mach\_msg\_type

Structure — Defines the data descriptor for long data items in messages

# SYNOPSIS

[1]	typedef struct	
[2]	{	
[3]	unsigned int	msgt_name: 8,
[4]		msgt_size: 8,
[5]		msgt_number: 12,
[6]		msgt_inline: 1,
[7]		msgt_longform: 1,
[8]		msgt_deallocate: 1,
[9]		msgt_unused: 1;
[10]	<pre>} mach_msg_type_t;</pre>	

## DESCRIPTION

Each data item in a MACH IPC message has a type descriptor, a **mach\_msg\_type\_t** or a **mach\_msg\_type\_long\_t**. The **mach\_msg\_type\_long\_t** type descriptor allows larger values for these fields.

## FIELDS

msgt\_name

Specifies the data's type. The following types are predefined:

MACH\_MSG\_TYPE\_UNSTRUCTURED un-interpreted data (32 bits)

MACH\_MSG\_TYPE\_BIT single bit

MACH\_MSG\_TYPE\_BOOLEAN boolean value (32 bits)

MACH\_MSG\_TYPE\_INTEGER\_16 16 bit integer

MACH\_MSG\_TYPE\_INTEGER\_32 32 bit integer

MACH\_MSG\_TYPE\_CHAR single character

MACH\_MSG\_TYPE\_BYTE 8-bit byte MACH\_MSG\_TYPE\_INTEGER\_8 8-bit integer

MACH\_MSG\_TYPE\_REAL floating value (32 bits)

MACH\_MSG\_TYPE\_STRING null terminated

- MACH\_MSG\_TYPE\_STRING\_C null terminated
- MACH\_MSG\_TYPE\_PORT\_NAME type of **mach\_port\_t**. This is the type of the name for a port, not the type to specify if a port right is to be specified.
- MACH\_MSG\_TYPE\_MOVE\_RECEIVE move the name receive right
- MACH\_MSG\_TYPE\_MOVE\_SEND move the named send right
- MACH\_MSG\_TYPE\_MOVE\_SEND\_ONCE move the named send-once right
- MACH\_MSG\_TYPE\_COPY\_SEND make a copy of the named send right
- MACH\_MSG\_TYPE\_MAKE\_SEND make a send right from the named receive right
- MACH\_MSG\_TYPE\_MAKE\_SEND\_ONCE make a send-once right from the named send or receive right

The last six types specify port rights, and receive special treatment. The type MACH\_MSG\_TYPE\_PORT\_NAME describes port right names, when no rights are being transferred, but just names. For this purpose, it should be used in preference to MACH\_MSG\_TYPE\_INTEGER\_32.

msgt\_size

Specifies the size of each datum, in bits. For example, the *msgt\_size* of MACH\_MSG\_TYPE\_INTEGER\_32 data is 32.

msgt\_number

Specifies how many data elements comprise the data item. Zero is a legitimate number. The total length specified by a type descriptor is (*msgt\_size* \* *msgt\_number*), rounded up to an integral number of bytes. In-line data is then padded to an integral number of long-words. This ensures that type descriptors always start on long-word boundaries. It implies that message sizes are always an integral multiple of a long-word's size.

#### msgt\_inline

When FALSE, specifies that the data actually resides in an out-of-line region. The address of the data region follows the type descriptor in the message body. The *msgt\_name*, *msgt\_size*, and *msgt\_number* fields describe the data region, not the address.

#### msgt\_longform

Specifies, when TRUE, that this type descriptor is a **mach\_msg\_type\_long\_t** instead of a **mach\_msg\_type\_t**.

#### msgt\_deallocate

Used with out-of-line regions. When TRUE, it specifies the data region should be de-allocated from the sender's address space (as if with **vm\_deallocate**) when the message is sent.

#### msgt\_unused

Not used, should be zero.

### **RELATED INFORMATION**

Functions: mach\_msg, mach\_msg\_receive, mach\_msg\_send.

Data Structures: mach\_msg\_header, mach\_msg\_type\_long.

# mach\_msg\_type\_long

Structure — Defines the data descriptor for long data items in messages

### **SYNOPSIS**

[1]	typedef struct	
[2]	{	
[3]	mach_msg_type_t	msgtl_header;
[4]	unsigned short	msgtl_name;
[5]	unsigned short	msgtl_size;
[6]	unsigned int	msgtl_number;
[7]	} mach_msg_type_long_t;	

### DESCRIPTION

Each data item has a type descriptor, a **mach\_msg\_type\_t** or a **mach\_msg\_type\_long\_t**. The **mach\_msg\_type\_long\_t** type descriptor allows larger values for these fields. The *msgtl\_header* field in the long descriptor is only used for its in-line, long-form, and de-allocate bits.

### FIELDS

msgtl\_header

A header in common with **mach\_msg\_type\_t**. When the *msgt\_longform* bit in the header is TRUE, this type descriptor is a **mach\_msg\_type\_long\_t** instead of a **mach\_msg\_type\_t**. The *msgt\_name*, *msgt\_size*, and *msgt\_number* fields should be zero. Instead, **mach\_msg** uses the following: *msgtl\_name*, *msgtl\_size*, and *msgtl\_number* fields.

#### msgtl\_name

Specifies the data's type. The defined values are the same as those for **mach\_msg\_type**.

#### msgtl\_size

Specifies the size of each datum, in bits. For example, the *msgtl\_size* of MACH\_MSG\_TYPE\_INTEGER\_32 data is 32.

#### msgtl\_number

Specifies how many data elements comprise the data item. Zero is a legitimate number. The total length specified by a type descriptor is  $(msgtl\_size * msgtl\_number)$ , rounded up to an integral number of bytes. In-line data is then padded to an integral number of long-words. This ensures that type descriptors always start on long-word boundaries. It implies that message sizes are always an integral multiple of a long-word's size.

## **RELATED INFORMATION**

Functions: mach\_msg, mach\_msg\_receive, mach\_msg\_send.

Data Structures: mach\_msg\_header, mach\_msg\_type.

# mach\_port\_status

**Structure** — Defines information for a port

# **SYNOPSIS**

[1]	struct mach_port_status	
[2]	{	
[3]	mach_port_t	mps_pset;
[4]	mach_port_seqno_t	mps_seqno;
[5]	mach_port_mscount_t	mps_mscount;
[6]	mach_port_msgcount_t	mps_qlimit;
[7]	mach_port_msgcount_t	mps_msgcount;
[8]	mach_port_rights_t	mps_sorights;
[9]	boolean_t	mps_srights;
[10]	boolean_t	mps_pdrequest;
[11]	boolean_t	mps_nsrequest;
[12]	};	
[13]	typedef struct mach_port_status	<pre>mach_port_status_t;</pre>

# **DESCRIPTION**

The **mach\_port\_status** structure defines information about a port.

# **FIELDS**

mps\_pset

Containing port set

mps\_seqno

Current sequence number for the port.

mps\_mscount

Make-send count

#### mps\_qlimit

Queue limit

### mps\_msgcount

Number in the queue

## mps\_sorights

How many send-once rights

### mps\_srights

True if send rights exist

*mps\_pdrequest* True if there is a port-deleted requested

mps\_nsrequest

True if no-senders requested

# **RELATED INFORMATION**

Functions: mach\_port\_get\_receive\_status.

# mach\_proc\_services

**Structure** — Defines the services that a task is allowed to request of a kernel host server on a kernel processor port.

# SYNOPSIS

[1]	struct mach_proc_services	
[2]	{	
[3]	unsigned char	<pre>psv_assign_processor_to_set: 1,</pre>
[4]		<pre>psv_get_processor_assignment: 1,</pre>
[5]		<pre>psv_get_processor_info: 1,</pre>
[6]		<pre>psv_may_control_processor:1,</pre>
[7]		psv_pad: 4;
[8]	};	
[9]	<pre>typedef struct mach_proc_services</pre>	<pre>mach_proc_services_data_t;</pre>
[10]	typedef struct mach_proc_services'	<pre>mach_proc_services_t;</pre>

# DESCRIPTION

The **mach\_proc\_services** structure defines the services that a requesting task is allowed to make to a kernel processor self port.

# SECURITY

The system security policy specifies the criteria for setting the fields in this vector. The kernel enforces the allowed operations on each processor port directed kernel request.

## **FIELDS**

A **TRUE** value in a specific field indicates that the requesting task is allowed to make the request identified in the field.

### **RELATED INFORMATION**

Functions: See list above.

Data Structures: mach\_access\_vector\_t, and mach\_services\_t.

# mach\_proc\_set\_services

**Structure** — Defines the services that a task is allowed to request of a kernel host server on a kernel processor set port.

# SYNOPSIS

[1]	struct mach_proc_set_services	
[2]	{	
[3]	unsigned char	pssv_assign_processor: 1,
[4]		pssv_assign_task: 1,
[5]		pssv_assign_thread: 1,
[6]		pssv_chg_pset_max_pri: 1,
[7]		<pre>pssv_define_new_scheduling_policy: 1,</pre>
[8]		pssv_destroy_pset: 1,
[9]	pssv_get_pset_info: 1,	
[10]		pssv_invalidate_scheduling_policy: 1,
[11]		pssv_observe_pset_processes: 1,
[12]		psv_pad: 5;
[13]	};	
[14]	<pre>typedef struct mach_proc_set_ser</pre>	vices mach_proc_set_services_data_t;
[15]	typedef struct mach_proc_set_ser	vices* mach_proc_set_services_t;

### DESCRIPTION

The **mach\_proc\_set\_services** structure defines the services that a requesting task is allowed to make to a kernel processor set port.

### SECURITY

The system security policy specifies the criteria for setting the fields in this vector. The kernel enforces the allowed operations on each processor set directed kernel request.

### **FIELDS**

A **TRUE** value in a specific field indicates that the requesting task is allowed to make the request identified in the field.

## **RELATED INFORMATION**

Functions: See list above.

Data Structures: mach\_access\_vector\_t and mach\_services\_t.

# mach\_services

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**Structure** — Defines service vectors that control the services that task is allowed to request on kernel ports.

## SYNOPSIS

....

[1]	union mach_services	
[2]	{	
[3]	mach_device_services_data_t	dev_sv;
[4]	mach_host_priv_services_data_t	host_priv_sv;
[5]	mach_host_services_data_t	host_sv;
[6]	mach_mem_obj_services_data_t	mem_obj_sv;
[7]	mach_mem_ctrl_services_data_t	mem_ctrl_sv;
[8]	mach_proc_services_data_t	proc_sv;
[9]	mach_proc_set_services_data_t	proc_set_sv;
[10]	mach_task_services_data_t	task_sv;
[11]	mach_thread_services_data_t	thread_sv;
[12]		
[13]	mach_generic_services_data_t	gen_sv;
[14]	};	
[15]		
[16]	typedef union mach_services	mach_services_data_t;
[17]	typedef union mach_services	*mach_services_t;

# DESCRIPTION

The **mach\_services** union defines the classes of services that the kernel will enforce as well as the general operation vector which may be used by non kernel system servers. The interpretation of the fields of this vector are specified by the system security policy and enforced by the receiver of the associated port.

# SECURITY

The service field in the access vector allows the system security policy to specify which services a specific task may make to a particular port. It is the responsibility of a port's receiver to enforce the information provided in the allowed operations portion of an access vector. This provides two levels of control over operations. First it is possible to deny a task permission to send a message to a port, and second it is possible to control which services will be allowed.

# **FIELDS**

dev\_sv

Bit vector indicating which device port directed service requests the requesting task is allowed to make.

#### host\_priv\_sv

Bit vector indicating which host priv port directed service requests the requesting task is allowed to make.

#### host\_sv

Bit vector indicating which host port directed service requests the requesting task is allowed to make.

#### mem\_obj\_sv

Bit vector indicating which object port directed service requests the requesting task is allowed to make.

#### mem\_ctrl\_sv

Bit vector indicating which memory control port directed service requests the requesting task is allowed to make.

#### proc\_sv

Bit vector indicating which processor port directed service requests the requesting task is allowed to make.

#### proc\_set\_sv

Bit vector indicating which processor set port directed service requests the requesting task is allowed to make.

#### task\_sv

Bit vector indicating which task port directed service requests the requesting task is allowed to make.

#### thread\_sv

Bit vector indicating which thread port directed service requests the requesting task is allowed to make.

#### gen\_sv

A port specific bit vector indicating which services the requesting task is allowed to make to the port. The purpose of this field is to determine the maximum number of bits in a service vector.

### **RELATED INFORTION**

### Functions: None.

Data Structures: mach\_device\_services, mach\_host\_priv\_services, mach\_host\_services, mach\_mem\_obj\_services, mach\_mem\_ctrl\_services, mach\_proc\_services, mach\_proc\_set\_services, mach\_task\_services and mach\_thread\_services.

# mach\_task\_services

**Structure** — Defines the services that a task is allowed to request of a kernel host server on a kernel task port.

# SYNOPSIS

[1]	struct mach_task_services	
[2]	{	
[3]	unsigned char	tsv_access_mach_nattribute: 1,
[4]		tsv_add_name: 1,
[5]		<i>tsv_add_thread</i> : 1,
[6]		<i>tsv_add_thread_secure</i> : 1,
[7]		<pre>tsv_allocate_vm_region: 1,</pre>
[8]		tsv_alter_pns_info: 1,
[9]		tsv_assign_task_to_pset: 1,
[10]		<pre>tsv_chg_vm_region_prot: 1;</pre>
[11]	unsigned char	tsv_chg_task_priority: 1,
[12]		tsv_copy_vm: 1,
[13]		tsv_create_task: 1,
[14]		<pre>tsv_create_task_secure: 1,</pre>
[15]		<pre>tsv_deallocate_vm_region: 1,</pre>
[16]		tsv_extract_right: 1,
[17]		tsv_get_emulation: 1,
[18]		<pre>tsv_get_task_assignment: 1;</pre>
[19]	unsigned char	tsv_get_task_boot_port: 1,
[20]		<pre>tsv_get_task_exception_port: 1,</pre>
[21]		tsv_get_task_info: 1,
[22]		tsv_get_task_kernel_port: 1,
[23]		tsv_get_task_threads: 1,
[24]		<pre>tsv_get_vm_region_info: 1,</pre>
[25]		tsv_get_vm_statistics: 1,
[26]		tsv_lookup_ports: 1;
[27]	unsigned char	<pre>tsv_manipulate_port_set: 1,</pre>
[28]		tsv_observe_pns_info: 1,
[29]		tsv_port_rename: 1,
[30]		<pre>tsv_read_vm_region: 1,</pre>
[31]		tsv_register_notification: 1,
[32]		tsv_register_ports: 1,
[33]		tsv_remove_name: 1,
[34]		tsv_resume_task: 1;
[35]	unsigned char	tsv_sample_task: 1,
[36]		tsv_set_emulation: 1,
[37]		<pre>tsv_set_vm_region_inherit: 1,</pre>
[38]		tsv_set_ras: 1,
[39]		tsv_set_task_boot_port: 1,
[40]		<pre>tsv_set_task_exception_port: 1,</pre>
[41]		tsv_set_task_kernel_port: 1,
[42]		tsv_suspend_task: 1;

[43]	unsigned char	tsv_terminate_task: 1,
[44]		tsv_wire_vm_for_task: 1,
[45]		tsv_write_vm_region: 1,
[46]		<pre>tsv_cross_context_create: 1,</pre>
[47]		<pre>tsv_cross_context_inherit: 1,</pre>
[48]		tsv_chg_sid: 1,
[49]		tsv_make_sid: 1,
[50]		tsv_transition_sid: 1,
[51]	};	
[52]	<pre>typedef struct mach_task_services</pre>	mach_task_services_data_t;
[53]	typedef struct mach_task_services*	<pre>mach_task_services_t;</pre>

### **DESCRIPTION**

The **mach\_task\_services** structure defines the services that a requesting task is allowed to make on a kernel task port.

## SECURITY

The system security policy specifies the criteria for setting the fields in this vector. The kernel enforces the allowed operations on each task directed kernel request.

### **FIELDS**

A **TRUE** value in a specific field indicates that the requesting task is allowed to make the request identified in the field.

## **RELATED INFORMATION**

Functions: See list above.

Data Structures: mach\_access\_vector\_t and mach\_services\_t.

# mach\_thread\_services

**Structure** — Defines the services that a task is allowed to request of a kernel host server on a kernel thread port.

# SYNOPSIS

[1]	struct mach_thread_services		
[2]	{		
[3]	unsigned char	thsv_abort_thread: 1,	
[4]		thsv_abort_thread_depress: 1,	
[5]		thsv_assign_thread_to_pset: 1,	
[6]		thsv_can_swtch: 1,	
[7]		thsv_can_swtch_pri: 1,	
[8]		thsv_depress_pri: 1,	
[9]		thsv_get_thread_assignment: 1,	
[10]		thsv_get_thread_exception_port: 1;	
[11]	unsigned char	thsv_get_thread_info: 1,	
[12]		thsv_get_thread_kernel_port: 1,	
[13]		thsv_get_thread_state:1,	
[14]		thsv_initate_secure:1,	
[15]		thsv_raise_exception: 1,	
[16]		thsv_resume_thread: 1,	
[17]		thsv_sample_thread: 1,	
[18]		thsv_set_max_thread_prioity: 1;	
[19]	unsigned char	thsv_set_thread_exception_port: 1,	
[20]		thsv_set_thread_kernel_port: 1,	
[21]		thsv_set_thread_policy: 1,	
[22]		thsv_set_thread_priority: 1,	
[23]		thsv_set_thread_state: 1,	
[24]		thsv_suspend_thread: 1,	
[25]		thsv_switch_thread: 1,	
[26]		thsv_terminate_thread: 1;	
[27]	unsigned char	thsv_wait_evc: 1;	
[28]		<pre>thsv_wire_thread_into_memory: 1;</pre>	
[29]		thsv_pad 6;	
[30]	};		
[31]	typedef struct mach_thread_serve		
[32]	typedef struct mach_thread_served	ces* mach_thread_services_t;	

# DESCRIPTION

The **mach\_thread\_services** structure defines the services that a requesting task is allowed to make on a kernel thread port.

## SECURITY

The system security policy specifies the criteria for setting the fields in this vector. The kernel enforces the allowed operations on each thread port directed kernel request.

## **FIELDS**

A **TRUE** value in a specific field indicates that the requesting task is allowed to make the request identified in the field.

# **RELATED INFORMATION**

Functions: See list above.

Data Structures: mach\_access\_vector\_t and mach\_services\_t.

# mapped\_time\_value

**Structure** — Defines format of kernel maintained time in the mapped clock device

## SYNOPSIS

[1]	struct mapped_time_value	
[2]	{	
[3]	long	seconds;
[4]	long	microseconds;
[5]	long	check_seconds;
[6]	};	
[7]	typedef struct mapped_time_value	mapped_time_value_t;

# DESCRIPTION

The **mapped\_time\_value** structure defines the format of the current-time structure maintained by the kernel and visible by mapping (**device\_map**) the "time" pseudo-device. The data in this structure is updated at every clock interrupt. It contains the same value that would be returned by **host\_get\_time**.

## FIELDS

seconds

Seconds since system initialization

microseconds

Microseconds in the current second

check\_seconds

A field used to synchronize with the kernel's setting of the time.

### NOTES

Because of the race between the referencing of these multiple fields and the kernel's setting them, they should be referenced as follows:

```
[1] do
```

- [2] {
- [3]  $secs = mtime \rightarrow seconds;$

[4]  $usecs = mtime \rightarrow microseconds;$ 

[5] } while (secs!= mtime  $\rightarrow$  check\_seconds);

### **RELATED INFORMATION**

Functions: device\_map, host\_adjust\_time, host\_get\_time, host\_set\_time.

# processor\_basic\_info

Structure — Defines the basic information about a processor.

## SYNOPSIS

[1]	struct processor_basic_info	
[2]	{	
[3]	cpu_type_t	cpu_type;
[4]	cpu_subtype_t	cpu_subtype;
[5]	boolean_t	running;
[6]	int	slot_num;
[7]	boolean_t	is_master;
[8]	};	
[9]	typedef struct processor_basic_info*	processor_basic_info_t;

# DESCRIPTION

The **processor\_basic\_info** structure defines the information available about a processor slot.

# FIELDS

cpu\_type

Type of CPU

*cpu\_subtype* Sub-type of CPU

running

True if the CPU is running

slot\_num

Slot number of the CPU

is\_master

True if this is the master processor

### **RELATED INFORMATION**

Functions: processor\_info.

# processor\_set\_basic\_info

Structure — Defines the basic information about a processor set.

### **SYNOPSIS**

[1]	struct processor_set_basic_info	
[2]	{	
[3]	int	processor_count;
[4]	int	task_count;
[5]	int	thread_count;
[6]	int	load_average;
[7]	int	mach_factor;
[8]	};	
[9]	typedef struct processor_set_basic_info*	<pre>processor_set_basic_info_t;</pre>

## **DESCRIPTION**

The **processor\_set\_basic\_info** structure defines the basic information available about a processor set.

# FIELDS

processor\_count Number of processors in this set

#### task\_count

Number of tasks currently assigned to this processor set

### thread\_count

Number of threads currently assigned to this processor set

#### load\_average

Average number of runnable processes divided by number of CPU

#### mach\_factor

The processing resources available to a new thread — the number of CPUs divided by (1 + the number of threads)

# **RELATED INFORMATION**

Functions: processor\_set\_info.

Data Structures: processor\_set\_sched\_info.

# processor\_set\_sched\_info

Structure — Defines the scheduling information about a processor set.

# **SYNOPSIS**

- [1] struct processor\_set\_sched\_info
- [2] {
  - int policies;
- [3] [4] int *max\_priority*;
  - [5] };
  - [6] typedef struct processor\_set\_sched\_info\* processor\_set\_sched\_info\_t;

# **DESCRIPTION**

The processor\_set\_sched\_info structure defines the global scheduling information available about a processor set.

# **FIELDS**

policies

Number of Allowed policies.

*max\_priority* 

Maximum scheduling priority for new threads.

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# **RELATED INFORMATION**

Functions: processor\_set\_info.

Data Structures: processor\_set\_basic\_info.

# sampled\_pc

Structure — Defines PC sampling information

# **SYNOPSIS**

[1]	struct sampled_pc	
[2]	{	
[3]	unsigned int	id;
[4]	vm_offset_t	<i>pc</i> ;
[5]	sampled_pc_flavor_t	sampletype;
[6]	};	
[7]	typedef struct sampled_pc	<pre>sampled_pc_t;</pre>
[8]	typedef struct sampled_pc*	<pre>sampled_pc_array_t;</pre>

## DESCRIPTION

The **sampled\_pc** structure defines the information provided by the pc sampling routines.

# **FIELDS**

id

The sampled thread id.

pc

The sampled pc value.

sampletype

The sample flavor.

# **RELATED INFORMATION**

Functions:task\_disable\_pc\_sampling,<br/>task\_get\_sampled\_pc,task\_enable\_pc\_sampling,<br/>thread\_disable\_pc\_sampling,<br/>thread\_enable\_pc\_sampling, thread\_get\_sampled\_pc.

Data Structures: sampled\_pc\_flavor\_t

# security\_id\_t

Structure — Defines the Security Identifier (SID) structure

# SYNOPSIS

[1]	struct <b>security_id</b>	
[2]	{	
[3]	mandatory_id_t	mid;
[4]	auth_id_t	aid;
[5]	};	
[6]	typedef struct security_id	<pre>security_id_t;</pre>

## DESCRIPTION

The **security\_id** structure defines the label that is associated with subjects and objects in the system.

## FIELDS

mid

The mandatory identifier (MID) is a 64-bit field. Its 10 most significant bits define the classifier and can be used by user applications to manage their own objects. The remaining 54 bits are reserved by the Security Server and no structure can be assumed about them.

aid

The authentication identifier (AID) is a 32-bit field.

# **RELATED INFORMATION**

Functions:	SSI_compute_access_vector,	SSI_context_to_mid,
SSI_short_contex	kt_to_mid,	SSI_mid_to_context,
SSI_mid_to_shor	t_context, task_change_sid, any :	function with _secure suf-
fix.		

# task\_basic\_info

Structure — Defines basic information for tasks

### SYNOPSIS

[1]	struct task_basic_info	
[2]	{	
[3]	int	suspend_count;
[4]	int	base_priority;
[5]	vm_size_t	virtual_size;
[6]	vm_size_t	resident_size;
[7]	time_value_t	user_time;
[8]	time_value_t	system_time;
[9]	};	
[10]	typedef struct task_basic_info*	task_basic_info_t;

#### DESCRIPTION

The **task\_basic\_info** structure defines the basic information array for tasks. The **task\_info** function returns this array for a specified task.

#### FIELDS

suspend\_count

The current suspend count for the task.

#### base\_priority

The base scheduling priority for the task.

#### virtual\_size

The number of virtual pages for the task.

#### resident\_size

The number of resident pages for the task

#### user\_time

The total user run time for terminated threads within the task.

#### system\_time

The total system run time for terminated threads within the task.

### **RELATED INFORMATION**

Functions: task\_info.

Data Structures: task\_thread\_times\_info.

# task\_basic\_secure\_info

Structure — Defines basic information including security information for tasks.

### SYNOPSIS

[1]	struct task_basic_secure_info	
[2]	{	
[3]	int	suspend_count;
[4]	int	base_priority;
[5]	vm_size_t	virtual_size;
[6]	vm_size_t	resident_size;
[7]	time_value_t	user_time;
[8]	time_value_t	system_time;
[9]	security_id_t	subj_sid;
[10]	};	
[11]	typedef struct task_basic_secure_info*	task_basic_secure_info_t;

#### DESCRIPTION

The **task\_basic\_secure\_info** structure defines the basic information array, including the task's *subj\_sid*, for a task. The **task\_info** function returns this array for a specified task.

### FIELDS

suspend\_count

The current suspend count for the task.

#### base\_priority

The base scheduling priority for the task.

#### virtual\_size

The number of virtual pages for the task.

#### resident\_size

The number of resident pages for the task

#### user\_time

The total user run time for terminated threads within the task.

#### system\_time

The total system run time for terminated threads within the task.

#### subj\_sid

The task's associated security identifier.

# **RELATED INFORMATION**

Functions: task\_info.

Data Structures: task\_thread\_times\_info.

# task\_thread\_times\_info

Structure — Defines thread execution times information for tasks

### SYNOPSIS

- [1] struct task\_thread\_times\_info
- [2] {
- [3] time\_value\_t
- [4] time\_value\_t
- [5] };
- [6] typedef struct task\_thread\_times\_info\*

task\_thread\_times\_info\_t;

user\_time;

system\_time;

### DESCRIPTION

The **task\_thread\_times\_info** structure defines thread execution time statistics for tasks. The **task\_info** function returns these times for a specified task. The **thread\_info** function returns this information for a specific thread.

### **FIELDS**

user\_time

Total user run time for live threads.

system\_time

Total system run time for live threads.

### **RELATED INFORMATION**

Functions: task\_info.

Data Structures: task\_basic\_info, thread\_info.

# thread\_basic\_info

Structure — Defines basic information for threads

#### **SYNOPSIS**

[1]	struct thread_basic_info	
[2]	{	
[3]	time_value_t	user_time;
[4]	time_value_t	system_time;
[5]	int	cpu_usage;
[6]	int	base_priority;
[7]	int	cur_priority;
[8]	int	run_state;
[9]	int	flags;
[10]	int	suspend_count;
[11]	long	sleep_time;
[12]	};	
[13]	typedef struct thread_basic_info*	thread_basic_info_t;

### DESCRIPTION

The **thread\_basic\_info** structure defines the basic information array for threads. The **thread\_info** function returns this array for a specified thread.

### **FIELDS**

user\_time

The total user run time for the thread.

#### system\_time

The total system run time for the thread.

#### cpu\_usage

Scaled CPU usage percentage for the thread.

#### base\_priority

The base scheduling priority for the thread.

#### cur\_priority

The current scheduling priority for the thread.

#### run\_state

The thread's run state. Possible values are:

### TH\_STATE\_RUNNING

The thread is running normally.

TH\_STATE\_STOPPED The thread is stopped.

TH\_STATE\_WAITING The thread is waiting normally.

TH\_STATE\_UNINTERRUPTIBLE The thread is in an un-interruptible wait state.

TH\_STATE\_HALTED The thread is halted at a clean point.

flags

Swap/idle flags for the thread. Possible values are:

TH\_FLAGS\_SWAPPED The thread is swapped out.

TH\_FLAGS\_IDLE The thread is an idle thread.

#### suspend\_count

The current suspend count for the thread.

sleep\_time

The number of seconds that the thread has been sleeping.

### **RELATED INFORMATION**

Functions: thread\_info.

Data Structures: thread\_sched\_info.

# thread\_sched\_info

**Structure** — Defines scheduling information for threads

#### SYNOPSIS

[1] [2]	struct <b>thread_sched_info</b> {	
[3]	int	policy;
[4]	int	data;
[5]	int	base_priority;
[6]	int	max_priority;
[7]	int	cur_priority;
[8]	boolean_t	depressed;
[9]	int	depress_priority;
[10]	};	
[11]	typedef struct thread_sched_info*	<pre>thread_sched_info_t;</pre>

### DESCRIPTION

The **thread\_sched\_info** structure defines the scheduling information array for threads. The **thread\_info** function returns this array for a specified thread.

### FIELDS

policy

Scheduling policy in effect

#### data

Associated data for the scheduling policy

#### base\_priority

Base scheduling priority

#### max\_priority

Maximum scheduling priority

#### cur\_priority

Current scheduling priority

#### depressed

True if scheduling priority is depressed

#### depress\_priority

Scheduling priority from which depressed

# **RELATED INFORMATION**

Functions: thread\_info.

Data Structures: thread\_basic\_info.

# time\_value

**Structure** — Defines format of system time values

### **SYNOPSIS**

- [1] struct **time\_value**
- [2] {
- [3] long
- [4] long

[5] };

[6] typedef struct time\_value

microseconds; time\_value\_t;

seconds;

### DESCRIPTION

The **time\_value** structure defines the format of the time structure supplied to or returned from the kernel.

### FIELDS

seconds

Seconds since system initialization

microseconds Microseconds in the current second

### **RELATED INFORMATION**

Functions: host\_adjust\_time, host\_get\_time, host\_set\_time.

# vm\_statistics

Structure — Defines statistics for the kernel's use of virtual memory

### SYNOPSIS

[1]	struct vm_statistics	
[2]	{	
[3]	long	pagesize;
[4]	long	free_count;
[5]	long	active_count;
[6]	long	inactive_count;
[7]	long	wire_count;
[8]	long	zero_fill_count;
[9]	long	reactivations;
[10]	long	pageins;
[11]	long	pageouts;
[12]	long	faults;
[13]	long	cow_faults;
[14]	long	lookups;
[15]	long	hits;
[16]	};	
[17]	typedef struct vm_statistics*	vm_statistics_t;

### DESCRIPTION

The **vm\_statistics** structure defines the statistics available on the kernel's use of virtual memory. The statistics record virtual memory usage since the kernel was booted.

You can also find *pagesize* by using the global variable *vm\_page\_size*. This variable is set at task initialization and remains constant for the life of the task.

For related information for a specific task, see the **task\_basic\_info** structure.

### FIELDS

pagesize

The virtual page size, in bytes.

#### free\_count

The total number of free pages in the system.

#### active\_count

The total number of pages currently in use and pageable.

inactive\_count

The number of inactive pages.

#### wire\_count

The number of pages that are wired in memory and cannot be paged out.

#### zero\_fill\_count

The number of zero-fill pages.

#### reactivations

The number of reactivated pages.

#### pageins

The number of requests for pages from a pager (such as the i-node pager).

#### pageouts

The number of pages that have been paged out.

#### faults

The number of times the **vm\_fault** routine has been called.

#### cow\_faults

The number of copy-on-write faults.

#### lookups

The number of object cache lookups.

#### hits

The number of object cache hits.

#### **RELATED INFORMATION**

Functions: task\_info, vm\_statistics.

Data Structures: task\_basic\_info.

Data Structures

# APPENDIX F Error Return Values

This appendix lists the various kernel return values.

# **Error Code Format**

An error code has the following format:

- system code (6 bits). The err\_get\_system (err) macro extracts this field.
- subsystem code (12 bits). The err\_get\_sub (err) macro extracts this field.
- error code (14 bits). The err\_get\_code (err) macro extracts this field.

The various system codes are:

- err\_kern —kernel
- *err\_us* user space library
- *err\_server* user space servers
- *err\_mach\_ipc* Mach-IPC errors
- *err\_local* user defined errors

A typical user error code definition would be:

#define SOMETHING\_WRONG err\_local | err\_sub (13) | 1

# **MIG Stub Errors**

### MIG\_ARRAY\_TOO\_LARGE

User specified array not large enough to hold returned array

### **MIG\_BAD\_ARGUMENTS**

Message receiver found an invalid message size, invalid header fields or invalid descriptors. This could only happen if an invalidly formatted message (i.e., one that did not pass through Mach IPC) were passed to a MIG de-multiplexing routine.

### MIG\_BAD\_ID

Bad message ID. This is only returned by MIG de-multiplexing routines when the message ID in the supplied message is not handled by that routine.

### MIG\_REPLY\_MISMATCH

The message ID in a reply message is not 100 more than the message ID of the request message.

#### MIG\_SERVER\_DIED

Message recipient no longer exists, or the recipient destroyed the request message without replying.

### **MIG\_TYPE\_ERROR**

The wrong number or size of data or rights was received.

# **Base IPC Status**

#### MACH\_MSG\_SUCCESS

Normal IPC success. This is the same value as KERN\_SUCCESS.

### MACH\_MSG\_IPC\_KERNEL

(mask bit) Kernel resource shortage handling an IPC capability.

### MACH\_MSG\_IPC\_SPACE

(mask bit) No room in IPC name space for another capability name.

### MACH\_MSG\_VM\_KERNEL

(mask bit) Kernel resource shortage handling out-of-line memory.

### MACH\_MSG\_VM\_SPACE

(mask bit) No room in VM address space for out-of-line memory.

### MACH\_MSG\_INSUFFICIENT\_PERMISSION

(mask bit) A permission check failure prevented the reception of a port right.

# **IPC Send Errors**

The following errors can occur when **mach\_msg** is used with the MACH\_SEND\_MSG option. This is also the case for all function interfaces.

### MACH\_SEND\_INTERRUPTED

Message send interrupted.

### MACH\_SEND\_INVALID\_DATA

Message buffer is unreadable.

### MACH\_SEND\_INVALID\_DEST

The destination port name in the message is MACH\_PORT\_NULL, MACH\_PORT\_DEAD, names a null or dead right, names a port set or is a right whose type (receive, send or send-once) does not match the type specified.

### MACH\_SEND\_INVALID\_HEADER

A field in the message header had a bad value.

### MACH\_SEND\_INVALID\_MEMORY

An out-of-line memory region does not exist in the address space or is not readable.

### MACH\_SEND\_INVALID\_NOTIFY

The notify port name (MACH\_SEND\_CANCEL) specified to **mach\_msg** is not a receive right.

### MACH\_SEND\_INVALID\_REPLY

The reply port name in the message is MACH\_PORT\_DEAD, names a null right, names a port set or is a right whose type (receive, send or send-once) does not match the type specified.

### MACH\_SEND\_INVALID\_RIGHT

A port name in the message body is MACH\_PORT\_DEAD, names a null right, names a port set or is a right whose type (receive, send or send-once) does not match the type specified.

#### MACH\_SEND\_INVALID\_TYPE

Invalid message type specification.

### MACH\_SEND\_MSG\_TOO\_SMALL

Message buffer doesn't contain a complete message.

### MACH\_SEND\_NO\_BUFFER

No kernel message buffer is available.

#### MACH\_SEND\_NO\_NOTIFY

Resource shortage; can't request message-accepted notification.

### MACH\_SEND\_NOTIFY\_IN\_PROGRESS

Message-accepted notification already pending.

### MACH\_SEND\_TIMED\_OUT

Message not sent before time-out expired.

### MACH\_SEND\_WILL\_NOTIFY

A message-accepted notification will be generated.

# **IPC Receive Errors**

The following errors can be returned by **mach\_msg** when used with the MACH\_RCV\_MSG option. They can occur for kernel function interfaces.

### MACH\_RCV\_BODY\_ERROR

Error receiving kernel message body. See special bits.

### MACH\_RCV\_HEADER\_ERROR

Error receiving message header. See special bits.

### MACH\_RCV\_IN\_SET

The receive port name specified to **mach\_msg** is a member of a port set.

### MACH\_RCV\_INTERRUPTED

A software interrupt occurred.

### MACH\_RCV\_INVALID\_DATA

The message buffer was not writable.

### MACH\_RCV\_INVALID\_NAME

The receive port name specified to **mach\_msg** is MACH\_PORT\_NULL, MACH\_PORT\_DEAD, names a null or dead right or is a right whose type (receive, send or send-once) does not match the type specified.

### MACH\_RCV\_INVALID\_NOTIFY

The notify port name (MACH\_RCV\_NOTIFY) specified to **mach\_msg** is not a receive right.

### MACH\_RCV\_PORT\_CHANGED

Receive right specified to **mach\_msg** was moved into a set during the receive.

### MACH\_RCV\_PORT\_DIED

Receive right (or set) specified to mach\_msg was sent away/died during receive.

### MACH\_RCV\_TIMED\_OUT

A message was not received within the time-out value.

### MACH\_RCV\_TOO\_LARGE

Message buffer is not large enough for the message.

## **Generic Kernel Errors**

### KERN\_SUCCESS

Successful completion

#### KERN\_INSUFFICIENT\_PERMISSION

The requesting task does not have sufficient permission to make the request.

#### KERN\_INVALID\_ARGUMENT

The function requested was not applicable to this type of object.

### KERN\_INVALID\_CAPABILITY

The supplied right is dead, null or not of the proper type.

#### KERN\_INVALID\_VALUE

A parameter's value was out of range (or possibly ill-formed). Specific error return values are returned if the parameter's value is properly formed and in range, but not a usable value at this time.

#### KERN\_RESOURCE\_SHORTAGE

A system resource could not be allocated to fulfill this request. This failure may not be permanent.

### **Port Manipulation Errors**

### KERN\_INVALID\_NAME

The port name doesn't denote a right in the task.

### KERN\_INVALID\_RIGHT

The port name denotes a right, but not an appropriate right.

### KERN\_NAME\_EXISTS

The port name already denotes a right in the task.

#### KERN\_NO\_SPACE

The task's port name space is full.

#### KERN\_NOT\_IN\_SET

The receive right is not a member of a port set.

#### KERN\_RIGHT\_EXISTS

The task already has send or receive rights for the port under another name.

#### KERN\_UREFS\_OVERFLOW

Operation would overflow limit on user-references.

# **Virtual Memory Manipulation Errors**

#### KERN\_INVALID\_ADDRESS

Specified virtual address is not currently valid.

#### KERN\_MEMORY\_ERROR

During a page fault, the memory object indicated that the data could not be returned. This failure may be temporary; future attempts to access this same data may succeed, as defined by the memory object.

#### KERN\_MEMORY\_FAILURE

During a page fault, the target address refers to a memory object that has been destroyed. This failure is permanent.

#### KERN\_NO\_SPACE

The task's address space is full (not sufficient free space) or the specified address range is already in use.

### **KERN\_PROTECTION\_FAILURE**

Specified memory is valid, but does not permit the required forms of access or the protection being requested exceeds that permitted.

### **Random Kernel Errors**

#### EML\_BAD\_CNT

Invalid syscall number specified for an emulation vector entry

#### EML\_BAD\_TASK

Target of a syscall emulation vector manipulation call is not a task

### **KERN\_ABORTED**

The operation was aborted.

### **KERN\_FAILURE**

A catch-all error for implementation dependent failures.

#### KERN\_INVALID\_HOST

An argument supplied to assert system privilege was not a host control port.

### KERN\_INVALID\_TASK

Target task isn't an active task.

# **Kernel Device Errors**

### **D\_SUCCESS**

Normal device return. This is the same value as KERN\_SUCCESS.

### **D\_ALREADY\_OPEN**

Exclusive-use device already open

#### **D\_DEVICE\_DOWN**

Device has been shut down

### **D\_INVALID\_OPERATION**

No filter port was specified.

### **D\_INVALID\_RECNUM**

Invalid record (block) number

### **D\_INVALID\_SIZE**

Invalid IO size

### **D\_IO\_ERROR**

Hardware IO error

### **D\_NO\_SUCH\_DEVICE**

No device with that name, or the device is not operational.

# D\_OUT\_OF\_BAND

Out-of-band condition occurred on device (such as typing control-C)

### **D\_READ\_ONLY**

Data cannot be written to this device.

# D\_WOULD\_BLOCK

Operation would block, but D\_NOWAIT set

**Error Return Values** 

# APPENDIX G Permission Definitions

This appendix lists the various permission definitions and their associated values. These permission values are passed from the kernel to the security server to identify which permission is being checked for the given pair. The permission value is also displayed in audit logs, and kernel debugging messages.

# **Device Port Permissions**

### DSV\_CLOSE\_DEVICE

0x01000011

### DSV\_GET\_DEVICE\_STATUS

0x01000012

DSV\_MAP\_DEVICE

0x01000013

### DSV\_OPEN\_DEVICE

0x01000014

### **DSV\_READ\_DEVICE**

DSV\_SET\_DEVICE\_FILTER 0x01000016

DSV\_SET\_DEVICE\_STATUS 0x01000017

DSV\_WRITE\_DEVICE 0x01000018

DSV\_PAGER\_CTRL 0x01000019

# **Host Priviledge Port Permissions**

HPSV\_GET\_BOOT\_INFO 0x02000011

HPSV\_GET\_HOST\_PROCESSORS 0x02000012

HPSV\_PSET\_CTL\_PORT 0x02000013

HPSV\_REBOOT\_HOST 0x02000014

### HPSV\_SET\_DEFAULT\_MEMORY\_MGR

0x02000015

HPSV\_SET\_TIME 0x02000016

### HPSV\_WIRE\_THREAD 0x02000017

### HPSV\_WIRE\_VM

0x02000018

# **Host Port Permissions**

### HSV\_CREATE\_PSET

0x03000011

### HSV\_FLUSH\_PERMISSION

0x03000012

# HSV\_GET\_DEFAULT\_PSET\_NAME

0x03000013

### HSV\_GET\_HOST\_INFO

0x03000014

### HSV\_GET\_HOST\_NAME

0x03000015

### HSV\_GET\_HOST\_VERSION

0x03000016

### HSV\_GET\_TIME

0x03000017

### HSV\_PSET\_NAMES

0x03000018

### HSV\_GET\_AUDIT\_PORT

### HSV\_GET\_SECURITY\_CLIENT\_PORT

0x0300001A

### HSV\_GET\_SECURITY\_MASTER\_PORT

0x0300001B

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0x0300001C

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0x0B000011

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0x04000011

### MOSV\_HAVE\_READ

0x04000012

### MOSV\_HAVE\_WRITE

0x04000013

### MOSV\_PAGE\_VM\_REGION

0x04000015

# **Memory Control Port Permissions**

### MCSV\_CHANGE\_PAGE\_LOCKS

# MCSV\_DESTROY\_OBJECT

0x05000012

# MCSV\_GET\_ATTRIBUTE

0x05000013

# MCSV\_INVOKE\_LOCK\_REQUEST

0x05000014

### MCSV\_MAKE\_PAGE\_PRECIOUS

0x05000015

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0x05000016

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0x06000013

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0x06000014

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0x07000011

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0x07000012

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0x07000013

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0x07000014

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0x07000016

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0x07000017

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0x07000018

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0x07000019

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0x08000011

### TSV\_ADD\_NAME

0x08000012

### TSV\_ADD\_THREAD

0x08000013

# TSV\_ADD\_THREAD\_SERVICE

0x08000014

# TSV\_ALLOCATE\_VM\_REGION

0x08000015

### TSV\_ALTER\_PNS\_INFO

### TSV\_ASSIGN\_TASK\_TO\_PSET

0x08000017

# TSV\_CHG\_VM\_REGION\_PROT

0x08000018

### TSV\_CHG\_TASK\_PRIORITY

0x08000019

### TSV\_COPY\_VM

0x0800001a

### TSV\_CREATE\_TASK

0x0800001b

### TSV\_CREATE\_TASK\_SECURE

0x0800001c

### TSV\_DEALLOCATE\_VM\_REGION

0x0800001d

# TSV\_EXTRACT\_RIGHT

0x0800001e

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0x0800001f

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0x0800002c

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0x0800002d

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0x0800002f

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0x08000030

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0x08000032

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0x0800003a

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0x0800003c

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0x0800003d

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0x0800003e

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0x09000011

# THSV\_ABORT\_THREAD\_DEPRESS

0x09000012

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### THSV\_CAN\_SWTCH

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0x0900001c

### THSV\_RAISE\_EXCEPTION

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### THSV\_RESUME\_THREAD

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0x09000027

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0x09000029

#### THSV\_WIRE\_THREAD\_INTO\_MEMORY

0x0900002a

# **IPC Permissions**

## AV\_CAN\_RECEIVE

0x0a000001

#### AV\_RECEIVE

0x0a000001

# AV\_CAN\_SEND

0x0a000002

# AV\_SEND

0x0a000002

#### AV\_HOLD\_RECEIVE

0x0a000003

#### AV\_HOLD\_SEND

0x0a000004

# AV\_HOLD\_SEND\_ONCE

0x0a000005

### **AV\_INTERPOSE**

0x0a000006

## AV\_SPECIFY

0x0a000007

# AV\_TRANSFER\_OOL

0x0a000008

#### AV\_TRANSFER\_RECEIVE

0x0a000009

#### AV\_TRANSFER\_SEND

0x0a00000a

# AV\_TRANSFER\_SEND\_ONCE

0x0a00000b

#### AV\_TRANSFER\_RIGHTS

0x0a00000c

# MOSV\_MAP\_VM\_REGION

0x0a00000d

# AV\_SET\_REPLY

0x0a00000e

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